

# Ultrafast Rectifier, 2 A FRED Pt®



### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 A			
V <sub>R</sub>	100 V			
V <sub>F</sub> at I <sub>F</sub>	0.66 V			
t <sub>rr</sub> typ.	24 ns			
T <sub>J</sub> max.	175 °C			
Package	SMB (DO-214AA)			
Circuit configuration	Single			

#### **FEATURES**

• Ultrafast recovery time, reduced Q<sub>rr</sub> and soft recovery



FREE

• 175 °C maximum operating junction temperature

• Specific for output and snubber operation

Low forward voltage drop

• Low leakage current

- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **DESCRIPTION / APPLICATIONS**

State of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop, ultrafast recovery time, and fast recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in snubber, output operation, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element.

### **MECHANICAL DATA**

Case: SMB (DO-214AA)

Molding compound meets UL 94 V-0 flammability rating

Halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per

J-STD-002

Polarity: color band denotes the cathode end

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		100	V
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>L</sub> = 150 °C <sup>(1)</sup>	2	۸
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C, 6 ms square pulse	70	A
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C

#### Note

(1) Mounted on PCB with 6 mm x 3.5 mm lands

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_{R}$	I <sub>R</sub> = 100 μA	100	-	-	
Forward voltage	V	I <sub>F</sub> = 2 A	-	0.84	0.9	V
	$V_{F}$	I <sub>F</sub> = 2 A, T <sub>J</sub> = 150 °C	-	0.66	0.7	
Deviana laskaga avimust		V <sub>R</sub> = V <sub>R</sub> rated	-	-	2	
Reverse leakage current I <sub>R</sub>		T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	-	20	μΑ
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 100 V	-	13	-	рF
Critical rate if rise of reverse voltage	$dV/dt_{\tau}$		-	-	10 000	V/µs



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	24	-	
		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	27	-	
Reverse recovery time $t_{rr}$	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr} = 0.25 \text{ A}$		-	-	23	ns	
	T <sub>J</sub> = 25 °C		-	21	-		
	T <sub>J</sub> = 125 °C		-	26	-		
Dools recovery overest	Deal area and a	T <sub>J</sub> = 25 °C	$I_F = 2 A$	-	2.7	-	^
Peak recovery current I <sub>RRM</sub>	T <sub>J</sub> = 125 °C	$dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 100 \text{ V}$	-	3.4	-	A	
Reverse recovery charge Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	28	-	nC	
	T <sub>J</sub> = 125 °C	1	-	43	-		

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C
Thermal resistance, junction to mount	R <sub>thJM</sub> <sup>(1)</sup>		-	-	17	°C/W
Thermal resistance, junction to ambient	R <sub>thJA</sub> (1)		-	-	80	C/VV
Approximate Weight			0.1		g	
Approximate weight				0.003		oz.
Marking device		Case style SMB (DO-214AA)		21	<del>1</del> 1	

#### Note

<sup>(1)</sup> Units mounted on PCB 6 mm x 3.5 mm land areas

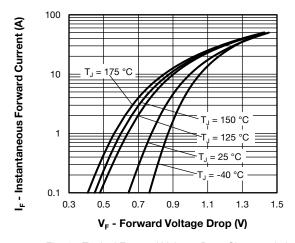


Fig. 1 - Typical Forward Voltage Drop Characteristics

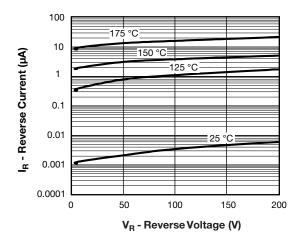


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltagejjjl



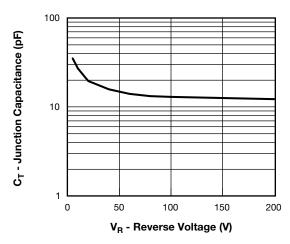


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

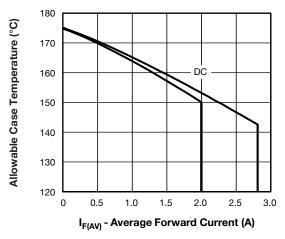


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

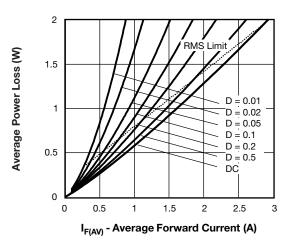


Fig. 5 - Forward Power Loss Characteristics

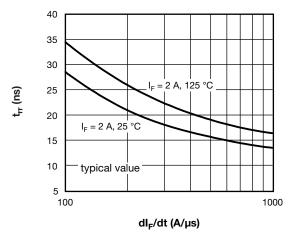


Fig. 6 - Typical Reverse Recovery vs. dl<sub>F</sub>/dt

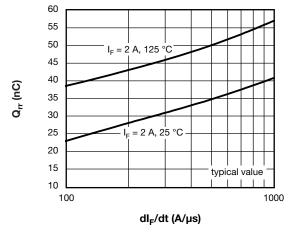
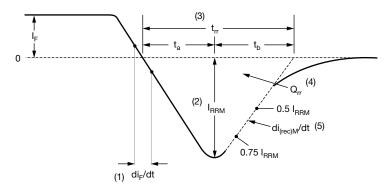


Fig. 7 - Typical Stored Charge vs. dl<sub>F</sub>/dt



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# Vishay Semiconductors



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
  - ough zero crossing and I<sub>RRM</sub>
- (2) I<sub>RRM</sub> peak reverse recovery current
- $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$

(4)  $\mathbf{Q}_{\mathrm{rr}}$  - area under curve defined by  $\mathbf{t}_{\mathrm{rr}}$ 

- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_F$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (5)  $di_{(rec)M}/dt$  peak rate of change of current during  $t_b$  portion of  $t_{rr}$

Fig. 8 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION (Example)				
PREFERRED P/N	PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION	
VS-2EGH01-M3/5BT	5BT	3200	13"diameter plastic tape and reel	

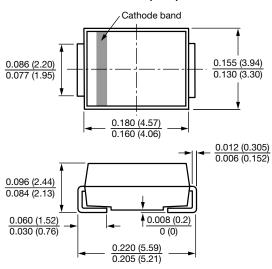
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95401</u>				
Part marking information	www.vishay.com/doc?95472			
Packaging information	www.vishay.com/doc?95404			
SPICE model	www.vishay.com/doc?96021			



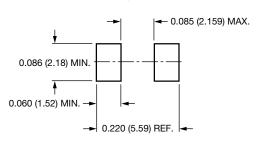
## **SMB**

## **DIMENSIONS** in inches (millimeters)

### DO-214AA (SMB)



### **Mounting Pad Layout**





## **Legal Disclaimer Notice**

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