

Ultrafast Rectifier, 2 A FRED Pt®



SMB (DO-214AA)

LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS						
I _{F(AV)} 2 A						
V _R	200 V					
V _F at I _F	0.66 V					
t _{rr} typ.	24 ns					
T _J max.	175 °C					
Package	SMB (DO-214AA)					
Circuit configuration	Single					

FEATURES

- Ultrafast recovery time, reduced Q_{rr} and soft recovery
- 175 °C maximum operating junction temperature
- Specific for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop, ultrafast recovery time, and fast recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in snubber, output operation, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element.

MECHANICAL DATA

Case: SMB (DO-214AA)

Molding compound meets UL 94 V-0 flammability rating Halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per J-STD-002

Polarity: color band denotes the cathode end

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Peak repetitive reverse voltage	V _{RRM}		200	V		
Average rectified forward current	I _{F(AV)}	$T_L = 150 \ ^{\circ}C \ ^{(1)}$	2	А		
Non-repetitive peak surge current	I _{FSM}	$T_J = 25 \ ^{\circ}C$, 6 ms square pulse	70	~		
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C		

Note

⁽¹⁾ Mounted on PCB with 6 mm x 3.5 mm lands

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	200	-	-		
Ferryard valtage	N/	I _F = 2 A	-	0.84	0.9	V	
Forward voltage	V _F	I _F = 2 A, T _J = 150 °C	-	0.66	0.7		
Poweree lookage ourrent		$V_{R} = V_{R}$ rated	-	-	2		
Reverse leakage current	IR	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	20	μA	
Junction capacitance	CT	V _R = 200 V	-	12	-	pF	
Critical rate if rise of reverse voltage	dV/dt_{τ}		-	-	10 000	V/µs	

Revision: 17-Mar-2021

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Document Number: 96444

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RoHS COMPLIANT HALOGEN

FREE



DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25 \text{ °C}$ unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS		
			00 A/µs, V _R = 30 V	-	24	-		
		I _F = 1.0 A, dI _F /dt = 50 A/µs, V _R = 30 V		-	27	-		
Reverse recovery time t _{rr}	t _{rr}	I _F = 0.5 A, I _R = 1 A, I _{rr} = 0.25 A		-	-	23	ns	
	T _J = 25 °C		-	21	-			
		T _J = 125 °C		-	26	-		
Peak recovery current		T _J = 25 °C	$I_{\rm F} = 2 {\rm A}$	-	2.7	-	Α	
Peak recovery current I _{RRM}	T _J = 125 °C	dl _F /dt = 200 A/µs V _B = 100 V	-	3.4	-	A		
Deverse we example the week	0	T _J = 25 °C		-	28	-	nC	
neverse recovery charge	everse recovery charge Q _{rr}			-	43	-	nu	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C
Thermal resistance, junction to mount	R _{thJM} ⁽¹⁾		-	-	17	°C/W
Thermal resistance, junction to ambient	R _{thJA} ⁽¹⁾		-	-	80	0/10
Approximate Weight				0.1		g
Approximate Weight				0.003		oz.
Marking device		Case style SMB (DO-214AA)	2H2			

Note

⁽¹⁾ Units mounted on PCB 6 mm x 3.5 mm land areas

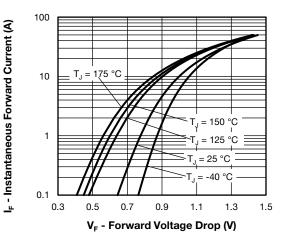


Fig. 1 - Typical Forward Voltage Drop Characteristics

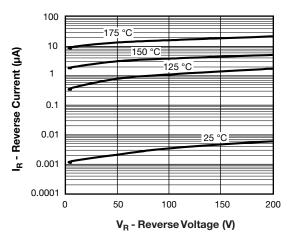
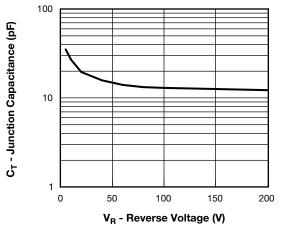


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

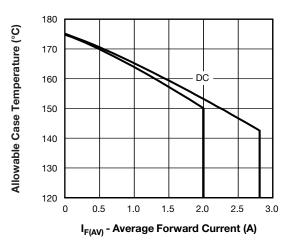


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

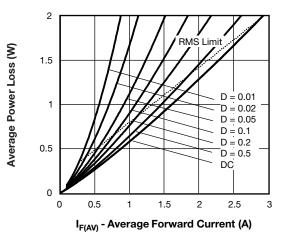
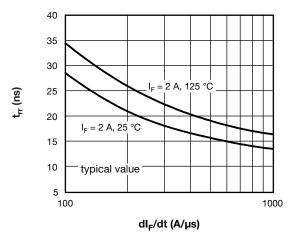
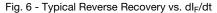


Fig. 5 - Forward Power Loss Characteristics





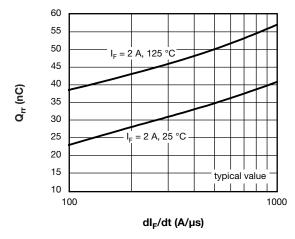
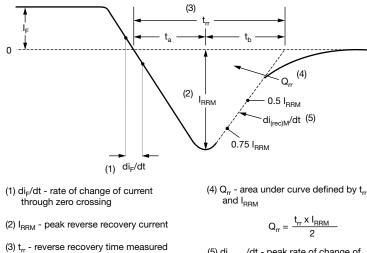


Fig. 7 - Typical Stored Charge vs. dl_F/dt

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(3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current. (5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 8 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

SHAY

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Device code	VS-	2	Е	G	н	02	-M3
	(1)	(2)	(3)	(4)	(5)	6	(7)
	1	- Visl	nay Sen	niconduo	ctors pro	oduct	
	2	- Cur	rent rati	ng (2 = 2	2 A)		
	3	- Circ	cuit conf	iguratior	ו:		
		E =	single c	liode			
	4	- G =	SMB pa	ackage			
	5	- Pro	cess typ	e,			
	_	H = hyperfast recovery					
	6		•	le (02 =	,		
	7	M3	= halog	gen-free	, RoHS-	complia	ant, and

ORDERING INFORMATION (Example)							
PREFERRED P/N	EFERRED P/N PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-2EGH02-M3/5BT	5BT	3200	13"diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95401					
Part marking information	www.vishay.com/doc?95472				
Packaging information	www.vishay.com/doc?95404				
SPICE model	www.vishay.com/doc?96021				

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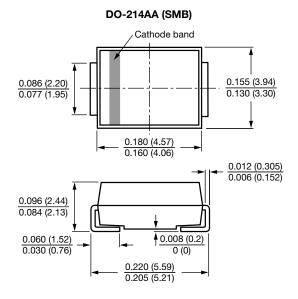


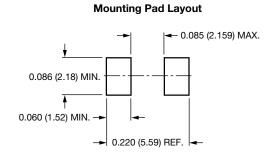
Outline Dimensions

Vishay Semiconductors

SMB

DIMENSIONS in inches (millimeters)







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Revision: 01-Jan-2024