VS-VSKDF600/06PbF

Vishay Semiconductors

FRED Pt[®] Gen 4 Doubler Ultrafast Diode, 600 A (INT-A-PAK Power Modules)



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FEATURES

- Gen 4 FRED Pt[®] dices technology
- Ultrasoft reverse recovery characteristics
- Low I_{RRM} and reverse recovery charge
- Very low forward voltage drop
- 175 °C operating junction temperature
- UL approved file E78996 for application with maximum case temperature up to 140 °C
- Large creepage distances
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

Gen 4 FRED Pt technology, state of the art, ultra low V_F , soft switching optimized for IGBT F/W diode.

The minimized conduction loss, optimized storage charge, and low recovery current, minimized the switching losses and reduce the over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Cathode to anode voltage	V _R		600	V		
Continuous forward current	1	T _C = 25 °C	771			
Continuous forward current	I _F	T _C = 63 °C	640	А		
Single pulse forward current	I _{FSM}	$t_p = 10$ ms, 50 Hz, sine half wave, initial T _J = 175 °C	4140			
Maximum a success dis sis stices	PD	T _C = 25 °C	1923	w		
Maximum power dissipation	гD	T _C = 90 °C	1090	vv		
Operating junction temperature range	TJ		-40 to +175	°C		
Storage temperature range	T _{Stg}		-40 to +150	U U		
RMS insulation voltage	V _{INS}	50 Hz, circuit to base, all terminals shorted, t = 1 s	3500	V		

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	TYP.	MAX.	UNITS	
Cathode to anode breakdown voltage	V _{BR}	I _R = 500 μA	600	-	-	
Forward voltage drop	V _{FM}	I _F = 300 A	-	1.305	-	V
		I _F = 600 A	-	1.60	1.77	
		I _F = 300 A, T _J = 150 °C	-	1.08	-	
		I _F = 600 A, T _J = 150 °C	-	1.47	-	
Reverse leakage current	I _{RM}	V _R = 600 V	-	13	-	μA
		T _J = 150 °C, V _R = 600 V	-	3.2	-	mA

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PRIMARY CHARACTERISTICS					
V _R	600 V				
I _{F(AV)} at T _C	600 A at 25 °C				
t _{rr} at 25 °C	150 ns				
Туре	Modules - diode, FRED Pt®				
Package	INT-A-PAK				
Circuit configuration	Diode doubler circuit				



COMPLIANT

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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
Reverse recovery time t _{rr}	+	$T_J = 25 \ ^{\circ}C$		-	150	-	ns
	۲r	T _J = 125 °C	l _F = 150 A dl/dt = 200 A/µs	-	310	-	
Peak recovery current Irr		T _J = 25 °C		-	14	-	Α μC
	Irr	T _J = 125 °C	$V_{\rm B} = 400 \text{V}$	-	33	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	1.65	-	
		T _J = 125 °C		-	7.03	-	

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum thermal resistance, junction to case per leg	R _{thJC}	DC operation	0.088		
Typical thermal resistance, case to heat sink	R _{thCS}	Mounting surface, flat, smooth and greased	0.035	K/W	
Mounting to heat sink		A mounting compound is recommended and the			
torque ± 10 % busbar		torque should be rechecked after a period of 3 hours to allow the spread of the compound.	4 to 6	Nm	
Approximate weight			200	g	
Approximate weight			7.1	OZ.	
Case style			INT-A-PAK		



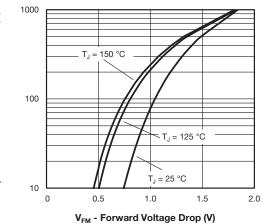


Fig. 1 - Typical Forward Voltage Drop Characteristics

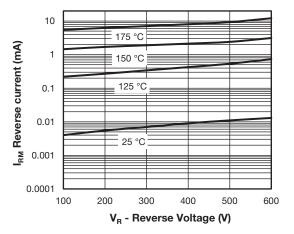


Fig. 2 - Typical Value of Reverse Current vs. Reverse Voltage



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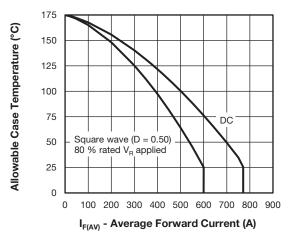


Fig. 3 - Maximum Allowable Case Temperature vs. Average Forward Current

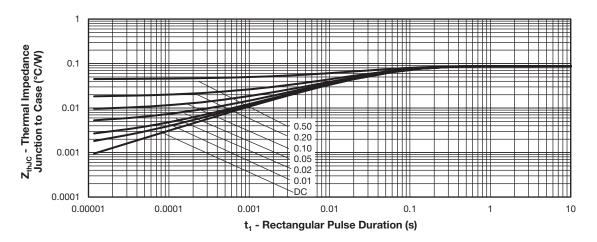


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

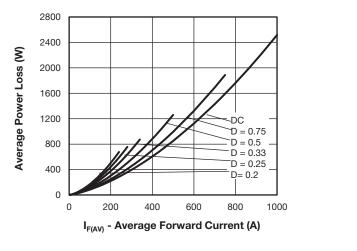


Fig. 5 - Forward Power Loss Characteristics

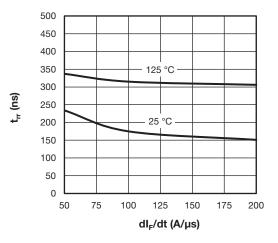


Fig. 6 - Typical Reverse Recovery Time vs. dI_F/dt I_{FM} = 150 A, V_R = 400 V

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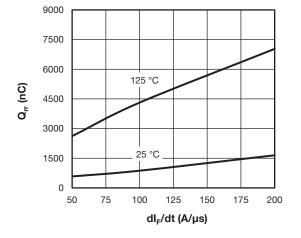


Fig. 7 - Typical Reverse Recovery Charge vs. dl_F/dt I_{FM} = 150 A, V_R = 400 V

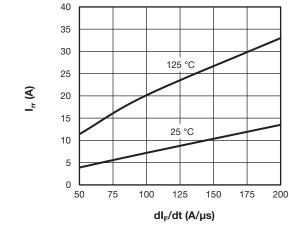


Fig. 8 - Typical Reverse Recovery Current vs. dl_F/dt I_{FM} = 150 A, V_R = 400 V

ORDERING INFORMATION TABLE

Device code	VS-VS	KD	F	600	06	PbF	
	1	2	3	4	5	6	
	1 - 2 - 3 - 4 - 5 - 6 -	Circu F = F Curre Volta	uit config RED Pt ent ratin age ratin	conduct juration: [®] ultrafa g (600 = g (06 = Pb)-free	KD = d ast diode 600 A) 600 V)	oubler c e	ircuit

CIRCUIT CONFIGURATION						
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING				
Diode doubler circuit	KD	KD reversed polarity				

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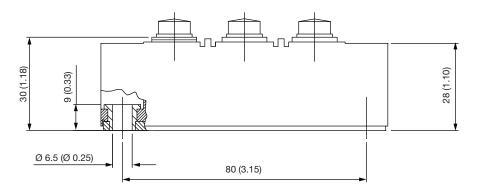
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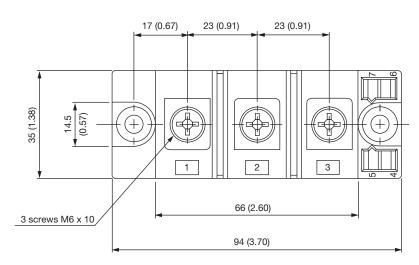


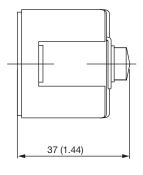
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DIMENSIONS in millimeters (inches)







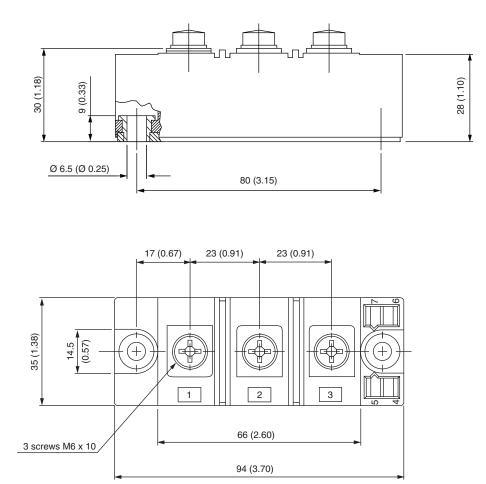


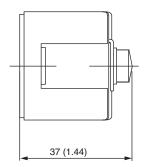
Outline Dimensions

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INT-A-PAK DBC

DIMENSIONS in millimeters (inches)







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