

## EMI Suppression Capacitor, Ceramic Disc, Class X1, 275 V<sub>AC</sub>


**RoHS**  
COMPLIANT

### FEATURES

- Complying with IEC 60384-14
- High reliability
- Wide range of different leadstyles
- Singlelayer AC disc safety capacitors
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)

### APPLICATIONS

- X1 according to IEC 60384-14
- Line-to-line filtering (Class X)
- EMI / RFI suppression and filtering

### DESIGN

The capacitors consist of ceramic disc both sides of which are silver plated. Connection leads are made of tinned copper having diameters of 0.6 mm.

The capacitors may be supplied with straight or kinked leads having a lead spacing of 7.5 mm.

Coating is made of blue colored flame retardant epoxy resin in accordance with UL 94 V-0.

### QUICK REFERENCE DATA

DESCRIPTION	VALUE
Ceramic Class	2
Ceramic Dielectric	Y5V
Voltage (V <sub>AC</sub> )	275
Min. Capacitance (pF)	4700
Max. Capacitance (pF)	22 000
Mounting	Radial

### OPERATING TEMPERATURE RANGE

-40 °C to +125 °C <sup>(1)</sup>

#### Note

<sup>(1)</sup> For explanation about the difference of operating temperature range and temperature characteristic of capacitance please see [www.vishay.com/doc?48299](http://www.vishay.com/doc?48299)

### TEMPERATURE CHARACTERISTICS

Class 2: Y5V

### SECTIONAL SPECIFICATIONS

Climatic category (according to EN 60058-1)

Class 2: 40 / 125 / 21

### APPROVALS

IEC 60384-14

DIN EN 60384-14

### CAPACITANCE RANGE

4.7 nF to 22 nF

### TOLERANCE ON CAPACITANCE

± 20 %

### RATED VOLTAGE

X1: 275 V<sub>AC</sub>, 50 Hz (IEC 60384-14)

### TEST VOLTAGE

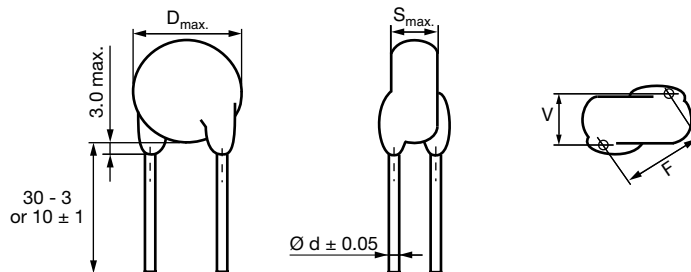
- 4000 V<sub>DC</sub>, 2 s      Component test (100 %)
- 3500 V<sub>DC</sub>, 60 s      Random sampling test (destructive)
- 2000 V<sub>AC</sub>, 50 Hz, 60 s      Voltage proof of coating (destructive)

### INSULATION RESISTANCE AT 500 V<sub>DC</sub>

≥ 6000 MΩ (60 s)

### DISSIPATION FACTOR

Class 2: max. 2.5 % (1 kHz)

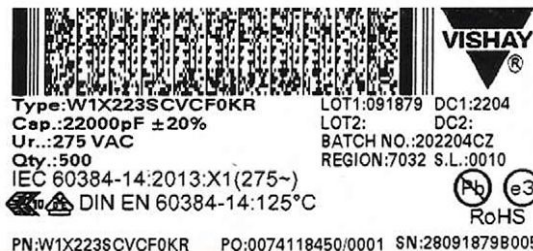
**DIMENSIONS** in millimeters

**TECHNICAL DATA**

CAPACITANCE C (pF)	CAPACITANCE TOLERANCE	BODY DIAMETER D <sub>MAX.</sub> (mm)	BODY THICKNESS S <sub>MAX.</sub> (mm)	LEAD SPACING <sup>(1)</sup> F (mm) ± 1 mm	LEAD DIAMETER <sup>(1)</sup> d (mm) ± 0.05 mm	WIDTH <sup>(1)</sup> V (mm) ± 0.5 mm	PART NUMBER MISSING DIGITS SEE ORDERING CODE BELOW
<b>Y5V</b>							
4700	± 20 %	11.0	3.0	7.5	0.6	1.4	W1X472#CV###KR
6800		11.0					W1X682#CV###KR
10 000		15.0					W1X103#CV###KR
15 000		17.0				1.6	W1X153#CV###KR
22 000		20.0					W1X223#CV###KR

**Note**
<sup>(1)</sup> Standard lead configuration, other lead spacing and diameter available on request

**ORDERING CODE**

#	7 <sup>th</sup> digit	Capacitance tolerance	± 10 % = K, ± 20 % = M				
###	10 <sup>th</sup> to 12 <sup>th</sup> digit	Lead configuration	See "General Information" <a href="http://www.vishay.com/doc?22001">www.vishay.com/doc?22001</a>				
<b>Example</b>	<b>W1X</b>	<b>223</b>	<b>M</b>	<b>CV</b>	<b>CRU</b>	<b>K</b>	<b>R</b>
	Series	Capacitance value	Tolerance code	Voltage code	Lead configuration	Internal code	RoHS compliant

**MARKING**


## APPROVALS

IEC 60384-14 - Safety tests  
This approval together with CB test certificate substitutes all national approvals.

### CB Certificate ([www.vishay.com/doc?22223](http://www.vishay.com/doc?22223))

X1-capacitor: CB test certificate: DE 1-11148-A1 4.7 nF to 22 nF 275 V<sub>AC</sub>  
Minimum thickness of insulation: 0.4 mm

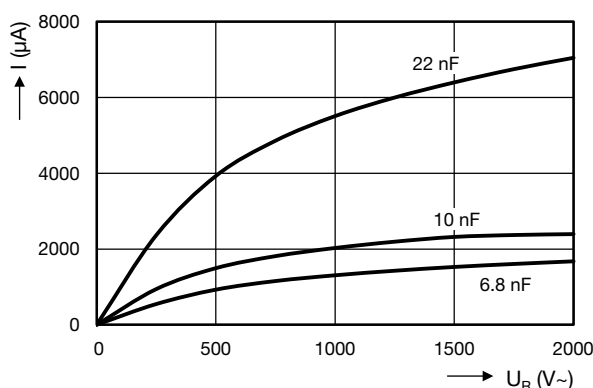


### VDE ([www.vishay.com/doc?22224](http://www.vishay.com/doc?22224))

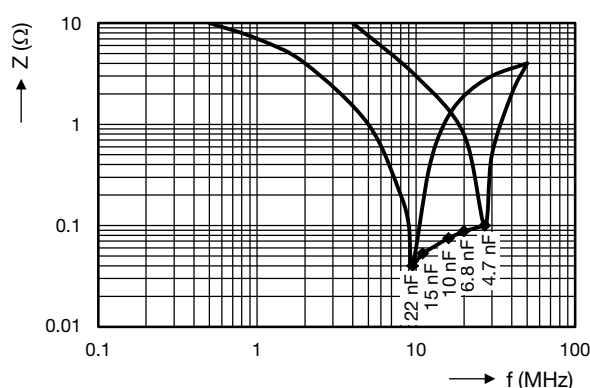
X1-capacitor: VDE marks approval: 137890 4.7 nF to 22 nF 275 V<sub>AC</sub>  
DIN EN 60384-14 (VDE 0565-1-1)  
Minimum thickness of insulation: 0.4 mm



## AC CURRENT VS. VOLTAGE (typical)



## IMPEDANCE VS. FREQUENCY (typical)



## STORAGE

The capacitors must not be stored in a corrosive atmosphere, where sulphide or chloride gas, acid, alkali or salt are present. Exposure of the components to moisture, should be avoided. The solderability of the leads is not affected by storage of up to 24 months (temperature +10 °C to +35 °C, relative humidity up to 60 %). Class 2 ceramic dielectric capacitors are also subject to aging, see [www.vishay.com/doc?22001](http://www.vishay.com/doc?22001).

## SOLDERING

### SOLDERING SPECIFICATIONS

Soldering test for capacitors with wire leads: (according to IEC 60068-2-20, solder bath method)

	SOLDERABILITY	RESISTANCE TO SOLDERING HEAT
Soldering temperature	235 °C ± 5 °C	260 °C ± 5 °C
Soldering duration	2 s ± 0.5 s	10 s ± 1 s
Distance from component body	≥ 2 mm	≥ 5 mm

## SOLDERING RECOMMENDATIONS

Soldering of the component should be achieved using a Sn60/40 type or a silver-bearing Sn62/36/2Ag type solder. Ceramic capacitors are very sensitive to rapid changes in temperature (thermal shock) therefore the solder heat resistance specification (see Soldering Specifications table) should not be exceeded. Subjecting the capacitor to excessive heating may result in thermal shocks that can crack the ceramic body. Similarly, excessive heating can cause the internal solder junction to melt.

## CLEANING

The components should be cleaned immediately following the soldering operation with vapor degreasers.



## SOLVENT RESISTANCE

The coating and marking of the capacitors are resistant to the following test method: IEC 60068-2-45 (method XA).

## MOUNTING

If a defined product stop is required for mounting on a PCB, a mechanically formed product stop (kinked or inline wire) or a mounting tool should be used.

We do not recommend modifying the lead terminals, e.g. bending or cropping. This action could break the coating or crack the ceramic insert. If however, the lead must be modified in any way, we recommend support of the lead with a clamping fixture next to the coating.

## OPERATING VOLTAGE

In case the voltage is applied to the circuit, starting as well as stopping, may generate irregular voltage for a transit period because of resonance or switching. Be sure to use a capacitor with a rated voltage range that includes these irregular voltages.

## OPERATING TEMPERATURE AND SELF-GENERATED HEAT

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high frequency, pulse, or similar application, it may have self-generated heat due to dielectric dissipation.

Temperature increase due to self-generated heating should not exceed 20 °C while operating at an atmosphere temperature of 25 °C.

When measuring, the surface temperature, make sure that the capacitor is not affected by radiant, conductive and convective heat by its surroundings. Excessive heat may lead to thermo-mechanical deterioration of the capacitor's characteristics and reliability.

RELATED DOCUMENTS	
General Information	<a href="http://www.vishay.com/doc?22001">www.vishay.com/doc?22001</a>
CB Test Certificate	<a href="http://www.vishay.com/doc?22223">www.vishay.com/doc?22223</a>
VDE Marks Approval	<a href="http://www.vishay.com/doc?22224">www.vishay.com/doc?22224</a>



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