

Surface Mount Multilayer Chip Capacitors for Commodity Solutions

Below tables are test procedures and requirements unless specified in detail datasheet.

VJ....W1BC TEST PROCEDURES AND REQUIREMENTS				
TEST	PROCEDURE	REQUIREMENTS		
1) Visual and mechanical		<ul style="list-style-type: none"> • No remarkable defect • Dimensions should confirm to individual specification sheet 		
2) Capacitance		<ul style="list-style-type: none"> • Shall not exceed the limits given in the detailed specification 		
		COG (NPO): Cap. \geq 30 pF; Q \geq 1000 Cap. < 30 pF; Q \geq 400 + 20 C X5R, X7R:		
		RATED VOLTAGE	DF \leq	EXCEPTION OF DF \leq
3) Q/DF (dissipation factor)	Class 1: C0G (NP0) Cap. \leq 1000 pF; 1.0 V _{RMS} \pm 0.2 V _{RMS} ; 1 MHz \pm 10 % Cap. > 1000 pF; 1.0 V _{RMS} \pm 0.2 V _{RMS} ; 1 kHz \pm 10 % Class 2: X7R, X5R, Y5V Cap. \leq 10 μ F; 1.0 V _{RMS} \pm 0.2 V _{RMS} ; 1 kHz \pm 10 % ⁽¹⁾ Cap. > 10 μ F; 0.5 V _{RMS} \pm 0.2 V _{RMS} ; 120 Hz \pm 20 %	\geq 50 V	2.5 %	3 % 0201 (50 V); 0603 \geq 0.047 μ F; 0805 \geq 0.18 μ F; 1206 \geq 0.47 μ F
				5 % 1210 \geq 4.7 μ F
				10 % 0603 \geq 1 μ F; 0805 \geq 1 μ F; 1206 \geq 2.2 μ F; 1210 \geq 10 μ F
		35 V	3.5 %	10 % 0805 \geq 2.2 μ F; 1210 \geq 10 μ F
				5 % 0201 \geq 0.01 μ F; 0805 \geq 1 μ F; 1210 \geq 10 μ F
				7 % 0603 \geq 0.33 μ F; 1206 \geq 4.7 μ F
		25 V	3.5 %	10 % 0402 \geq 0.10 μ F; 0603 \geq 0.47 μ F; 0805 \geq 2.2 μ F; 1206 \geq 6.8 μ F; 1210 \geq 22 μ F
				5 % 0201 \geq 0.01 μ F; 0402 \geq 0.033 μ F; 0603 \geq 0.15 μ F; 0805 \geq 0.68 μ F; 1206 \geq 2.2 μ F; 1210 \geq 4.7 μ F
				10 % 0402 \geq 0.22 μ F; 0603 \geq 0.68 μ F; 0805 \geq 2.2 μ F; 1206 \geq 4.7 μ F; 1210 \geq 22 μ F
		16 V	3.5 %	10 % 0201 \geq 0.012 μ F; 0402 \geq 0.33 μ F; 0603 \geq 0.33 μ F; 0805 \geq 2.2 μ F; 1206 \geq 2.2 μ F; 1210 \geq 22 μ F
				15 % 0201 \geq 0.1 μ F; 0402 \geq 1 μ F
		10 V	5 %	15 % 0201 \geq 0.1 μ F; 0402 \geq 1 μ F;
				10 % 0201 \geq 0.01 μ F; 0402 \geq 0.033 μ F; 0603 \geq 0.15 μ F; 0805 \geq 0.68 μ F; 1206 \geq 2.2 μ F; 1210 \geq 4.7 μ F
		6.3 V	10 %	15 % 0201 \geq 0.1 μ F; 0402 \geq 1 μ F; 0603 \geq 0.15 μ F; 0805 \geq 4.7 μ F; 1206 \geq 47 μ F; 1210 \geq 100 μ F
				20 % 0402 \geq 2.2 μ F
		4 V	15 %	- -
		Y5V:		
		RATED VOLTAGE	DF \leq	EXCEPTION OF DF \leq
		\geq 50 V	5 %	7 % 0603 \geq 0.1 μ F; 0805 \geq 0.47 μ F; 1206 \geq 4.7 μ F
				- -
		35 V	7 %	- -
				7 % 0402 \geq 0.047 μ F; 0603 \geq 0.1 μ F; 0805 \geq 0.33 μ F; 1206 \geq 1 μ F; 1210 \geq 4.7 μ F
		25 V	5 %	9 % 0402 \geq 0.068 μ F; 0603 \geq 0.47 μ F; 1206 \geq 4.7 μ F; 1210 \geq 22 μ F
				9 % 0402 \geq 0.068 μ F; 0603 \geq 0.68 μ F
		16 V C < 1.0 μ F	7 %	12.5 % 0402 \geq 0.22 μ F
				12.5 % 0603 \geq 2.2 μ F; 0805 \geq 3.3 μ F; 1206 \geq 10 μ F; 1210 \geq 22 μ F
		16 V C \geq 1.0 μ F	9 %	20 % 0402 \geq 0.47 μ F
		10 V	12.5 %	20 % 0402 \geq 0.47 μ F
		6.3 V	20 %	- -

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4) Dielectric strength	<ul style="list-style-type: none"> To apply voltage (≤ 100 V) 250 % Duration: 1 s to 5 s Charge and discharge current less than 50 mA <ul style="list-style-type: none"> To apply voltage 200 V to 300 V ≥ 2 times V_{DC} 500 V to 999 V ≥ 1.5 times V_{DC} 1000 V to 3000 V ≥ 1.2 times V_{DC} Cut-off, set at 10 mA TEST = 15 s RAMP = 0 	<ul style="list-style-type: none"> No evidence of damage or flash-over during test 																				
5) Insulation resistance	<p>Rated voltage ≤ 100 V: To apply rated voltage for max. 120 s</p> <table border="1"> <thead> <tr> <th>RATED VOLTAGE</th> <th>INSULATION RESISTANCE</th> </tr> </thead> <tbody> <tr> <td>100 V: X7R</td> <td rowspan="6">10 GΩ or $R \times C \geq 500$ ΩF whichever is smaller</td> </tr> <tr> <td>50 V: 0603 ≥ 1 μF; 0805 ≥ 1 μF; 1206 ≥ 2.2 μF; 1210 ≥ 4.7 μF</td> </tr> <tr> <td>35 V: 0805 ≥ 2.2 μF; 1210 ≥ 10 μF</td> </tr> <tr> <td>25 V: 0402 ≥ 1 μF; 0603 ≥ 2.2 μF; 0805 ≥ 2.2 μF; 1206 ≥ 10 μF; 1210 ≥ 10 μF</td> </tr> <tr> <td>16 V: 0402 ≥ 0.22 μF; 0603 ≥ 1 μF; 0805 ≥ 2.2 μF; 1206 ≥ 10 μF; 1210 ≥ 47 μF</td> </tr> <tr> <td>10 V: 0201 ≥ 47 nF; 0402 ≥ 0.47 μF; 0603 ≥ 0.47 μF; 0805 ≥ 2.2 μF; 1206 ≥ 4.7 μF; 1210 ≥ 47 μF</td> </tr> <tr> <td>6.3 V; 4 V</td> </tr> </tbody> </table> <p>Rated voltage: 200 V to 630 V</p> <p>To apply rated voltage (500 V max.) for 60 s</p> <p>Rated voltage: > 630 V</p> <p>To apply 500 V for 60 s</p>	RATED VOLTAGE	INSULATION RESISTANCE	100 V: X7R	10 G Ω or $R \times C \geq 500$ Ω F whichever is smaller	50 V: 0603 ≥ 1 μ F; 0805 ≥ 1 μ F; 1206 ≥ 2.2 μ F; 1210 ≥ 4.7 μ F	35 V: 0805 ≥ 2.2 μ F; 1210 ≥ 10 μ F	25 V: 0402 ≥ 1 μ F; 0603 ≥ 2.2 μ F; 0805 ≥ 2.2 μ F; 1206 ≥ 10 μ F; 1210 ≥ 10 μ F	16 V: 0402 ≥ 0.22 μ F; 0603 ≥ 1 μ F; 0805 ≥ 2.2 μ F; 1206 ≥ 10 μ F; 1210 ≥ 47 μ F	10 V: 0201 ≥ 47 nF; 0402 ≥ 0.47 μ F; 0603 ≥ 0.47 μ F; 0805 ≥ 2.2 μ F; 1206 ≥ 4.7 μ F; 1210 ≥ 47 μ F	6.3 V; 4 V	<p>10 GΩ or $R \times C \geq 500$ ΩF whichever is smaller</p> <p>Class 2 (X5R, X7R, Y5V):</p> <table border="1"> <thead> <tr> <th>RATED VOLTAGE</th> <th>INSULATION RESISTANCE</th> </tr> </thead> <tbody> <tr> <td>100 V: X7R</td> <td rowspan="6">10 GΩ or $R \times C \geq 100$ ΩF whichever is smaller</td> </tr> <tr> <td>50 V: 0603 ≥ 1 μF; 0805 ≥ 1 μF; 1206 ≥ 2.2 μF; 1210 ≥ 4.7 μF</td> </tr> <tr> <td>35 V: 0805 ≥ 2.2 μF; 1210 ≥ 10 μF</td> </tr> <tr> <td>25 V: 0402 ≥ 1 μF; 0603 ≥ 2.2 μF; 0805 ≥ 2.2 μF; 1206 ≥ 10 μF; 1210 ≥ 10 μF</td> </tr> <tr> <td>16 V: 0402 ≥ 0.22 μF; 0603 ≥ 1 μF; 0805 ≥ 2.2 μF; 1206 ≥ 10 μF; 1210 ≥ 47 μF</td> </tr> <tr> <td>10 V: 0201 ≥ 47 nF; 0402 ≥ 0.47 μF; 0603 ≥ 0.47 μF; 0805 ≥ 2.2 μF; 1206 ≥ 4.7 μF; 1210 ≥ 47 μF</td> </tr> <tr> <td>6.3 V; 4 V</td> </tr> </tbody> </table> <p>> 10 GΩ or $R \times C > 100$ ΩF whichever is smaller</p> <p>> 10 GΩ</p>	RATED VOLTAGE	INSULATION RESISTANCE	100 V: X7R	10 G Ω or $R \times C \geq 100$ Ω F whichever is smaller	50 V: 0603 ≥ 1 μ F; 0805 ≥ 1 μ F; 1206 ≥ 2.2 μ F; 1210 ≥ 4.7 μ F	35 V: 0805 ≥ 2.2 μ F; 1210 ≥ 10 μ F	25 V: 0402 ≥ 1 μ F; 0603 ≥ 2.2 μ F; 0805 ≥ 2.2 μ F; 1206 ≥ 10 μ F; 1210 ≥ 10 μ F	16 V: 0402 ≥ 0.22 μ F; 0603 ≥ 1 μ F; 0805 ≥ 2.2 μ F; 1206 ≥ 10 μ F; 1210 ≥ 47 μ F	10 V: 0201 ≥ 47 nF; 0402 ≥ 0.47 μ F; 0603 ≥ 0.47 μ F; 0805 ≥ 2.2 μ F; 1206 ≥ 4.7 μ F; 1210 ≥ 47 μ F	6.3 V; 4 V
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6) Temperature coefficient	<p>With no electrical load:</p> <table border="1"> <thead> <tr> <th>T.C.</th> <th>Operating Temp.</th> <th>T.C.</th> <th>Capacitance change</th> </tr> </thead> <tbody> <tr> <td>C0G (NP0)</td> <td>- 55 °C to 125 °C at 25 °C</td> <td>C0G (NP0)</td> <td>Within ± 30 ppm/°C</td> </tr> <tr> <td>X7R</td> <td>- 55 °C to 125 °C at 25 °C</td> <td>X7R</td> <td>Within ± 15 %</td> </tr> <tr> <td>X5R</td> <td>- 55 °C to 85 °C at 25 °C</td> <td>X5R</td> <td>Within ± 15 %</td> </tr> <tr> <td>Y5V</td> <td>- 25 °C to 85 °C at 20 °C</td> <td>Y5V</td> <td>Within + 30 %/ - 80 %</td> </tr> </tbody> </table>	T.C.	Operating Temp.	T.C.	Capacitance change	C0G (NP0)	- 55 °C to 125 °C at 25 °C	C0G (NP0)	Within ± 30 ppm/°C	X7R	- 55 °C to 125 °C at 25 °C	X7R	Within ± 15 %	X5R	- 55 °C to 85 °C at 25 °C	X5R	Within ± 15 %	Y5V	- 25 °C to 85 °C at 20 °C	Y5V	Within + 30 %/ - 80 %	
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7) Adhesive strength of termination	<ul style="list-style-type: none"> Pressurizing force: 0201: 2N 0402 and 0603: 5 N $>$ 0603: 10 N Test time 10 s ± 1 s 	<ul style="list-style-type: none"> No remarkable damage or removal of the terminations 																				
8) Vibration resistance	<ul style="list-style-type: none"> Vibration frequency: 10 Hz/min to 55 Hz/min Total amplitude: 1.5 mm Test time: 6 h (2 h each in 3 mutually perpendicular directions) 	<ul style="list-style-type: none"> No remarkable damage Capacitance change and Q/DF: to meet initial specification 																				
9) Solderability	<ul style="list-style-type: none"> Solder temperature: 235 °C ± 5 °C Dipping time: 2 s ± 0.5 s 	95 % minimum coverage of all metallized area																				

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10) Bending test	<ul style="list-style-type: none"> The middle part of the substrate shall be pressurized by means of the pressurizing rod at a rate of about 1 mm per s until the deflection becomes 1 mm and then the pressure shall be maintained for 5 s ± 1 s Measurement to be made after keeping at room temperature for 24 h ± 2 h 	<ul style="list-style-type: none"> No remarkable damage Capacitance change: C0G (NP0): within ± 5.0 % or ± 0.5 pF whichever is larger X7R, X5R: within ± 12.5 % Y5V: within ± 30 % <p>(This capacitance change means the change of capacitance under specified flexure of substrate from the capacitance measured before the test)</p>															
11) Resistance to soldering heat	<ul style="list-style-type: none"> Solder temperature: 260 °C ± 5 °C Dipping time: 10 s ± 1 s Preheating: 120 °C to 150 °C for 1 min before immerse the capacitor in a eutectic solder Before initial measurement (class 2 only): Perform 150 °C + 0 °C/- 10 °C for 1 h and then set for 24 h ± 2 h at room temperature Measurement to be made after keeping at room temperature for 24 h ± 2 h 	<ul style="list-style-type: none"> No remarkable damage Capacitance change: C0G (NP0): within ± 2.5 % or ± 0.25 pF whichever is larger X7R, X5R: within ± 7.5 % Y5V: within ± 20 % Q/DF, I.R. and dielectric strength: To meet initial requirements 25 % maximum leaching on each edge 															
12) Temperature cycle	<table border="1"> <thead> <tr> <th>Step</th> <th>Temperature (°C)</th> <th>Time (min.)</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>Min. operating temp. + 0/- 3</td> <td>30 ± 3</td> </tr> <tr> <td>2</td> <td>Room temperature</td> <td>2 ~ 3</td> </tr> <tr> <td>3</td> <td>Max. operating temp. + 3/- 0</td> <td>30 ± 3</td> </tr> <tr> <td>4</td> <td>Room temperature</td> <td>2 ~ 3</td> </tr> </tbody> </table> <ul style="list-style-type: none"> Conduct the 5 cycles according to the temperature and time Before initial measurement (class 2 only): Perform 150 °C + 0 °C/- 10 °C for 1 h and then set for 24 h ± 2 h at room temperature Measurement to be made after keeping at room temperature for 24 h ± 2 h 	Step	Temperature (°C)	Time (min.)	1	Min. operating temp. + 0/- 3	30 ± 3	2	Room temperature	2 ~ 3	3	Max. operating temp. + 3/- 0	30 ± 3	4	Room temperature	2 ~ 3	<ul style="list-style-type: none"> No remarkable damage Capacitance change: C0G (NP0): within ± 2.5 % or ± 0.25 pF whichever is larger X7R, X5R: within ± 7.5 % Y5V: within ± 20 % Q/DF, I.R. and dielectric strength: To meet initial requirements
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<ul style="list-style-type: none"> • No remarkable damage • Capacitance change: C0G (NP0): within $\pm 5.0\%$ or $\pm 0.5\text{ pF}$ whichever is larger X7R, X5R: $\geq 10\text{ V}$ within $\pm 12.5\%$; 6.3 V, within $\pm 25\%$ Y5V: $\geq 10\text{ V}$ within $\pm 30\%$, 6.3 V within $+30\%/-40\%$ • Q/DF value: C0G (NP0): more than 30 pF: $Q \geq 350$ $10\text{ pF} \leq C \leq 30\text{ pF}$: $Q \geq 275 + 2.5\text{ C}$ Less than 10 pF: $Q \geq 200 + 10\text{ C}$ <p>X5R, X7R:</p> <table border="1"> <thead> <tr> <th>RATED VOLTAGE</th> <th>DF \leq</th> <th colspan="2">EXCEPTION OF DF \leq</th> </tr> </thead> <tbody> <tr> <td rowspan="3">$\geq 50\text{ V}$</td> <td rowspan="3">3 %</td> <td>6 %</td> <td>$0201 (50\text{ V})$; $0603 \geq 0.047\text{ }\mu\text{F}$; $0805 \geq 0.18\text{ }\mu\text{F}$; $1206 \geq 0.47\text{ }\mu\text{F}$</td> </tr> <tr> <td>10 %</td> <td>$1210 \geq 4.7\text{ }\mu\text{F}$</td> </tr> <tr> <td>20 %</td> <td>$0603 \geq 1\text{ }\mu\text{F}$; $0805 \geq 1\text{ }\mu\text{F}$; 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<ul style="list-style-type: none"> • No remarkable damage • Capacitance change: C0G (NP0): within $\pm 7.5\%$ or $\pm 0.75\text{ pF}$ whichever is larger. X7R, X5R: $\geq 10\text{ V}$ within $\pm 12.5\%$; 6.3 V, with $\pm 25\%$ Y5V: $\geq 10\text{ V}$ within $\pm 30\%$; 6.3 V, within $+30\%/-40\%$ • Q/DF value: C0G (NP0) Cap $\geq 30\text{ pF}$: Q ≥ 200; Cap $< 30\text{ pF}$: Q $\geq 100 + 10/3\text{ C}$ <p>X5R, X7R:</p> <table> <thead> <tr> <th>RATED VOLTAGE</th> <th>DF \leq</th> <th colspan="2">EXCEPTION OF DF \leq</th> </tr> </thead> <tbody> <tr> <td rowspan="3">$\geq 50\text{ V}$</td> <td rowspan="3">3 %</td> <td>6 %</td> <td>0201 (50 V); 0603 $\geq 0.047\text{ }\mu\text{F}$; 0805 $\geq 0.18\text{ }\mu\text{F}$; 1206 $\geq 0.47\text{ }\mu\text{F}$</td> </tr> <tr> <td>10 %</td> <td>1210 $\geq 4.7\text{ }\mu\text{F}$</td> </tr> <tr> <td>20 %</td> <td>0603 $\geq 1\text{ }\mu\text{F}$; 0805 $\geq 1\text{ }\mu\text{F}$; 1206 $\geq 2.2\text{ }\mu\text{F}$; 1210 $\geq 10\text{ }\mu\text{F}$</td> </tr> <tr> <td>35 V</td> <td>5 %</td> <td>20 %</td> <td>0805 $\geq 2.2\text{ }\mu\text{F}$; 1210 $\geq 10\text{ }\mu\text{F}$</td> </tr> <tr> <td rowspan="3">25 V</td> <td rowspan="3">5 %</td> <td>10 %</td> <td>0201 $\geq 0.01\text{ }\mu\text{F}$; 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<ul style="list-style-type: none"> Test temperature: COG (NP0), X7R/X7E: $125^{\circ}\text{C} \pm 3^{\circ}\text{C}$ X5R, Y5V: $85^{\circ}\text{C} \pm 3^{\circ}\text{C}$ To apply voltage: <p>(1.1) 100 % of rated voltage for below range</p> <table border="1"> <thead> <tr> <th>SIZE</th><th>DIELECTRIC</th><th>RATED VOLTAGE</th><th>CAP. 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RANGE	0201	X5R/X7R	6.3 V, 10 V	$C \geq 0.1 \mu\text{F}$	0402	X5R/X7R	6.3 V, 10 V	$C \geq 1 \mu\text{F}$	0603	X5R/X7R	6.3 V, 10 V	$C \geq 4.7 \mu\text{F}$	0805	X5R/X7R	6.3 V	$C \geq 22 \mu\text{F}$	1206	X5R/X7R	6.3 V	$C \geq 47 \mu\text{F}$	SIZE	DIELECTRIC	RATED VOLTAGE	CAP. RANGE	0402	X5R/X7R	10 V, 16 V, 25 V	$C \geq 0.22 \mu\text{F}$	Y5V	16 V	$C \geq 0.47 \mu\text{F}$	0603	X5R/X7R	10 V, 16 V	$C \geq 1.0 \mu\text{F}$	Y5V	16 V	$C \geq 2.2 \mu\text{F}$	0805	X5R/X7R	10 V	$C \geq 4.7 \mu\text{F}$	Y5V	16 V	$C \geq 4.7 \mu\text{F}$	<ul style="list-style-type: none"> No remarkable damage Capacitance change: COG (NP0): $\pm 3.0\%$ or $\pm 0.3 \mu\text{F}$ whichever is larger. X7R, X5R: $\geq 10 \text{ V}$ within $\pm 12.5\%$; 6.3 V, with $\pm 25\%$ Y5V: $\geq 10 \text{ V}$ within $\pm 30\%$; 6.3 V, within $+30\%$ to -40% Q/DF value: COG (NP0): More than $30 \mu\text{F}$, $Q \geq 350$ $10 \mu\text{F} \leq C < 30 \mu\text{F}$: $Q \geq 275 \text{ C} + 2.5 \text{ C}$; Less than $10 \mu\text{F}$: $Q \geq 200 \text{ C} + 10 \text{ C}$ <p>X5R, X7R:</p> <table border="1"> <thead> <tr> <th>RATED VOLTAGE</th><th>DF \leq</th><th colspan="2">EXCEPTION OF DF \leq</th></tr> </thead> <tbody> <tr> <td rowspan="3">$\geq 50 \text{ V}$</td><td rowspan="3">3 %</td><td>6 %</td><td>0201 (50 V); 0603 $\geq 0.047 \mu\text{F}$; 0805 $\geq 0.18 \mu\text{F}$; 1206 $\geq 0.47 \mu\text{F}$</td></tr> <tr> <td>10 %</td><td>1210 $\geq 4.7 \mu\text{F}$</td></tr> <tr> <td>20 %</td><td>0603 $\geq 1 \mu\text{F}$; 0805 $\geq 1 \mu\text{F}$; 1206 $\geq 2.2 \mu\text{F}$; 1210 $\geq 10 \mu\text{F}$</td></tr> <tr> <td rowspan="3">35 V</td><td rowspan="3">5 %</td><td>20 %</td><td>0805 $\geq 2.2 \mu\text{F}$; 1210 $\geq 10 \mu\text{F}$</td></tr> <tr> <td>10 %</td><td>0201 $\geq 0.01 \mu\text{F}$; 0805 $\geq 1 \mu\text{F}$; 1210 $\geq 10 \mu\text{F}$</td></tr> <tr> <td>14 %</td><td>0603 $\geq 0.33 \mu\text{F}$; 1206 $\geq 4.7 \mu\text{F}$</td></tr> <tr> <td rowspan="3">25 V</td><td rowspan="3">5 %</td><td>15 %</td><td>0402 $\geq 0.10 \mu\text{F}$; 0603 $\geq 0.47 \mu\text{F}$; 0805 $\geq 2.2 \mu\text{F}$; 1206 $\geq 6.8 \mu\text{F}$; 1210 $\geq 22 \mu\text{F}$</td></tr> <tr> <td>10 %</td><td>0603 $\geq 0.15 \mu\text{F}$; 0805 $\geq 0.68 \mu\text{F}$; 1206 $\geq 2.2 \mu\text{F}$; 1210 $\geq 4.7 \mu\text{F}$</td></tr> <tr> <td>15 %</td><td>0201 $\geq 0.01 \mu\text{F}$; 0402 $\geq 0.033 \mu\text{F}$; 0603 $\geq 0.68 \mu\text{F}$; 0805 $\geq 2.2 \mu\text{F}$; 1206 $\geq 4.7 \mu\text{F}$; 1210 $\geq 22 \mu\text{F}$</td></tr> <tr> <td rowspan="2">16 V</td><td rowspan="2">5 %</td><td>15 %</td><td>0201 $\geq 0.012 \mu\text{F}$; 0402 $\geq 0.33 \mu\text{F}$; 0603 $\geq 0.33 \mu\text{F}$; 0805 $\geq 2.2 \mu\text{F}$; 1206 $\geq 2.2 \mu\text{F}$; 1210 $\geq 22 \mu\text{F}$</td></tr> <tr> <td>20 %</td><td>0201 $\geq 0.1 \mu\text{F}$; 0402 $\geq 1 \mu\text{F}$</td></tr> <tr> <td>10 V</td><td>7.5 %</td><td>15 %</td><td>0201 $\geq 0.1 \mu\text{F}$; 0402 $\geq 1 \mu\text{F}$; 0603 $\geq 10 \mu\text{F}$; 0805 $\geq 4.7 \mu\text{F}$; 1206 $\geq 47 \mu\text{F}$; 1210 $\geq 100 \mu\text{F}$</td></tr> <tr> <td>4 V</td><td>20 %</td><td>-</td><td>-</td></tr> <tr> <td colspan="5">Y5V:</td></tr> <tr> <td rowspan="2">$\geq 50 \text{ V}$</td><td rowspan="2">7.5 %</td><td>10 %</td><td>0603 $\geq 0.1 \mu\text{F}$; 0805 $\geq 0.47 \mu\text{F}$; 1206 $\geq 4.7 \mu\text{F}$</td></tr> <tr> <td>10 %</td><td>-</td></tr> <tr> <td rowspan="2">35 V</td><td rowspan="2">7.5 %</td><td>10 %</td><td>0402 $\geq 0.047 \mu\text{F}$; 0603 $\geq 0.1 \mu\text{F}$; 0805 $\geq 0.33 \mu\text{F}$; 1206 $\geq 1 \mu\text{F}$; 1210 $\geq 4.7 \mu\text{F}$</td></tr> <tr> <td>15 %</td><td>0402 $\geq 0.068 \mu\text{F}$; 0603 $\geq 0.47 \mu\text{F}$; 1206 $\geq 4.7 \mu\text{F}$; 1210 $\geq 22 \mu\text{F}$</td></tr> <tr> <td rowspan="2">25 V</td><td rowspan="2">10 %</td><td>12.5 %</td><td>0402 $\geq 0.068 \mu\text{F}$; 0603 $\geq 0.68 \mu\text{F}$</td></tr> <tr> <td>20 %</td><td>0402 $\geq 0.22 \mu\text{F}$</td></tr> <tr> <td rowspan="2">16 V $C < 1.0 \mu\text{F}$</td><td rowspan="2">12.5 %</td><td>20 %</td><td>0603 $\geq 2.2 \mu\text{F}$; 0805 $\geq 3.3 \mu\text{F}$; 1206 $\geq 10 \mu\text{F}$; 1210 $\geq 22 \mu\text{F}$</td></tr> <tr> <td>20 %</td><td>0402 $\geq 0.47 \mu\text{F}$</td></tr> <tr> <td rowspan="2">16 V $C \geq 1.0 \mu\text{F}$</td><td rowspan="12">12.5 %</td><td>30 %</td><td>0603 $\geq 2.2 \mu\text{F}$; 0805 $\geq 3.3 \mu\text{F}$; 1206 $\geq 10 \mu\text{F}$; 1210 $\geq 22 \mu\text{F}$</td></tr> <tr> <td>30 %</td><td>-</td></tr> <tr> <td colspan="5">• I. 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