

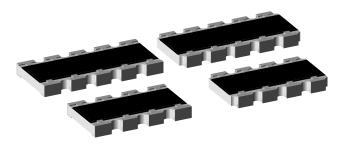
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CRCA

Vishay Dale

Document Number: 31044

# Thick Film Resistor/Capacitor Chip Array, Surface Mount



## **FEATURES**

 Single component reduces board space and component counts



- X7R dielectric characteristic
- Wrap around termination
- Thick film R/C element
- Inner electrode protection
- Flow and reflow solderable
- Automatic placement capability, standard size
- 8 pin or 10 pin configurations
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

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STANDARD ELECTRICAL SPECIFICATIONS										
	SCHEMATIC	RESISTOR CHARACTERISTICS				CAPACITOR CHARACTERISTICS				
GLOBAL MODEL		D	TEMP. COEFF. ± ppm/°C			DIELECTRIC	TEMPERATURE COEFFICIENT %	-	CAP. VOLTAGE V <sub>DC</sub>	CAP. RANGE
0004405	01	0.125	200	5	10 to 1M	X7R	± 15	20	50	10 pF to 270 pF
CRCA12E CRCA12S	02	0.125	200	5	10 to 1M	X7R	± 15	20	50	10 pF to 270 pF
	03	0.125	200	5	10 to 1M	X7R	± 15	20	50	10 pF to 270 pF

#### **Notes**

#### RESISTOR

- Operating temperature range: -55 °C to +125 °C
- Technology: Thick film
- Packaging: According to EIA 481.
- Ask about extended value ranges.

#### **CAPACITOR**

- Operating temperature range: X7R -55 °C to +125 °C
- Maximum dissipation factor: 2.5 %we
- Dielectric with standing voltage: 125  $V_{DC}$ , 5 s, 50 mA charge

TECHNICAL SPECIFICATIONS								
PARAMETER	UNIT	RESISTOR	X7R CAPACITOR					
Rated dissipation at 70 °C (CECC 40401 I EIA 575)	W	0.125	-					
Capacitor voltage rating	V	-	50					
Dielectric withstanding voltage (5 s, 50 mA charge)	$V_{DC}$	-	125					
Category temperature range	°C	-55 / +125	-55 / +125					
Insulation resistance	Ω	> -	10 <sup>10</sup>					

Power rating depends on the max, temperature at the solder point, the component placement density and the substrate material.

#### **GLOBAL PART NUMBER INFORMATION** New Global Part Numbering: CRCA12E081472220R (preferred part numbering format) 2 0 R C Ε 0 2 2 **CAPACITANCE** RESISTANCE PIN MODEL **SCHEMATIC PACKAGING SPECIAL** COUNT **VALUE** VALUE CRCA12E **08** = 8 pin **1** = 01 2 digit significant 2 digit significant E = Lead (Pb)-free, (Dash number) **10** = 10 pin CRCA12S **2** = 02 T/R (2000 pcs) figures, followed by figures, followed by (Up to 1 digit) 3 = 03a multiplier multiplier Blank = Standard R = Tin/Lead, 0 = Special **100** = $10 \Omega$ 100 = 10 pFT/R (2000 pcs) $683 = 68 \text{ k}\Omega$ 560 = 56 pF**105** = 1 $M\Omega$ **271** = 270 pF (Tolerance = $\pm 5 \%$ ) (Tolerance = $\pm 20 \%$ ) Historical Part Number Example: CRCA12E0801472J220MRB8 (will continue to be accepted) CRCA12E RB8 08 472 220 М RESISTANCE CAPACITANCE MODEL PIN COUNT **SCHEMATIC TOLERANCE** TOLERANCE **PACKAGING VALUE VALUE**

#### Note

Revision: 04-Nov-16

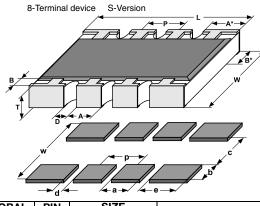
For additional information on packaging, refer to the Surface Mount Network Packaging document (www.vishav.com/doc?31540).

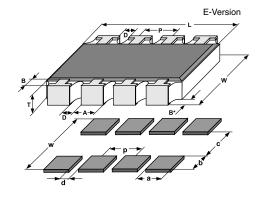


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# **DIMENSIONS**





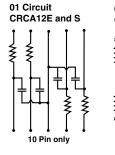
GLOBAL	PIN	S	SIZE	DIMENSIONS in millimeters									
MODEL	NO#	INCH	METRIC	L	W	Т	В	B*	Α	<b>A</b> *	D <sub>NOM</sub>	P <sub>NOM</sub>	
CRCA12E	8	2012	5032	$5.1 \pm 0.15$	$3.05 \pm 0.15$	$0.61 \pm 0.10$	$0.51 \pm 0.25$	$0.38 \pm 0.2$	$0.79 \pm 0.15$	-	0.25	1.27	
CRCA12S	8	2012	5032	$5.1 \pm 0.15$	$3.05 \pm 0.15$	$0.61 \pm 0.10$	$0.51 \pm 0.25$	$0.38 \pm 0.2$	$0.79 \pm 0.15$	$0.89 \pm 0.15$	0.25	1.27	
CRCA12E	10	2512	6432	$6.4 \pm 0.15$	$3.05 \pm 0.15$	$0.61 \pm 0.10$	$0.51 \pm 0.25$	$0.38 \pm 0.2$	$0.79 \pm 0.15$	-	0.25	1.27	
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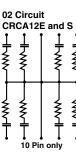
SOLDER PAD DIMENSIONS in millimeters									
c w d p a b e									
WAVE SOLDERING	2.2	4.3	0.57	1.27	0.71	1.05	1.09		
<b>REFLOW SOLDERING</b> 2.2 3.9 0.57 1.27 0.71 0.86 1.09									

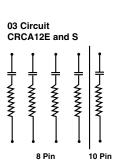
## Note

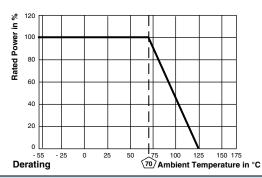
• The images shown are for an 8 pin part. For a 10 pin part, use the same pitch and add another pair of "a" dimension pads to the inner solder pads.

# **SCHEMATICS**









PERFORMANCE								
TEST	CONDITIONS OF TEST		TEST RESULTS (TYPICAL TEST LOTS)					
		R	С					
Endurance test at 70 °C MIL-STD-202 method 108	1000 h at 70 °C, 1.5 h "ON", 0.5 h "OFF"	± (5 % + 2 Ω)	± 20 %					
Dielectric withstanding voltage MIL-STD-202 method 301	125 V <sub>DC</sub> , 5 s, 50 mA charge	No physical damage						
Thermal shock MIL-STD-202 method 107	100 cycles, -55 °C to +125 °C	± (5 % + 2 Ω)	± 20 %					
Moisture MIL-STD-202 method 106	Omit steps 7A and B	± (5 % + 2 Ω)	± 20 %					
Resistance to soldering heat EIA 575	10 s at 260 °C solder bath temperature	± (5 % + 2 Ω)	± 20 %					
High temperature exposure EIA 575	125 °C for 100 h	± (5 % + 2 Ω)	± 20 %					
Low temperature operation EIA 575	1 h at -55 °C then 45 min at 50 V	± (5 % + 2 Ω)	± 20 %					
Solderability and leaching EIA 575 3.12	Condition C	95 % co	95 % coverage					

# **APPLICABLE SPECIFICATIONS**

- IPC standards
- EIA 575



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