IHLP-5050CE-07



Vishay Dale

IHLP[®] Commercial Inductors, High Saturation Series, 5 % DCR Tolerance



DESIGN SUPPORT TOOLS click logo to get started



Design Tools Available

STANDARD ELECTRICAL SPECIFICATIONS							
L ₀ INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A (μH)	DCR ± 5 % AT 25 °C (mΩ)	HEAT RATING CURRENT DC TYP. (A) ⁽¹⁾	SATURATION CURRENT DC TYP. (A) ⁽²⁾				
0.60	1.85	29	51				
0.68	2.34	28	49				
1.0	3.21	24	40				
1.5	4.97	19	35				
2.2	7.20	16	29				
3.3	10.69	12	27				
4.7	14.27	10	24				
5.6	18.19	9.5	19				
10	30.86	7	14				

Notes

- All test data is referenced to 25 °C ambient
- Operating temperature range -55 °C to +125 °C
- The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application
- Rated operating voltage (across inductor) = 75 V
- ⁽¹⁾ DC current (A) that will cause an approximate ΔT of 40 °C
- $^{(2)}$ DC current (A) that will cause L_0 to drop approximately 20 %

FEATURES

- Lowest height (3.5 mm) in this package footprint
- Shielded construction
- Frequency range up to 5.0 MHz
- Lowest DCR/µH, in this package size
 Handles high transient current spikes without
- Handles high transient current spikes without compliant saturation
 Litra low burgs points, due to composite construction
- Ultra low buzz noise, due to composite construction
- IHLP design. PATENT(S): <u>www.vishay.com/patents</u>
 Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

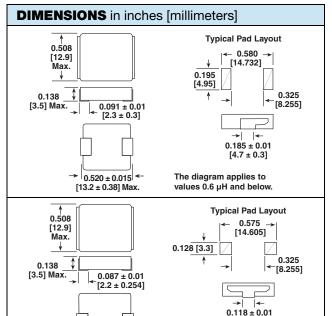
APPLICATIONS

- Tolerance DCR for current sense applications
- · Improved current balance in phased power supplies
- Improved thermal management
- PDA / notebook / desktop / server and battery powered devices
- High current, low profile POL converters

0.520 ± 0.015

[13.2 ± 0.38]

- DC/DC converters in distributed power systems
- DC/DC converter for Field Programmable Gate Array (FPGA)



The diagram applies to values 0.68 µH and above

 $[3.0 \pm 0.3]$

DESCRIPTION	l							
IHLP-5050CE-07	1.0 μH	± 20 %	ER	e3				
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC [®] LEAD (Pb)-FREE STANDARD				
GLOBAL PART NUMBER								
I H L	P 5 0	5 0 C E	E R 1	R 0	M 0 7			
PRODUCT FAN	л сл ИLY	SIZE	PACKAGE IN CODE	IDUCTANCE VALUE	TOL. SERIES			

PATENT(S): <u>www.vishay.com/patents</u> This Vishay product is protected by one or more United States and international patents.

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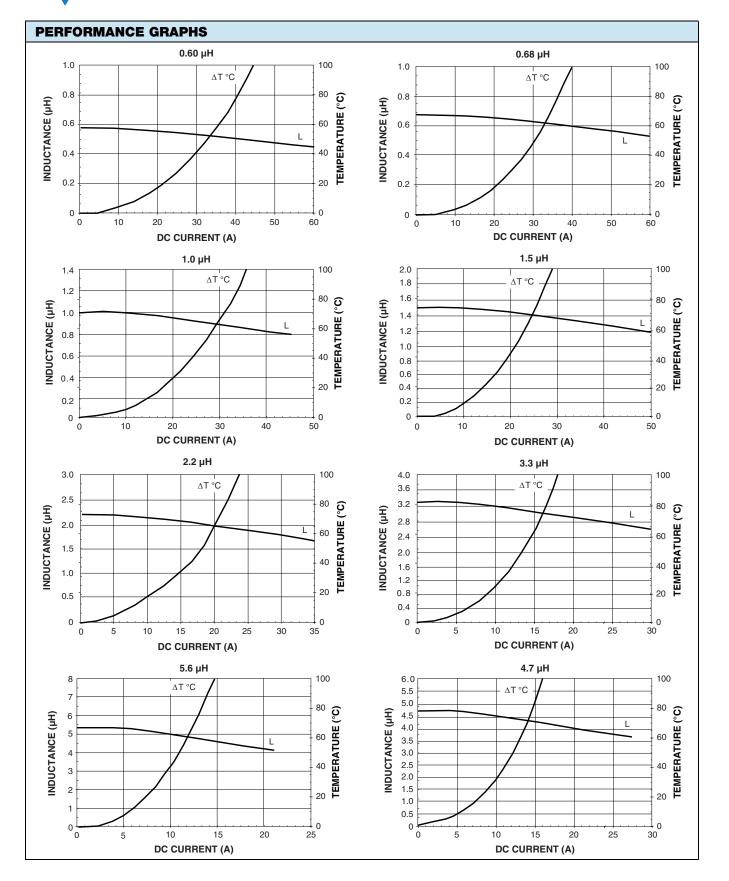
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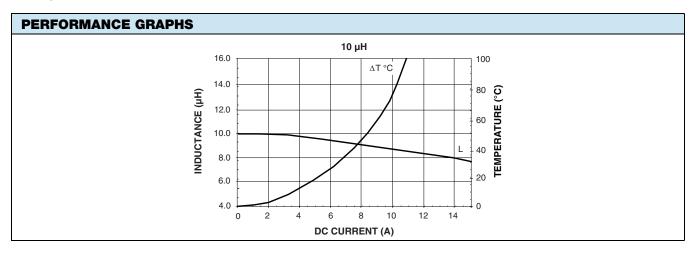
2 For technical questions, contact: <u>magnetics@vishav.com</u> Document Number: 34166

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1