

## Low Profile, High Current Coupled Inductor



### LINKS TO ADDITIONAL RESOURCES



3D Models



Calculators

STANDARD ELECTRICAL SPECIFICATIONS					
	$L_0$ INDUCTANCE $\pm 20\%$ AT 100 kHz, 0.25 V, 0 A ( $\mu$ H)	DCR NOM. 25 °C (m $\Omega$ )	DCR MAX. 25 °C (m $\Omega$ )	HEAT RATING CURRENT DC TYP. (A) <sup>(1)</sup>	SATURATION CURRENT DC TYP. (A) <sup>(2)</sup>
L <sub>1-2</sub>	4.7	30.5	33	6.1	7.2
L <sub>3-4</sub>	4.7	30.5	33	6.1	9.2
L <sub>1-4</sub> (L <sub>2-3</sub> shorted)	19	61	65	4	3
L <sub>1-3</sub> (L <sub>2-4</sub> shorted)	0.6	61	65	4	See note <sup>(3)</sup>
L <sub>Common Mode</sub> (1-3 and 2-4 shorted)	4.7	15.25	16.2	10	8
L <sub>Differential</sub> Mode (1-4 and 2-3 shorted)	0.0	15.25	16.2	10	See note <sup>(3)</sup>

#### Notes

- All test data is referenced to 25 °C ambient
- Operating temperature range -55 °C to +155 °C
- The part temperature (ambient + temp. rise) should not exceed 155 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application
- Rated operating voltage (across inductor) = 50 V
- SEPIC operation can generate up to 2x the input or output voltage across the inductor. Please limit  $V_{IN}$  and  $V_{OUT}$  to 25 V max. for SEPIC operation

<sup>(1)</sup> DC current (A) that will cause an approximate  $\Delta T$  of 40 °C

<sup>(2)</sup> DC current (A) that will cause  $L_0$  to drop approximately 20 %

<sup>(3)</sup> In this configuration, current flowing opposite directions through coils cancels and the 0.6  $\mu$ H inductance is very stable with varying current. Observe the heat rating current to avoid excessive temperature rise in this configuration

### FEATURES

- High temperature, up to 155 °C
- Shielded construction
- Frequency range up to 5.0 MHz
- Lowest DCR/ $\mu$ H in this package size
- Handles high transient current spikes without saturation
- Ultra low buzz noise, due to composite construction
- Coupling is > 90 % - optimized for SEPIC converters
- Tested to AEC-Q200 specifications <sup>(1)</sup>
- IHCL design; PATENT(S): [www.vishay.com/patents](http://www.vishay.com/patents)
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)

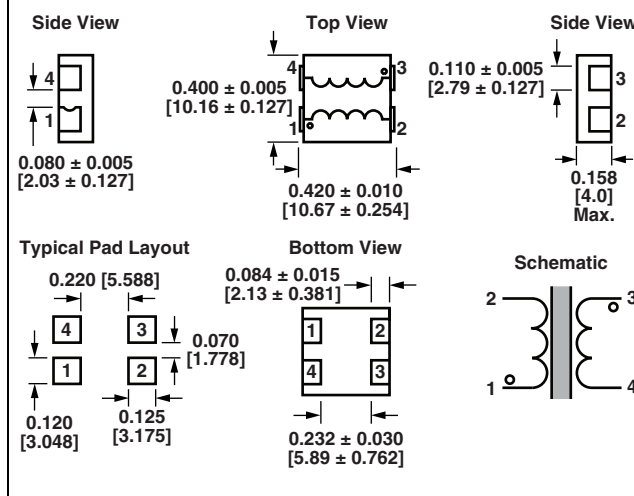
#### Note

<sup>(1)</sup> For automotive applications, use IHCL4040DZERXRXM5A.

### APPLICATIONS

- SEPIC converters
- DC/DC converters
- Common mode applications
- LED lighting

### DIMENSIONS in inches [millimeters]


PATENT(S): [www.vishay.com/patents](http://www.vishay.com/patents)

This Vishay product is protected by one or more United States and international patents.



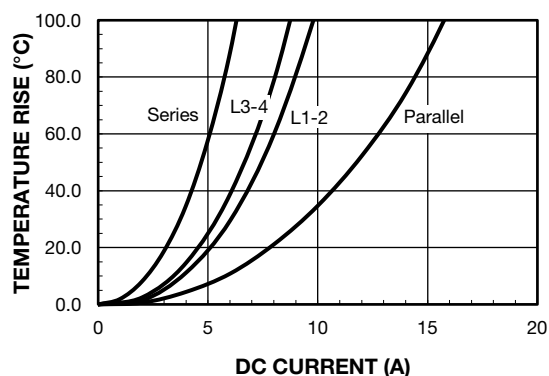
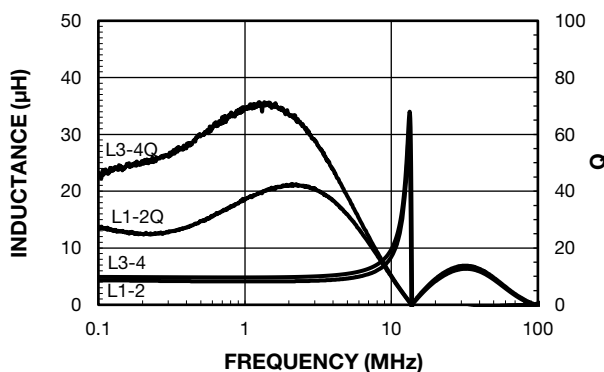
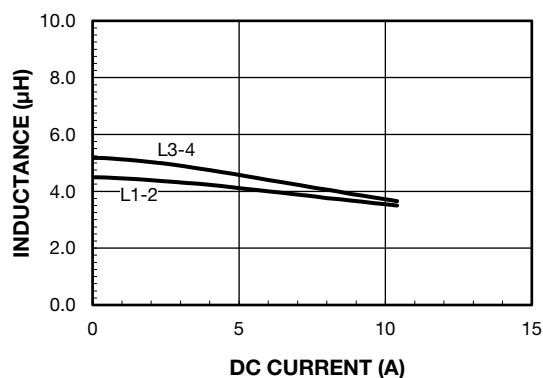
## DESCRIPTION

<b>IHCL-4040DZ-51</b>	<b>4.7 <math>\mu</math>H</b>	<b><math>\pm 20\%</math></b>	<b>ER</b>	<b>e3</b>
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD

## GLOBAL PART NUMBER

I	H	C	L	4	0	4	0	D	Z	E	R	4	R	7	M	5	1
PRODUCT FAMILY				SIZE				PACKAGE CODE		INDUCTANCE VALUE			TOL.	SERIES			

## PERFORMANCE GRAPHS





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