

Vishay Custom Magnetics

## SGIHLP<sup>®</sup> - Space Grade (MIL-STD-981 Compliant) IHLP<sup>®</sup> Inductors



### LINKS TO ADDITIONAL RESOURCES



STANDARD ELECTRICAL SPECIFICATIONS									
L <sub>0</sub> INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A (μH)	DCR TYP. 25 °C (mΩ)	DCR MAX. 25 °C (mΩ)	HEAT RATING CURRENT DC TYP. (A) <sup>(1)</sup>	SATURATION CURRENT DC TYP. (A) <sup>(2)</sup>	SRF TYP. (MHz)				
0.22	0.95	1.12	53.6	68	113				
0.33	1.05	1.23	49	44	79.6				
0.47	1.27	1.47	43	42	65.6				
1.0	1.88	2.08	32	26	33.4				
1.2	2.21	2.44	29	24.5	32.0				
1.5	2.63	2.89	27.5	23.5	29.2				
1.8	2.98	3.26	26	22.5	25.9				
2.2	3.66	3.99	25.5	21.5	23.3				
3.3	5.31	5.76	20.2	16.7	17.8				
4.7	7.64	8.25	17.4	18.5	15.8				
5.6	8.74	9.43	15.7	14.2	12.3				
6.8	11.53	12.41	14.2	8.7	13.4				
7.8	12.72	13.68	13.5	8.5	13.6				
8.2	13.32	14.32	13.2	7.6	10.3				
10	16.72	17.96	10.9	7.2	10.7				
12	19.12	20.53	10.6	6.9	7.59				
15	24.12	25.88	8.7	6.8	8.30				
22	31.42	33.69	8.3	5.5	6.43				

#### Notes

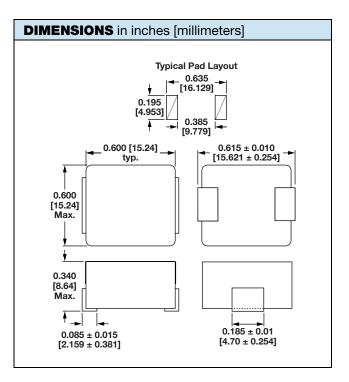
- All test data is referenced to 25 °C ambient
- Operating temperature range -55 °C to +180 °C
- The part temperature (ambient + temp. rise) should not exceed 180 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application
- Rated operating voltage (across inductor) = 75 V
- Maximum net weight = 8.5 g
- <sup>(1)</sup> DC current (A) that will cause an approximate  $\Delta T$  of 40 °C
- <sup>(2)</sup> DC current (A) that will cause  $L_0$  to drop approximately 20 %

### FEATURES

- MIL-STD-981 class S compliant (see "Screening Codes")
- High temperature rating, up to 180 °C
- Shielded construction
- Lowest DCR/µH, in this package size
- Handles high transient current spikes without saturation
- · Low profile package with high current saturation levels

### APPLICATIONS

- Low profile, high current power supplies
- High current POL converters
- DC/DC converters in distributed power systems
- Power converter for solar panels
- Noise suppression



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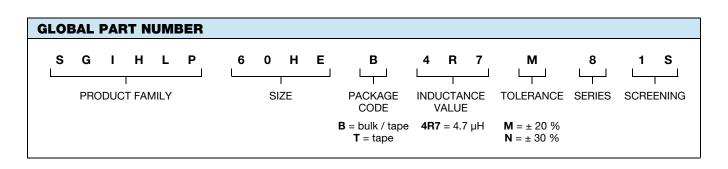
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#### DESCRIPTION

SGIHLP-60HE-8

MODEL

**4.7 μΗ** INDUCTANCE VALUE ± 20 % INDUCTANCE TOLERANCE B = bulk / tray, T = tape PACKAGE CODE



SCREENING BREAKDOWN									
	P = PRODUCTION SCREENED	L = LOW EARTH ORBIT (LEO) SCREENED	E3 = EEE-INST-002 LEVEL 3 SCREENED	E2 = EEE-INST-002 LEVEL 2 SCREENED	B = MIL-STD-981 TABLE VI CLASS B SCREENED	S = MIL-STD-981 TABLE VI AND XII CLASS S SCREENED			
PRODUCTION SCREENING (sample size = 100 %)									
Electrical characteristics (continuity, inductance (LS), turns ratio (TR), phase, leakage inductance, DWV, insulation resistance, DCR)	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	√	$\checkmark$			
Mechanical inspection	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$			
Visual inspection	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$			
QUALITY CONFORMANCE SCREENING (group A) (sample size = 100 %)									
5 cycle thermal shock (-55 °C to +155 °C)	n/a	n/a	$\checkmark$	n/a					
10 cycle thermal shock (-55 °C to +155 °C)	n/a	$\checkmark$	n/a	$\checkmark$	n/a	n/a			
96 hour burn-in at 155 °C (unpowered)	n/a	$\checkmark$	n/a	$\checkmark$	$\checkmark$	$\checkmark$			
25 cycle thermal shock (-55 °C to +155 °C)	n/a	n/a	n/a	n/a	$\checkmark$	$\checkmark$			
Dielectric withstanding voltage (DWV)	n/a	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$			
Dielectric withstanding voltage (DWV) at Altitude	n/a	n/a	$\checkmark$	$\checkmark$	n/a	n/a			
Insulation resistance (IR)	n/a	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$			
Electrical characteristics (continuity, LS, TR, phase, leakage inductance, DCR)	n/a	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$			
Radiographic inspection	n/a	n/a	n/a	n/a	n/a	$\checkmark$			
Mechanical inspection (sampled per table V; MIL-STD-981)	n/a	$\checkmark$	n/a	n/a	$\checkmark$	$\checkmark$			
Visual inspection (100 %)	n/a	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$			
QUALIFICATION INSPECTION (group B) (lot sampling)									
MIL-STD-981 table XII	n/a	n/a	n/a	n/a	n/a	$\checkmark$			
EEE-INST-002 Table 3	n/a	n/a	n/a	Optional	n/a	n/a			

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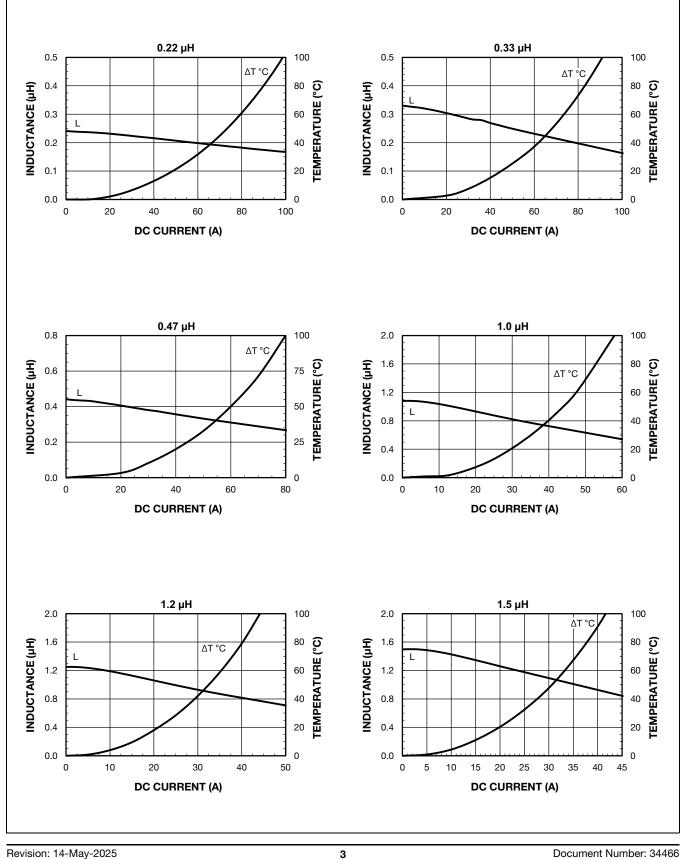
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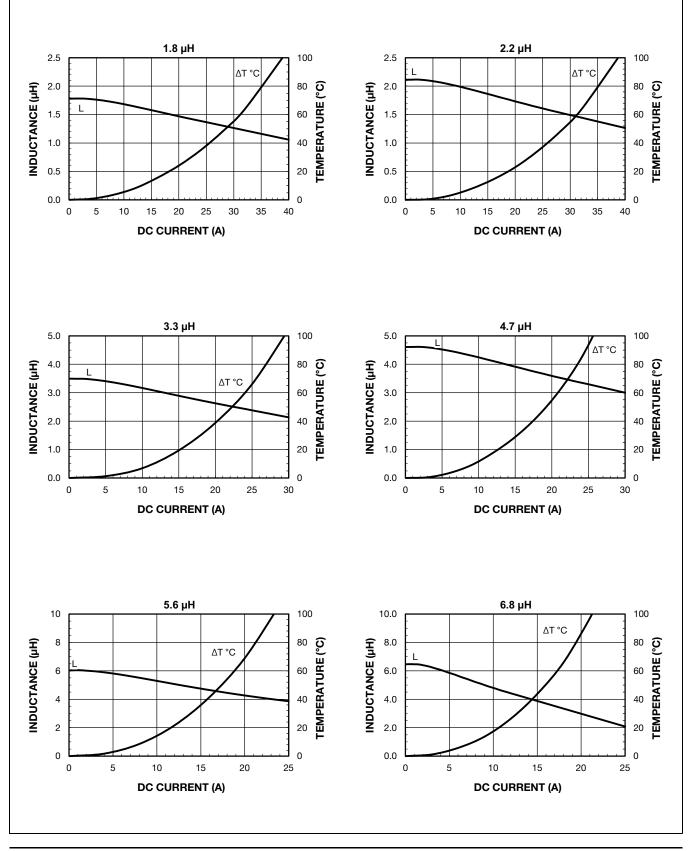
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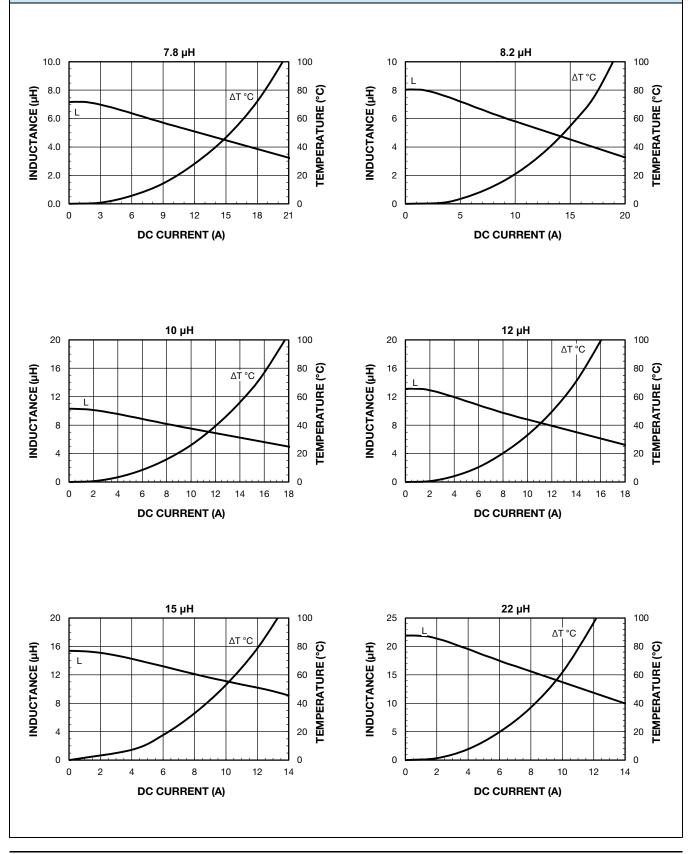
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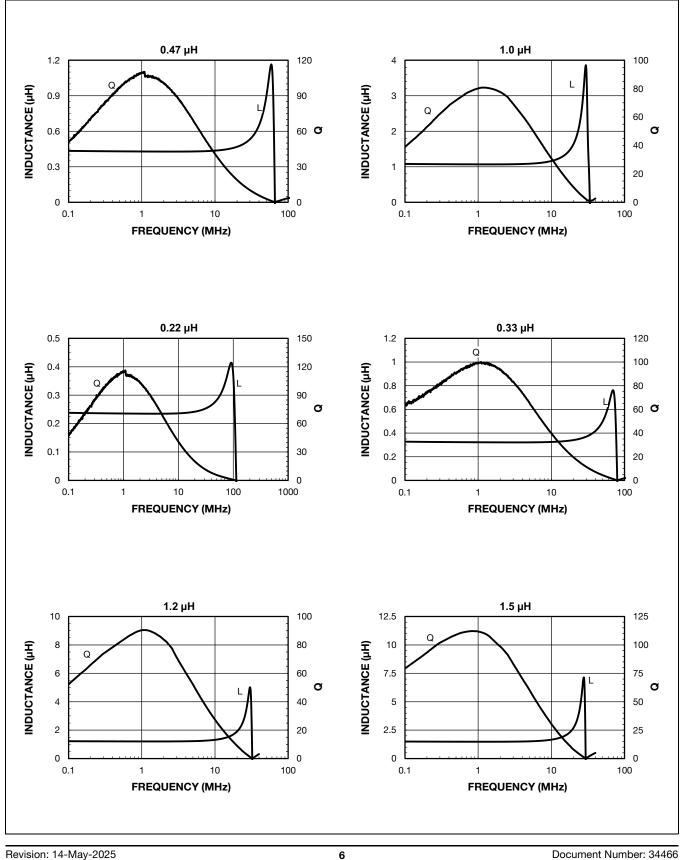
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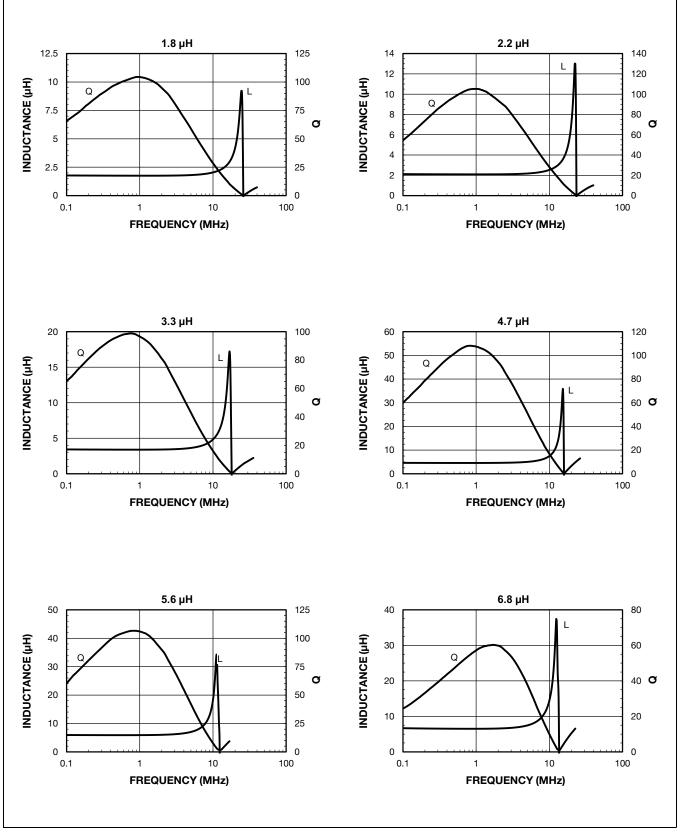
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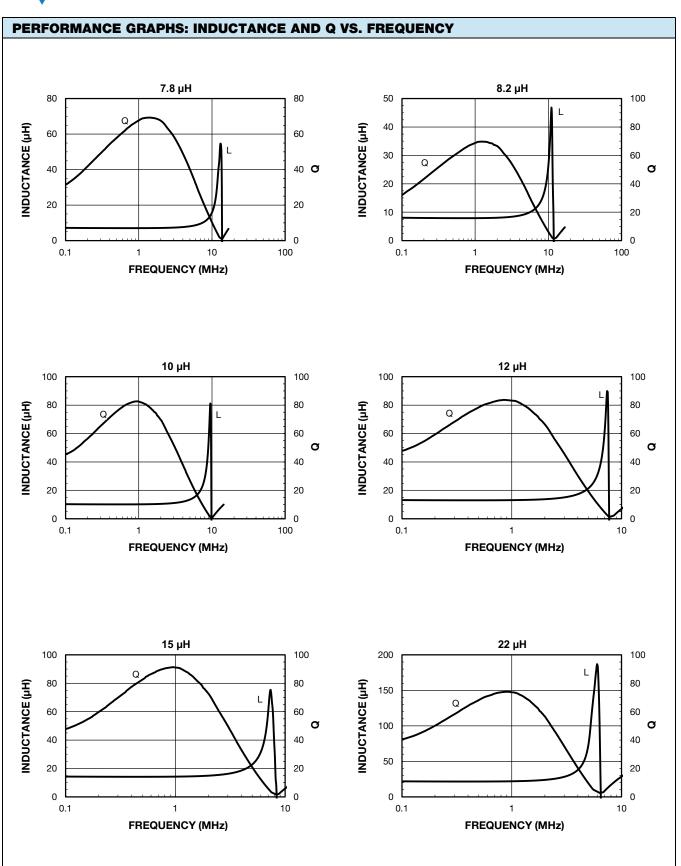
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