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Vishay Dale

RoHS

COMPLIANT

**HALOGEN** 

FREE

# Commercial Inductors, Ultra Low DCR, High Saturation Series





#### **LINKS TO ADDITIONAL RESOURCES**





#### **FEATURES**

- Magnetically shielded construction
- Optimized for high currents loads in high frequency converters
- Patented coil design achieves ultra low DCR and robust design
- Thermally conductive structure minimizes hot spots for enhanced heat dissipation over ferrite technologies in natural convection and active cooling environments
- Handles high transient current spikes without saturation
- IHSR design; PATENT(S): www.vishay.com/patents
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **APPLICATIONS**

- High current load EMI filters
- LIDAR boost inductor for laser diode with GaN FETs
- High frequency, low voltage converters (12 V to 1 V)
- · LC filter inductor

STANDARD ELECTRICAL SPECIFICATIONS									
	L <sub>0</sub> INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A	DCR ± 5 % AT 25 °C (mΩ)	HEAT RATING CURRENT DC (A) (1)	SATURATION CURRENT DC (A)		SRF TYP.			
PART NUMBER	(μΗ)	TYP.	TYP.	TYP. (2)	TYP. (3)	(MHz)			
IHSR1616ABER33NM01	0.033	1.15	37	41	62	856			
IHSR1616ABER68NM01	0.068	3.20	22	30	41	418			

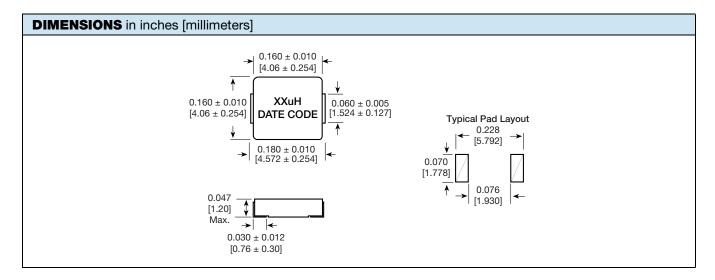
#### Notes

- All test data is referenced to 25 °C ambient
- Operating temperature range -55 °C to +125 °C
- The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component
  placement, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be
  verified in the end application
- $^{(1)}\,$  DC current (A) that will cause an approximate  $\Delta T$  of 40 °C
- $^{(2)}\,$  DC current (A) that will cause  $L_0$  to drop approximately 20 %
- (3) DC current (A) that will cause L<sub>0</sub> to drop approximately 30 %

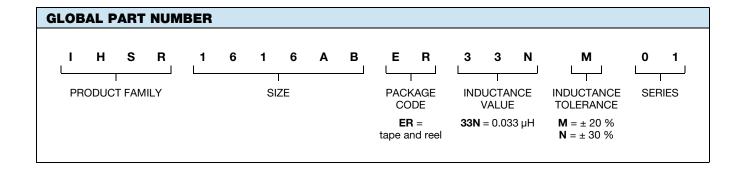
PATENT(S): www.vishay.com/patents

This Vishay product is protected by one or more United States and international patents.

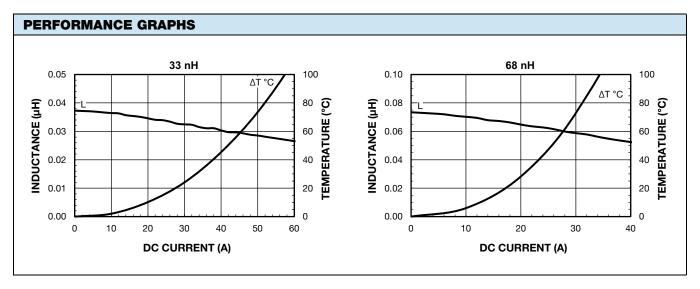
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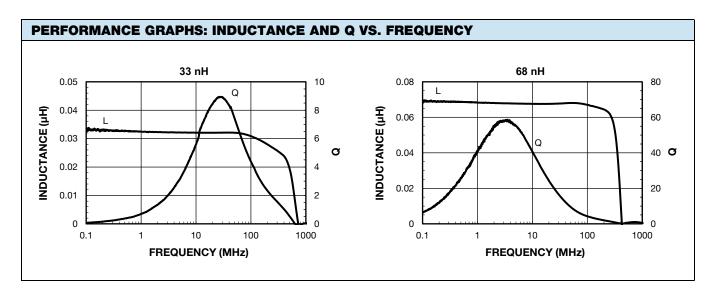


DESCRIPTION							
IHSR-1616AB-01	0.033 μΗ	± 20 %	ER	e3			
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD			











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