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(5-2008)





Thin Film Resistor Divider Network on Silicon, User Trimmable





Product may not be to scale

The SC7 and SCB series resistor chips offer a combination of nichrome user trimmability as a single resistor or as a ratio trim while maintaining the excellent TCR tracking characteristics of two resistors on the same chip.

The SC7 and SCBs are manufactured using Vishay Electro-Films (EFI) sophisticated thin film equipment and manufacturing technology. The SC7 and SCBs are 100 % electrically tested and visually inspected to MIL-STD-883, method 2032 class H or class K.

FEATURES

- Wire bondable
- Chip size
 SC7 0.030" x 0.030"
 SCB 0.050" x 0.050"
- Resistance range R_T : 100 Ω to 20 k Ω for SC7 100 Ω to 50 k Ω for SCB
- Case: 0303, 0505
- Silicon substrate
- Power: 250 mW or 400 mW capability
- Resistor material: Nichrome
- User trimmable
- Material categorization: For definitions of compliance please see <u>www.vishav.com/doc?99912</u>



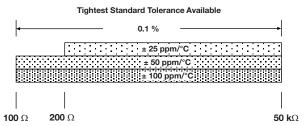
Vishay EFI SC7 and SCB chip resistors have excellent power dissipation capability and are ideally suited for prototyping.

Typical application areas are:

- Amplifiers
- Oscillators
- Attenuators
- Couplers
- Filters

Recommended for hermetic environments where die is not exposed to moisture

TEMPERATURE COEFFICIENT OF RESISTANCE, VALUES, AND TOLERANCES		
PARAMETER	VALUE	UNIT
Total Resistance Range	100 to 50K	Ω
Standard Tolerances	± 0.1	%
TCR	± 25, ± 50, ± 100	ppm/°C



STANDARD ELECTRICAL SPECIFICATIONS		
PARAMETER	VALUE	UNIT
Noise, MIL-STD-202, Method 308	- 20 typ.	dB
Stability, 1000 h, + 125 °C at Rated Power	+ 0.1 max. ∆R/R	%
Operating Temperature Range	- 55 to + 125	°C
Thermal Shock, MIL-STD-202, Method 107, Test Condition F	+ 0.25 max. ΔR/R	%
High Temperature Exposure, + 150 °C, 100 h	+ 0.25 max. ΔR/R	%
Dielectric Voltage Breakdown	400	V
Insulation Resistance	10 ¹² min.	Ω
Operating Voltage	100 max.	V
DC Power Rating at 70 °C	0.25 max. (0.030")	W
Derating to Zero at 150 °C	0.4 max. (0.050")	VV
5 x Rated Power Short-Time Overload, + 25 °C, 5 s	+ 0.25 max. ΔR/R	%

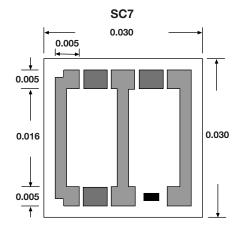
Note

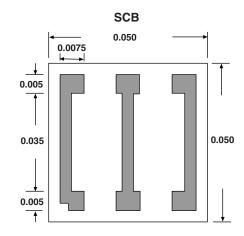
Performance characteristics are not guaranteed once user trimmed



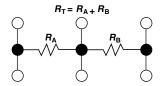
Vishay Electro-Films

DIMENSIONS in inches

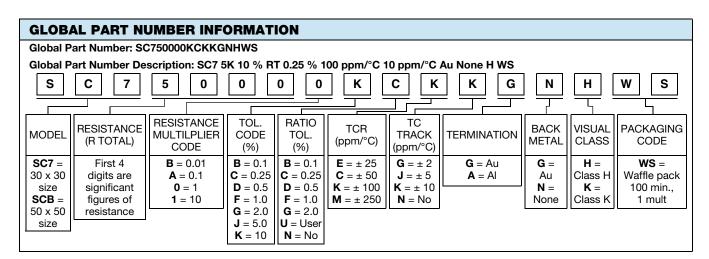




SCHEMATIC



MECHANICAL SPECIFICATIONS		
PARAMETER	VALUE	
Chip Size	0.030" x 0.030" ± 0.003" (0.76 mm x 0.76 mm ± 0.076 mm) 0.050" x 0.050" ± 0.003" (1.27 mm x 1.27 mm ± 0.076 mm)	
Chip Thickness	0.010" ± 0.002" (0.25 mm ± 0.05 mm)	
Chip Substrate Material	Oxidized silicon, 10 kÅ minimum SiO ₂	
Resistor Material	Nichrome	
Bonding Pad Size	0.005" x 0.005" (0.127 mm x 0.127 mm) min.	
Number of Pads	6	
Pad Material	15 kÅ minimum gold standard	
Backing	None, lapped semiconductor silicon	





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Vishay

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