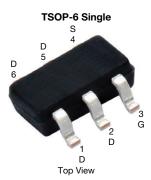


Vishay Siliconix

P-Channel 30 V (D-S) MOSFET



Marking code: BD

| PRODUCT SUMMARY | | | | | | |
|---|--------|--|--|--|--|--|
| V _{DS} (V)+ | -30 | | | | | |
| $R_{DS(on)}$ max. (Ω) at V_{GS} = -10 V | 0.165 | | | | | |
| $R_{DS(on)}$ max. (Ω) at V_{GS} = -4.5 V | 0.276 | | | | | |
| Q _g typ. (nC) | 2.4 | | | | | |
| I _D (A) ^a | -3.4 | | | | | |
| Configuration | Single | | | | | |

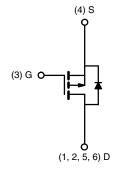
FEATURES

- TrenchFET® power MOSFET
- 100 % R_g tested
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912



APPLICATIONS

- Mobile computing
 - Load switch
 - DC/DC converters



P-Channel MOSFET

| ORDERING INFORMATION | |
|---------------------------------|-----------------|
| Package | TSOP-6 |
| Lead (Pb)-free and halogen-free | Si3453DV-T1-GE3 |

| PARAMETER | | SYMBOL | LIMIT | UNIT | |
|--|------------------------|-----------------------------------|----------------------|------|--|
| Drain-source voltage | | V _{DS} | -30 | V | |
| Gate-source voltage | | V _{GS} | ± 20 | | |
| | T _C = 25 °C | | -3.4 | | |
| O-ation | T _C = 70 °C | | -2.7 | | |
| Continuous drain current (T _J = 150 °C) | T _A = 25 °C | I _D | -2.5 ^{b, c} | | |
| | T _A = 70 °C | | -2 ^{b, c} | А | |
| Pulsed drain current (t = 100 µs) | | I _{DM} | -6 | | |
| Continuous autorio dia da comuna | T _C = 25 °C | | -2.5 | | |
| Continuous source-drain diode current | T _A = 25 °C | I _S | -1.3 ^{b, c} | | |
| | T _C = 25 °C | | 3 | | |
| Maximum power dissipation | T _C = 70 °C | _ | 1.9 | W | |
| | T _A = 25 °C | P _D | 1.6 ^{b, c} | | |
| | T _A = 70 °C | | 1 b, c | | |
| Operating junction and storage temperature range | | T _J , T _{stg} | -55 to +150 | °C | |

| THERMAL RESISTANCE RATINGS | | | | | | | |
|----------------------------------|--------------|------------|---------|---------|------|--|--|
| PARAMETER | | SYMBOL | TYPICAL | MAXIMUM | UNIT | | |
| Maximum junction-to-ambient b, d | t ≤ 5 s | R_{thJA} | 65 | 78 | °C/W | | |
| Maximum junction-to-foot (drain) | Steady state | R_{thJF} | 35 | 42 | C/VV | | |

Notes

- a. Based on T_C = 25 °C
- b. Surface mounted on 1" x 1" FR4 board
- c. t = 5 s
- d. Maximum under steady state conditions is 110 °C/W



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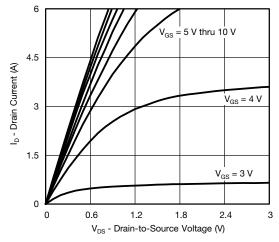
| PARAMETER | SYMBOL | SYMBOL TEST CONDITIONS | | | MAX. | UNIT |
|---|-------------------------|---|------|-------|-------|---------|
| Static | | | | | | • |
| Drain-source breakdown voltage | V_{DS} | $V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$ | -30 | - | - | V |
| V _{DS} temperature coefficient | $\Delta V_{DS}/T_{J}$ | 1 050 A | - | -20 | - | mV/°C |
| V _{GS(th)} temperature coefficient | $\Delta V_{GS(th)}/T_J$ | I _D = -250 μA | - | 3.8 | - | |
| Gate-source threshold voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_{D} = -250 \mu A$ | -1.2 | - | -2.5 | V |
| Gate-source leakage | I _{GSS} | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$ | - | - | ± 100 | nA |
| | I _{DSS} | V _{DS} = -30 V, V _{GS} = 0 V | - | - | -1 | μA |
| Zero gate voltage drain current | | V _{DS} = -30 V, V _{GS} = 0 V, T _J = 85 °C | - | - | -10 | |
| On-state drain current ^a | I _{D(on)} | $V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$ | -6 | - | - | Α |
| Due in account on atota and interest 2 | _ | V _{GS} = -10 V, I _D = -2.5 A | - | 0.137 | 0.165 | |
| Drain-source on-state resistance ^a | R _{DS(on)} | $V_{GS} = -4.5 \text{ V}, I_D = -1.9 \text{ A}$ | - | 0.230 | 0.276 | Ω |
| Forward transconductance a | 9 _{fs} | $V_{DS} = -15 \text{ V}, I_D = -2.5 \text{ A}$ | - | 3 | - | S |
| Dynamic ^b | | | | | | • |
| Input capacitance | C _{iss} | | - | 155 | - | pF |
| Output capacitance | C _{oss} | $V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$ | - | 35 | - | |
| Reverse transfer capacitance | C _{rss} | | - | 25 | - | 1 |
| Total gate charge | Qg | $V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -2.5 \text{ A}$ | - | 4.5 | 6.8 | |
| | | | - | 2.4 | 3.6 | |
| Gate-source charge | Q _{gs} | $V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -2.5 \text{ A}$ | - | 1.2 | =. | nC |
| Gate-drain charge | Q _{gd} | | - | 0.8 | =. | |
| Gate resistance | R_g | f = 1 MHz | 1.5 | 7.3 | 14.6 | Ω |
| Turn-on delay time | t _{d(on)} | | - | 4 | 8 | |
| Rise time | t _r | V_{DD} = -15 V, R_L = 7.5 Ω | - | 9 | 18 | 1 |
| Turn-off delay time | t _{d(off)} | $I_D\cong$ -2 A, $V_{GEN}=$ -10 V, $R_g=$ 1 Ω | - | 11 | 18 | 1 |
| Fall time | t _f | | - | 7 | 14 | 1 |
| Turn-on delay time | t _{d(on)} | | - | 33 | 50 | ns - |
| Rise time | t _r | V_{DD} = -15 V, R_L = 7.5 Ω | - | 21 | 32 | |
| Turn-off delay time | t _{d(off)} | $I_D \cong -2$ A, $V_{GEN} = -4.5$ V, $R_g = 1$ Ω | - | 10 | 20 | |
| Fall time | t _f | | - | 9 | 18 | |
| Drain-Source Body Diode Characteristic | cs | | | 1 | | |
| Continuous source-drain diode current | I _S | T _C = 25 °C | - | - | -2.5 | |
| Pulse diode forward current ^a | I _{SM} | | - | - | -6 | A |
| Body diode voltage | V _{SD} | I _S = -2 A | _ | -0.84 | -1.2 | V |
| Body diode reverse recovery time | t _{rr} | | - | 14 | 21 | ns |
| Body diode reverse recovery charge | Q _{rr} | $I_F = -2 \text{ A, di/dt} = 100 \text{ A/}\mu\text{s,}$ | - | 7 | 14 | nC |
| Reverse recovery fall time | ta | $T_{\rm J} = 25 ^{\circ}{\rm C}$ | - | 10 | - | _ |
| · · · · · · · · · · · · · · · · · · · | α | · · · · · · · · · · · · · · · · · · · | | | | ns |

Notes

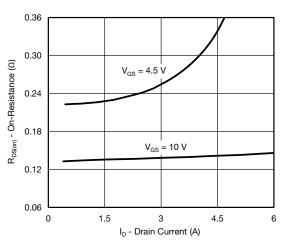
- a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

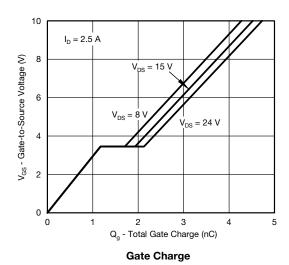


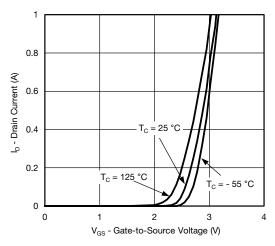


Output Characteristics

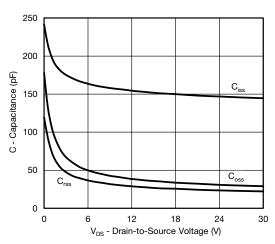


On-Resistance vs. Drain Current and Gate Voltage

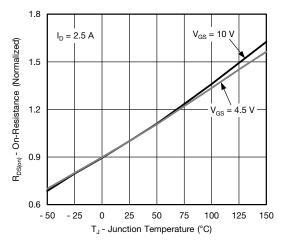




Transfer Characteristics

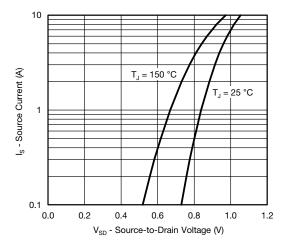


Capacitance

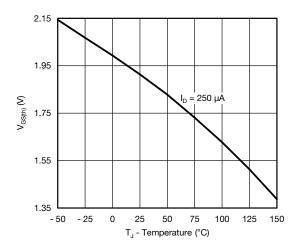


On-Resistance vs. Junction Temperature

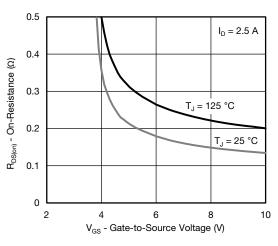




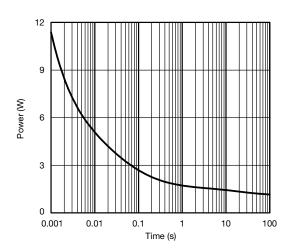
Source-Drain Diode Forward Voltage



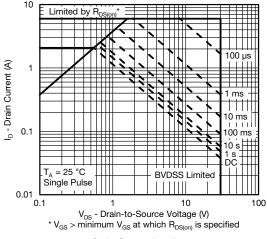
Threshold Voltage



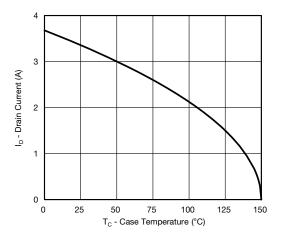
On-Resistance vs. Gate-to-Source Voltage



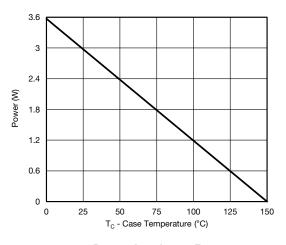
Single Pulse Power

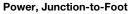


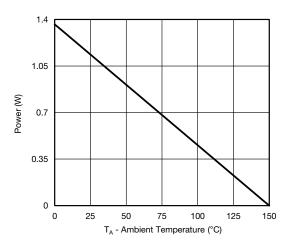




Current Derating a





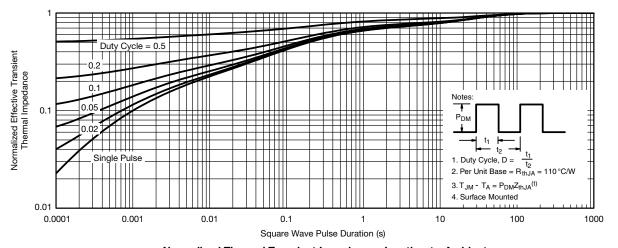


Power, Junction-to-Ambient

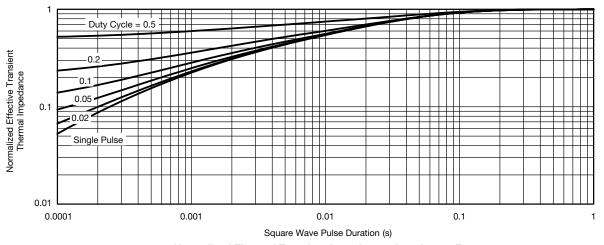
Note

a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit





Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

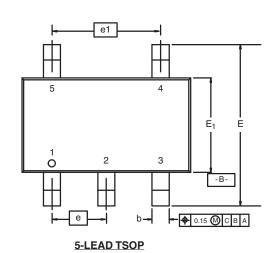
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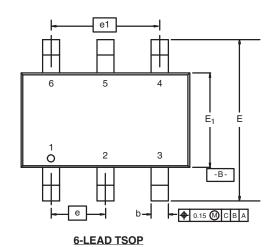


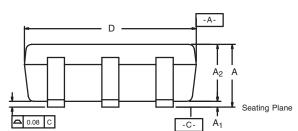


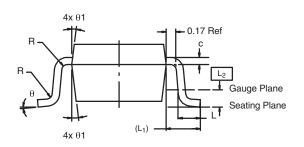
TSOP: 5/6-LEAD

JEDEC Part Number: MO-193C









| | MIL | LIMETER | RS | INCHES | | | |
|---|----------|----------|------|------------|-------|-------|--|
| Dim | Min | Nom | Max | Min | Nom | Max | |
| Α | 0.91 | - | 1.10 | 0.036 | - | 0.043 | |
| A ₁ | 0.01 | - | 0.10 | 0.0004 | - | 0.004 | |
| A ₂ | 0.90 | - | 1.00 | 0.035 | 0.038 | 0.039 | |
| b | 0.30 | 0.32 | 0.45 | 0.012 | 0.013 | 0.018 | |
| С | 0.10 | 0.15 | 0.20 | 0.004 | 0.006 | 0.008 | |
| D | 2.95 | 3.05 | 3.10 | 0.116 | 0.120 | 0.122 | |
| Е | 2.70 | 2.85 | 2.98 | 0.106 | 0.112 | 0.117 | |
| E ₁ | 1.55 | 1.65 | 1.70 | 0.061 | 0.065 | 0.067 | |
| е | | 0.95 BSC | | 0.0374 BSC | | | |
| e ₁ | 1.80 | 1.90 | 2.00 | 0.071 | 0.075 | 0.079 | |
| L | 0.32 | - | 0.50 | 0.012 | - | 0.020 | |
| L ₁ | 0.60 Ref | | | 0.024 Ref | | | |
| L ₂ | 0.25 BSC | | | 0.010 BSC | | | |
| R | 0.10 | - | - | 0.004 | - | - | |
| θ | 0° | 4° | 8° | 0° | 4° | 8° | |
| θ1 | | 7° Nom | | 7° Nom | | | |
| ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540 | | | | | | | |

Document Number: 71200 18-Dec-06

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