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Vishay Siliconix

Automotive P-Channel 30 V (D-S) 175 °C MOSFET



PRODUCT SUMMARY					
V _{DS} (V)	-30				
$R_{DS(on)}(\Omega)$ at $V_{GS} = -10 \text{ V}$	0.018				
$R_{DS(on)}(\Omega)$ at $V_{GS} = -4.5 \text{ V}$	0.031				
I _D (A)	-15				
Configuration	Single				

FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified d
- 100 % Rg and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





ROHS COMPLIANT HALOGEN FREE

G D
P-Channel MOSFET

ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free and halogen-free	SQ4435EY (for detailed order number please see www.vishay.com/doc?79771)

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-source voltage		V_{DS}	-30	V	
Gate-source voltage		V _{GS}	± 20	V	
Continuous drain current	T _C = 25 °C	I _D	-15		
	T _C = 125 °C		-8.7		
Continuous source current (diode conduction)		I _S	-6.2	Α	
Pulsed drain current ^a		I _{DM}	-60		
Single pulse avalanche current	L = 0.1 mH	I _{AS}	-25		
Single pulse avalanche energy	L = 0.1 Min	E _{AS}	31	mJ	
Maximum power dissipation ^a	T _C = 25 °C	D ₋	6.8	W	
	T _C = 125 °C	P_{D}	2.3	VV	
Operating junction and storage temperature range		T _J , T _{stg}	-55 to + 175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-ambient F	PCB mount b	R _{thJA}	85	°C/W	
Junction-to-case (drain)		R_{thJF}	22	- C/VV	

Notes

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- b. When mounted on 1" square PCB (FR-4 material)
- c. Parametric verification ongoing



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0, I _D = -250 μA		-30			V
Gate-source threshold voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_D = -250 \mu A$		-2.0	-2.5	V
Gate-source leakage	I _{GSS}	V _{DS} =	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	± 100	nA
		V _{GS} = 0 V	V _{DS} = -30 V	-	-	-1	μA
Zero gate voltage drain current	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = -30 V, T _J = 125 °C	-	-	-50	
		$V_{GS} = 0 V$	V _{DS} = -30 V, T _J = 175 °C	-	-	-150	
On-state drain current ^a	I _{D(on)}	V _{GS} = -10 V	V _{DS} ≤ -5 V	-30		-	Α
		V _{GS} = -10 V	I _D = -8 A	-	0.013	0.018	
Drain actives on state registeres a		V _{GS} = -10 V	I _D = -8 A, T _J = 125 °C	-		0.026	
Drain-source on-state resistance a	R _{DS(on)}	V _{GS} = -10 V	I _D = -8 A, T _J = 175 °C	-	-	0.030	Ω
	$V_{GS} = -4.5 \text{ V}$ $I_D = -6 \text{ A}$	-	0.023	0.031			
Forward transconductance b	9 _{fs}	V _{DS}	= -15 V, I _D = -8 A	-	22	-	S
Dynamic ^b							
Input capacitance	C _{iss}			-	1736	2170	pF
Output capacitance	C _{oss}	$V_{GS} = 0 V$	$V_{DS} = -15 \text{ V}, f = 1 \text{ MHz}$	-	392	490	
Reverse transfer capacitance	C _{rss}			-	268	335	
Total gate charge ^c	Qg			-	38.3	58	
Gate-source charge c	Q _{gs}	V _{GS} = -10 V	$V_{DS} = -15 \text{ V}, I_{D} = -4.6 \text{ A}$	-	5.9	-	nC
Gate-drain charge ^c	Q _{gd}			-	9	-	
Gate resistance	R _g	f = 1 MHz		2	-	7	Ω
Turn-on delay time ^c	t _{d(on)}			-	12.5	19	
Rise time ^c	t _r	V_{DD} = -15 V, R_L = 15 Ω $I_D \cong$ -1 A, V_{GEN} = -10 V, R_g = 1 Ω		-	9	15	
Turn-off delay time °	t _{d(off)}			-	45.3	68	ns -
Fall time ^c	t _f			-	10	15	
Source-Drain Diode Ratings and Char	acteristics ^b						
Pulsed current ^a	I _{SM}			ı	=	-60	Α
Forward voltage	V_{SD}	I _F = -8 A, V _{GS} = 0		-	-0.84	-1.2	V

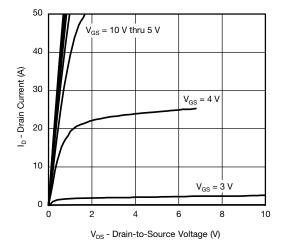
Notes

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

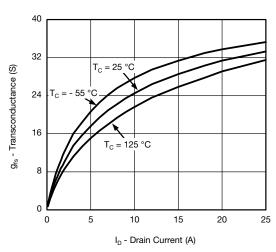
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



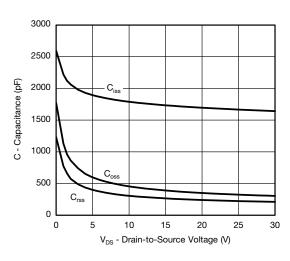
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



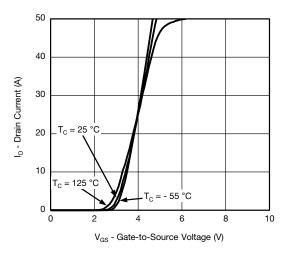
Output Characteristics



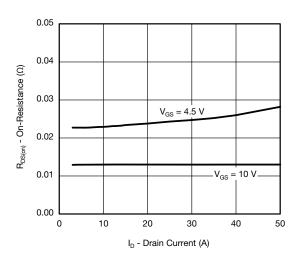
Transconductance



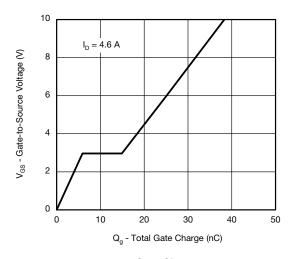
Capacitance



Transfer Characteristics



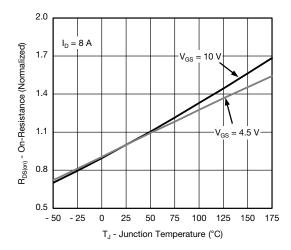
On-Resistance vs. Drain Current



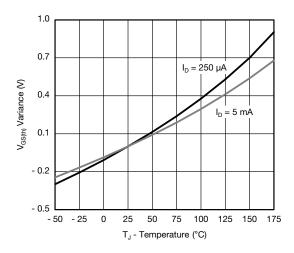
For technical questions, contact: automostechsu



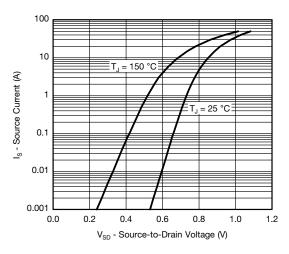
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



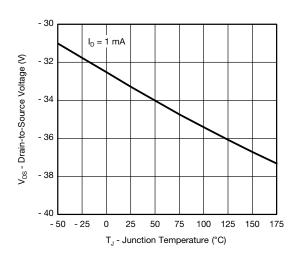
On-Resistance vs. Junction Temperature



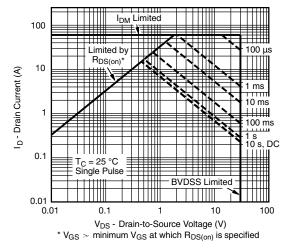
Threshold Voltage



Source Drain Diode Forward Voltage



Drain Source Breakdown vs. Junction Temperature

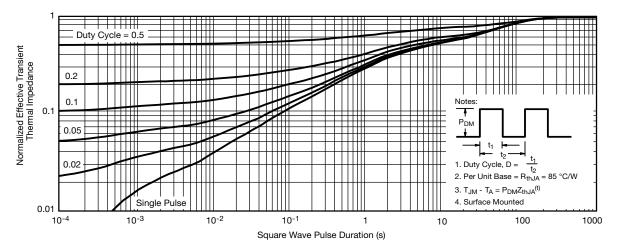


Safe Operating Area

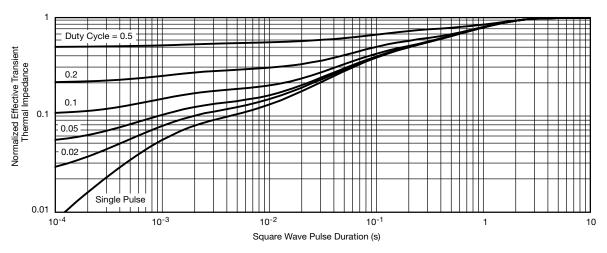
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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

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SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIM	IETERS	INCHES		
DIM	Min	Max	Min	Max	
Α	1.35	1.75	0.053	0.069	
A ₁	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
Е	3.80	4.00	0.150	0.157	
е	1.27	BSC	0.050 BSC		
Н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	
FCN: C-06527-Bey 11-Sen-06					

DWG: 5498

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RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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