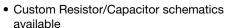


Thick Film Resistor/Capacitor Networks, Single-In-Line, Conformal Coated SIP



FEATURES

- · Isolated and bussed schematics available
- · Thick film resistors
- NP0 or X7R capacitors for line terminator
- Wide operating temperature range (- 55 °C to 125 °C)





 Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

Note

* Lead (Pb)-containing terminations are not RoHS-compliant. Exemptions may apply.

STANDARD ELECTRICAL SPECIFICATIONS											
MODEL	SCHEMATIC	RESISTOR CHARACTERISTICS				CAPACITOR CHARACTERISTICS					
		POWER RATING P _{70 °C} W	RESISTANCE RANGE Ω	RESISTANCE TOLERANCE (1) ± %	TEMP. COEFF. ± ppm/°C	TYPE (2)	CAPACITANCE RANGE	CAPACITANCE TOLERANCE (3) ± %	CAPACITANCE VOLTAGE V _{DC}		
TRC	01	0.20	10 to 1M	1, 2, 5	150	NP0	33 pF to 3900 pF	10, 20	50		
						X7R	470 pF to 0.1 μF	10, 20			
	02	0.20	10 to 1M	1, 2, 5	150	NP0	33 pF to 3900 pF	10, 20	- 50		
						X7R	470 pF to 0.1 μF	10, 20			
	09	0.20	10 to 1M	1, 2, 5	150	NP0	33 pF to 3900 pF	10, 20	- 50		
						X7R	470 pF to 0.1 μF	10, 20			

Notes

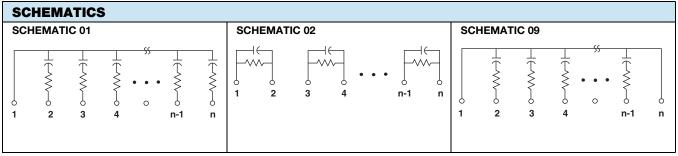
- $^{(1)}$ ± 2 % standard, ± 1 % and ± 5 % available
- (2) NP0 capacitors may be substituted for X7R capacitors
- (3) Tighter tolerances available on request

GLOBAL PART NUMBER INFORMATION												
New Global Part Numbering: TRC0801N101J560KTB (preferred part number format)												
	T R	C 0	8 0 1	_ N 1	0 1	J 5	6 0	K	В			
GLOBAL MODEL	PIN COUNT	SCHEMATIC	CHARACTERISTICS	RESISTANCE VALUE	RESISTANCE TOLERANCE	CAPACITANCE VALUE	CAPACITANCE TOLERANCE	TERMINAL FINISH	PACKAGING			
TRC	06 to 12		N = NP0 X = X7R	significant figure, $\mathbf{G} = 2 \%$		(In picofarads) 2 digit significant	K = 10 % M = 20 %	T = Sn90/Pb10 C =	B = Bulk			
	06 = 6 pin 08 =			followed by a multiplier $101 = 100 \Omega$		figure, followed by a multiplier		Sn95.5/ Ag3.9/ Cu0.6				
	8 pin 12 = 12 pin			220 = 22 Ω 102 = 1 kΩ		101 = 100 pF 392 = 3000 pF 104 = 0.1 μF						
Historical Part Numbering: TRC0801101J560KS10 (will continue to be accepted)												
HISTORICAL PIN SCHEMATIC RESIST					J ESISTANCE OLERANCE	560 CAPACITANC VALUE	E CAPACITA TOLERAI		S10 ERMINAL FINISH			

Notes

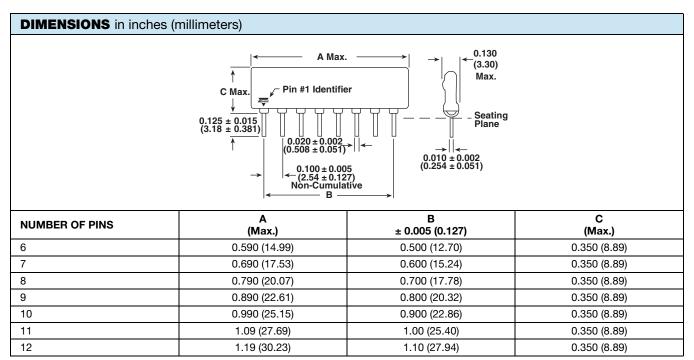
For additional information on packaging, refer to the Through-hole Network Packaging document (www.vishay.com/doc?31542).





Note

· Custom schematics available



Note

· Other sizes available



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