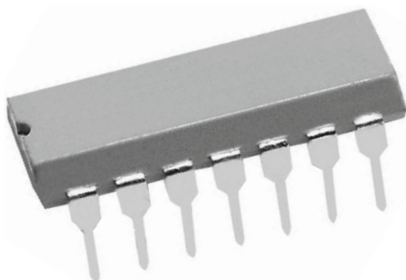


# Thick Film Resistor Networks, Dual-In-Line, Molded DIP



## FEATURES

- 10 bit, R/2R ladder networks for D/A and A/D converter with bi-polar or CMOS switches
- 0.190" (4.83 mm) maximum seated height
- Rugged, molded case construction
- Thick film resistive elements
- Low temperature coefficient (-55 °C to 125 °C) ± 100 ppm/°C
- Reduces total assembly costs
- Compatible with automatic inserting equipment
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)


**RoHS\***  
Available

## Note

\* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non-RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details.

## STANDARD ELECTRICAL SPECIFICATIONS

GLOBAL MODEL	SCHEMATIC	POWER RATING ELEMENT $P_{70\text{ }^{\circ}\text{C}}$ W	POWER RATING PACKAGE $P_{70\text{ }^{\circ}\text{C}}$ W	RESISTANCE RANGE <sup>(1)</sup> $\Omega$	TOLERANCE $\pm \%$	TEMPERATURE COEFFICIENT (0 °C to 70 °C) $\pm \text{ppm}/^{\circ}\text{C}$	LINEARITY (0 °C to 70 °C)
T14L	10	0.050	1.6	50 to 1M	2	100	± 1 LSB

## Note

<sup>(1)</sup> 25K, 50K, and 100K are standard, other values available on special order

## GLOBAL PART NUMBER INFORMATION

New Global Part Numbering: T14L10100KT (preferred part number format)

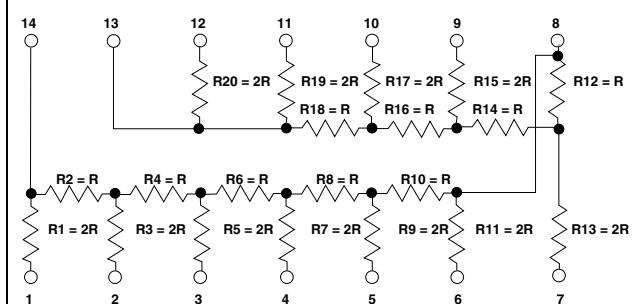
T	1	4	L	1	0	1	0	0	K	T	T
GLOBAL MODEL		SCHEMATIC		RESISTANCE VALUE (R)				TERMINAL FINISH		PACKAGING	
T14L		10		R = $\Omega$ K = k $\Omega$ M = M $\Omega$ <b>5K00 = 5 k<math>\Omega</math></b> <b>5K10 = 5.1 k<math>\Omega</math></b> <b>100K = 100 k<math>\Omega</math></b> <b>Reference schematic</b> <b>if R = 5 k<math>\Omega</math>, then 2R = 10 k<math>\Omega</math></b> <b>if R = 100 k<math>\Omega</math>, then 2R = 200 k<math>\Omega</math></b>				<b>T</b> = Sn90/Pb10 <b>C</b> = Sn95.5/Ag3.9/Cu0.6		<b>T</b> = Tube	

Historical Part Numbering: T14L10104S10 (will continue to be accepted)

T14L	10	104	S10
HISTORICAL MODEL	NUMBER OF BITS	RESISTANCE VALUE (R)	TERMINAL FINISH

## Note

<sup>(1)</sup> For additional information on packaging, refer to the "Through-Hole Network Packaging" document ([www.vishay.com/doc?31542](http://www.vishay.com/doc?31542))

**SCHEMATIC**

**RATIO MATCH TOLERANCE**

$$R1/R2 = 2 \% \pm 1 \%$$

$$R1/R3 = 1 \% \pm 1 \%$$

$$R1/R4 = 2 \% \pm 1 \%$$

$$R1/R5 = 1 \% \pm 1 \%$$

$$R1/R6 = 2 \% \pm 1 \%$$

$$R1/R7 = 1 \% \pm 1 \%$$

$$R1/R8 = 2 \% \pm 1 \%$$

$$R9/R10 = 2 \% \pm 0.5 \%$$

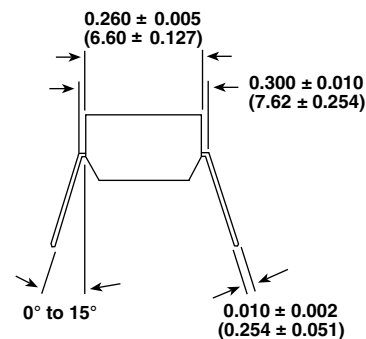
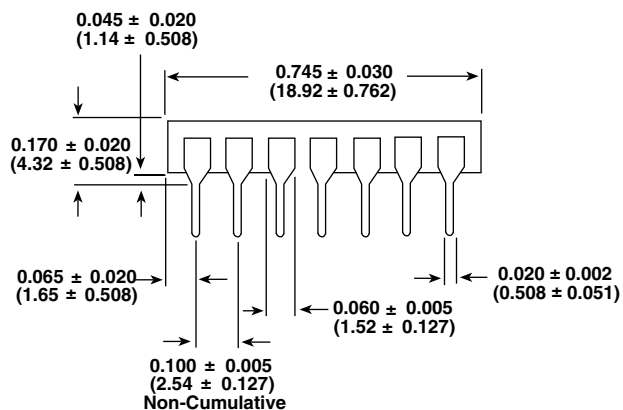
$$R11/R12 = 2 \% \pm 0.4 \%$$

$$R13/R14 = 2 \% \pm 0.2 \%$$

$$R15/R16 = 2 \% \pm 0.2 \%$$

$$R19/R17 = 1 \% \pm 0.1 \%$$

$$R19/R18 = 2 \% \pm 0.1 \%$$

**DIMENSIONS** in inches (millimeters)




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