### **Si2307CDS**

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RoHS COMPLIANT

HALOGEN FREE



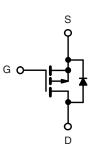
### P-Channel 30 V (D-S) MOSFET

#### **FEATURES**

- TrenchFET<sup>®</sup> power MOSFET
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **APPLICATIONS**

· Load switch for portable devices



P-Channel MOSFET

Marking code: N7

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	-30				
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS}$ = -10 V	0.088				
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS}$ = -4.5 V	0.138				
Q <sub>g</sub> typ. (nC)	4.1				
I <sub>D</sub> (A) <sup>a, b</sup>	-3.5				
Configuration	Single				

### ORDERING INFORMATION

Package	SOT-23			
Lead (Pb)-free	Si2307CDS-T1-E3			
Lead (Pb)-free and halogen-free	Si2307CDS-T1-GE3			

ABSOLUTE MAXIMUM RATINGS $T_A$	= 25 °C, unless	otherwise note	ed		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V <sub>DS</sub>	-30	v	
Gate-source voltage		V <sub>GS</sub>	± 20	v	
	T <sub>C</sub> = 25 °C		-3.5		
Continuous drain surrent (T 150 °C) a b	T <sub>C</sub> = 70 °C		-2.8		
Continuous drain current (T <sub>J</sub> = 150 °C) <sup>a, b</sup>	T <sub>A</sub> = 25 °C	I <sub>D</sub>	-2.7 <sup>a, b</sup>		
	T <sub>A</sub> = 70 °C		-2.2 <sup>a, b</sup>	А	
Pulsed drain current (10 µs pulse width)		I <sub>DM</sub>	-12		
Continuous source-drain diode current <sup>a, b</sup>	T <sub>C</sub> = 25 °C	1	-1.5		
	T <sub>A</sub> = 25 °C	I <sub>S</sub> –	-0.91 <sup>a, b</sup>		
Maximum power dissipation <sup>a, b</sup>	T <sub>C</sub> = 25 °C		1.8		
	T <sub>C</sub> = 70 °C		1.14	w	
	T <sub>A</sub> = 25 °C	P <sub>D</sub> -	1.1 <sup>a, b</sup>	vv	
	T <sub>A</sub> = 70 °C		0.7 <sup>a, b</sup>	1	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C	
Soldering recommendations (peak temperature) <sup>c</sup>			260		

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum junction-to-ambient a, c	t ≤ 5 s	R <sub>thJA</sub>	90	115	°C/W	
Maximum junction-to-foot (drain)	Steady state	R <sub>thJF</sub>	55	70	0/10	

#### Notes

a. Surface mounted on 1" x 1" FR4 board

b. t = 5 s

c. Maximum under steady state conditions is 166 °C/W

S-81580-Rev. A, 07-Jul-08

1

Document Number: 68768

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static				•			
Drain-source breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = -250 \mu A$	-30	-	-	V	
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$	L 050 A	-	-32	-	mV/°C	
V <sub>GS(th)</sub> temperature coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = -250 μΑ	-	4.5	-		
Gate-source threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -250 \ \mu A$	-1	-	-3	V	
Gate-source leakage	I <sub>GSS</sub>	$V_{DS}$ = 0 V, $V_{GS}$ = ± 20 V	-	-	-100	nA	
Zero gate voltage drain current	1	$V_{DS}$ = -30 V, $V_{GS}$ = 0 V	-	-	-1		
Zero gate voltage drain current	IDSS	$V_{DS}$ = -30 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 $^{\circ}\text{C}$	-	-	-10	μΑ	
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS}{\leq}5$ V, $V_{GS}{=}{-}10$ V	-6	-	-	Α	
Drain-source on-state resistance <sup>a</sup>	Brack	$V_{GS}$ = -10 V, I <sub>D</sub> = -3.5 A	-	0.073	0.088	Ω	
Drain-source on-state resistance	R <sub>DS(on)</sub>	$V_{GS}$ = -4.5 V, I <sub>D</sub> = -2.5 A	-	0.110	0.138		
Forward transconductance a	g <sub>fs</sub>	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -3.5 \text{ A}$	-	7	-	S	
Dynamic <sup>b</sup>							
Input capacitance	C <sub>iss</sub>		-	340	-	pF	
Output capacitance	C <sub>oss</sub>	$V_{DS}$ = -15 V, $V_{GS}$ = 0 V, f = 1 MHz	-	67	-		
Reverse transfer capacitance	C <sub>rss</sub>		-	51	-		
Total gate charge	Qg		-	4.1	6.2	nC	
Gate-source charge	Q <sub>gs</sub>	$V_{DS}$ = -15 V, $V_{GS}$ = -4.5 V, $I_{D}$ = -2.5 A	-	1.3	-		
Gate-drain charge	Q <sub>gd</sub>		-	1.8	-		
Gate resistance	R <sub>g</sub>	f = 1 MHz	-	10	-	Ω	
Turn-on delay time	t <sub>d(on)</sub>		-	40	60	_	
Rise time	tr	$V_{DD}$ = -15 V, $R_L$ = 15 $\Omega$	-	40	60		
Turn-off delay time	t <sub>d(off)</sub>	$I_D\cong$ -1 A, $V_{GEN}$ = -4.5 V, $R_g$ = 1 $\Omega$	-	20	40		
Fall time	t <sub>f</sub>		-	17	30	ns	
Turn-on delay time	t <sub>d(on)</sub>		-	5.5	10	115	
Rise time	t <sub>r</sub>	$V_{DD}$ = -15 V, $R_L$ = 15 $\Omega$	-	13	25	-	
Turn-off delay time	t <sub>d(off)</sub>	$I_D\cong$ -1 A, $V_{GEN}$ = -10 V, $R_g$ = 1 $\Omega$	-	17	30		
Fall time	t <sub>f</sub>			7.7	15		
Drain-Source Body Diode Characteris	stics						
Continuous source-drain diode current	I <sub>S</sub>	$T_{C} = 25 \ ^{\circ}C$	-	-	-1.5	А	
Pulse diode forward current	I <sub>SM</sub>		-	-	-12	~	
Body diode voltage	V <sub>SD</sub>	$I_{\rm S}$ = -0.75 A, $V_{\rm GS}$ = 0 V	-	-0.8	-1.2	V	
Body diode reverse recovery time	t <sub>rr</sub>		-	17	30	ns	
Body diode reverse recovery charge	Q <sub>rr</sub>	I <sub>F</sub> = -2.5 A, di/dt = 100 A/μs,	-	11	20	nC	
Reverse recovery fall time	t <sub>a</sub>	$T_J = 25 \ ^{\circ}C$	-	12	-	25	
Reverse recovery rise time	t <sub>b</sub>		-	5	-	ns	

Notes

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %

b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

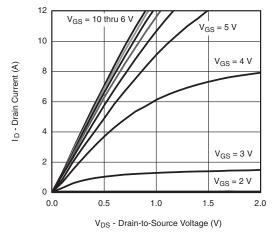
2

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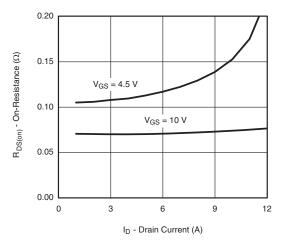


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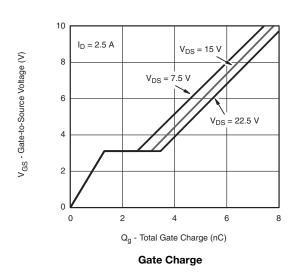
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

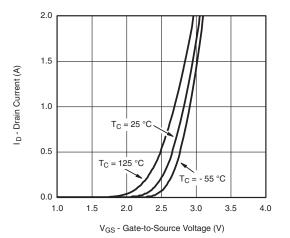


**Output Characteristics** 

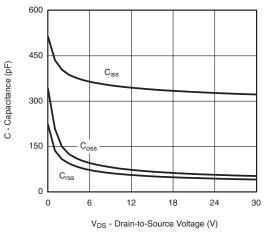


**On-Resistance vs. Drain Current and Gate Voltage** 

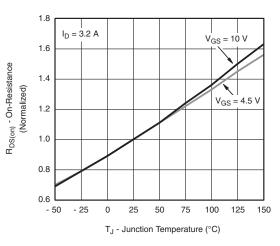




Transfer Characteristics







**On-Resistance vs. Junction Temperature** 

S-81580-Rev. A, 07-Jul-08

3

Document Number: 68768

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T<sub>J</sub> = 125 °C

T<sub>J</sub> = 25 °C

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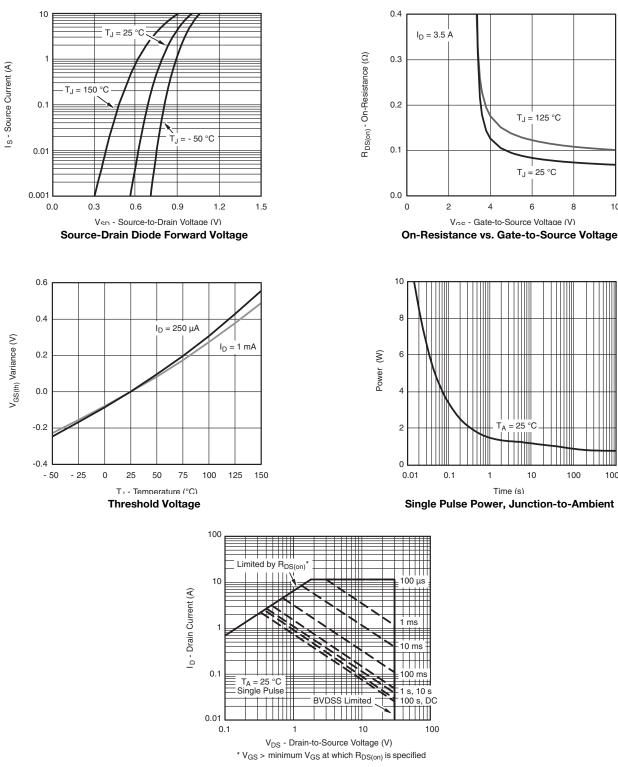
100

1000

8

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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

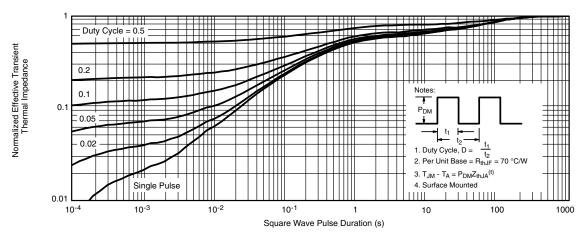


Safe Operating Area, Junction-to-Ambient

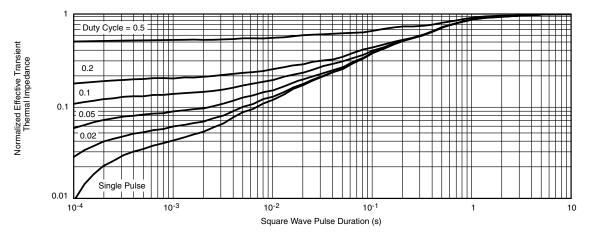


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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?68768.

5



## Package Information

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### SOT-23 (TO-236): 3-LEAD







Dim	MILLIN	METERS	INCHES		
	Min	Max	Min	Мах	
Α	0.89	1.12	0.035	0.044	
A <sub>1</sub>	0.01	0.10	0.0004	0.004	
A <sub>2</sub>	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E <sub>1</sub>	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e <sub>1</sub>	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L <sub>1</sub>	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	



# Application Note 826

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### **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



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1