



N-Channel 30-V (D-S) MOSFET

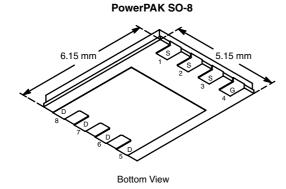
PRODUCT SUMMARY				
V _{DS} (V)	$r_{DS(on)}(\Omega)$	I _D (A)		
30	0.003 @ V _{GS} = 10 V	29		
	0.00425 @ V _{GS} = 4.5 V	25		

FEATURES

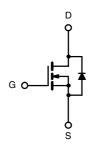
- TrenchFET® Power MOSFET
- PWM Optimized
- New Low Thermal Resistance PowerPAK® Package with Low 1.07-mm Profile
- 100% R_g Tested

APPLICATIONS

- DC/DC Converters
 - Low-Side MOSFET in Synchronous Buck in Desktops
- Secondary Synchronous Rectifier



Ordering Information: Si7880DP-T1



N-Channel MOSFET

Parameter		Symbol	10 secs	Steady State	Unit
Drain-Source Voltage		V _{DS}	30		٧
Gate-Source Voltage		V_{GS}	±20		
Continuous Drain Current (T _J = 150°C) ^a	T _A = 25°C	ID	29	18	
	T _A = 70°C		25	14	
Pulsed Drain Current (10 μs Pulse Width)		I _{DM}	60		Α
Avalanche Current	L = 0.1 mH	I _{AS}	50		
Continuous Source Current (Diode Conduction) ^a		I _S	4.5	1.6	
Maximum Power Dissipation ^a	T _A = 25°C		5.4	1.9	147
	T _A = 70°C	P _D	3.4	1.2	W
Operating Junction and Storage Temperature Range		T _J , T _{stg}	–55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	t ≤ 10 sec	R _{thJA}	18	23		
Maximum Junction-to-Ambient ^a	Steady State		50	65	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	R _{thJC}	1.0	1.5		

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

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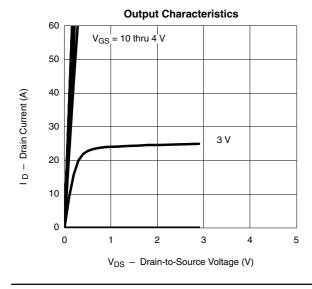


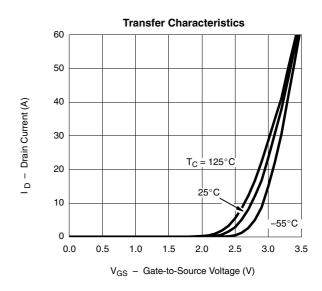
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit	
- i didiliotoi	- Cymbei	Tool Condition		.,,,,	max		
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS},I_D=250\;\mu\text{A}$	1.0		3.0	٧	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$	_{OS} = 24 V, V _{GS} = 0 V		1		
	I _{DSS}	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			5	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α	
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 29 A		0.0025	0.0030		
		$V_{GS} = 4.5 \text{ V}, I_D = 25 \text{ A}$		0.0035	0.00425	52	
Forward Transconductance ^a	9fs	$V_{DS} = 6 \text{ V}, I_{D} = 29 \text{ A}$		90		S	
Diode Forward Voltage ^a	V _{SD}	$I_S = 4.5 \text{ A}, V_{GS} = 0 \text{ V}$		0.68	1.1	V	
Dynamic ^b	<u> </u>						
Total Gate Charge	Qg			40.5	60	nC	
Gate-Source Charge	Q_{gs}	V_{DS} = 15 V, V_{GS} = 4.5 V, I_D = 29 A		18			
Gate-Drain Charge	Q_{gd}			10.5			
Gate Resistance	R _g		0.5	1.2	1.8	Ω	
Turn-On Delay Time	t _{d(on)}			30	50		
Rise Time	t _r	V_{DD} = 15 V, R_L = 15 Ω		15	25	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 6 \Omega$		110	200		
Fall Time	t _f			35	60		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 2.9 A, di/dt = 100 A/μs		55	80	1	

Notes

- a. Pulse test; pulse width $\leq 300~\mu s$, duty cycle $\leq 2\%$. b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



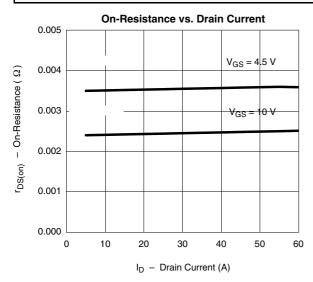


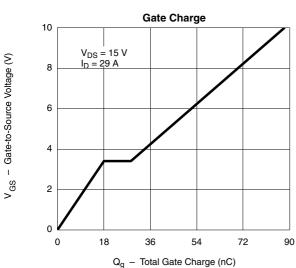


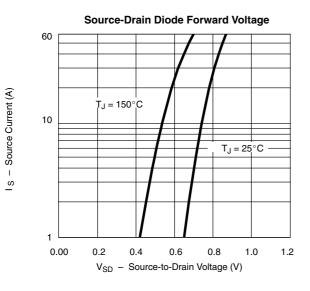


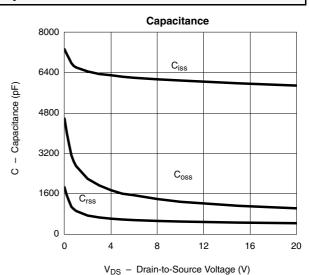
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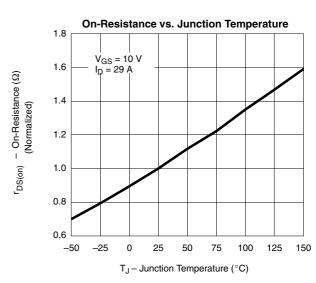
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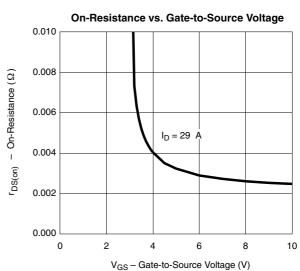








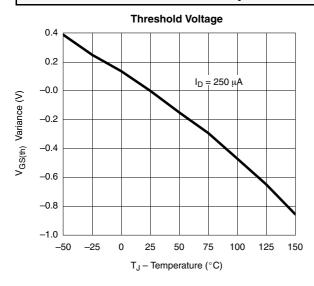


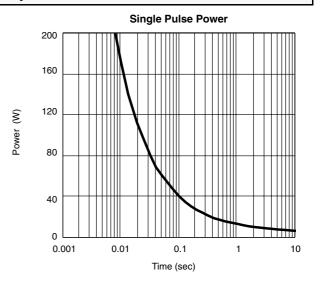


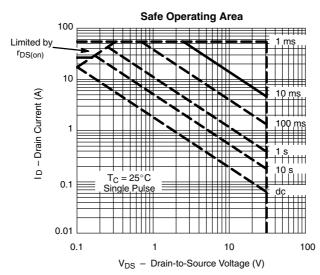
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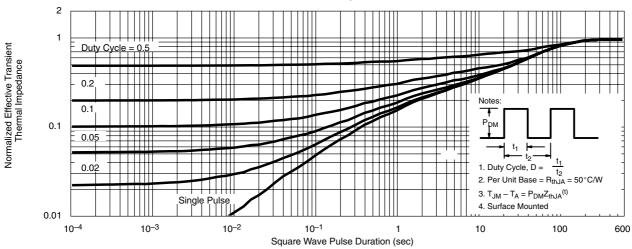
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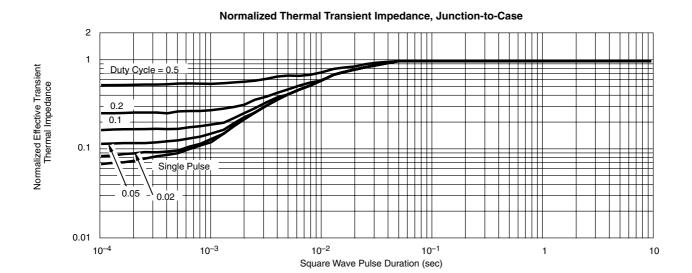


Normalized Thermal Transient Impedance, Junction-to-Ambient





TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



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