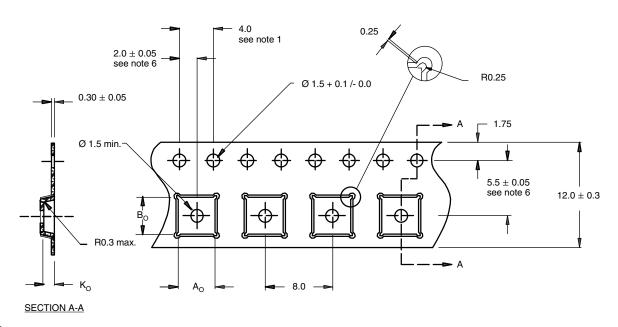
Vishay Siliconix

## PowerPAK® MLP(QFN), 4 x 4 / 3 x 3 DFN-10, 3 x 3 MLP33-8 / 10 PPAIR® 3 x 3



## Notes

- $^{(1)}$  10 sprocket hole pitch cumulative tolerance  $\pm$  0.2
- (2) Camber not to exceed 1 mm in 100 mm, also not to exceed 1.5 cm in 1 m actually
- (3) Material: PS + C

DWG: 93-5225-X

- (4) A<sub>0</sub> and B<sub>0</sub> measured as indicated
- (5) K<sub>0</sub> measured from a plane on the inside bottom of the pocket to the top surface of the carrier
- (6) Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole
- (7) All sizes in mm unless specified
- (8) Vishay part number must be labeled at all reels of cover and carrier tape
- (9) Surface resistivity  $10^5$  max.  $\Omega/\text{sq}$

VERSION	PACKAGE	<b>A</b> <sub>0</sub>	B <sub>0</sub>	K <sub>0</sub>
- 1	MLP(QFN), 4 x 4	4.35 ± 0.1	4.35 ± 0.1	1.11 ± 0.1
- 2	MLP(QFN), 3 x 3		3.30 ± 0.1	1.11 ± 0.1
	MLP33-8 / 10	3.30 ± 0.1		
	DFN-10, 3 x 3	3.30 ± 0.1		
	PPAIR, 3 x 3			
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