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# TSOP373..H, TSOP375..H

**Vishay Semiconductors** 

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HALOGEN

(5-2008)

# **IR Receiver Modules for Remote Control Systems**



### LINKS TO ADDITIONAL RESOURCES





#### DESCRIPTION

This IR receiver series is optimized for short burst remote control systems in different environments. The customer can chose between different IC settings (AGC variants), to find the optimum solution for his application. The higher the AGC, the better noise is suppressed, but the lower the code compatibility.

The devices contain a PIN diode and a preamplifier assembled on a lead frame. The epoxy package contains an IR filter. The demodulated output signal can be directly connected to a microprocessor for decoding. These components have not been qualified to automotive specifications.

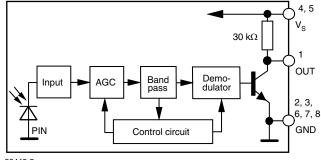
#### **FEATURES**

- Individual IC settings to reach maximum performance
- Immunity against noise (lamps, LCD TV, Wi-Fi)
- Low supply current
- RoHS · Photo detector and preamplifier in one package COMPLIANT
- Supply voltage: 2.0 V to 5.5 V
- FREE · Material categorization: GREEN for definitions of compliance please see www.vishay.com/doc?99912

#### **DESIGN SUPPORT TOOLS**

- 3D models
- Window size calculator

#### **BLOCK DIAGRAM**



20445-5

### Datasheet Values Refer to PCN-OPT-1308-2024



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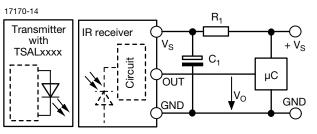
TSOP37...TT1 - top view taped, 1800 pcs/reel

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ORDERING CODE

Taping:

**APPLICATION CIRCUIT** 



 $\mathsf{R}_1$  and  $\mathsf{C}_1$  recommended in case there are strong ripple or spikes on the supply line.

PARTS TABLE					
AGC		NOISY ENVIRONMENTS AND SHORT BURSTS (AGC3)	VERY NOISY ENVIRONMENTS AND SHORT BURSTS (AGC5)		
	36 kHz	TSOP37336H <sup>(1)(2)</sup>	TSOP37536H		
Carrier	38 kHz	TSOP37338H <sup>(3)(4)(5)</sup>	TSOP37538H		
frequency	40 kHz	TSOP37340H	TSOP37540H		
	56 kHz	TSOP37356H	TSOP37556H		
Package		Belobog shield			
Pinning		1 = OUT; 2, 3, 6, 7, 8 = GND; 4, 5 = V <sub>S</sub>			
Dimensions (mm)		4.3 W x 4.3 H x 1.0 D			
Mounting		SMD			
Application		Remote control			
Best choice for		(1) MCIR (2) RCMM (3) RECS-80 Code (4) r-map (5) XMP			
Special options		Extended temperature range: <u>www.vishay.com/doc?82738</u>			

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Supply voltage		VS	-0.3 to +6	V	
Supply current		I <sub>S</sub>	3	mA	
Output voltage		Vo	-0.3 to (V <sub>S</sub> + 0.3)	V	
Output current		lo	5	mA	
Junction temperature		Tj	100	°C	
Storage temperature range		T <sub>stg</sub>	-25 to +85	°C	
Operating temperature range		T <sub>amb</sub>	-25 to +85	°C	
Power consumption $T_{amb} \le 85 \text{ °C}$		P <sub>tot</sub>	10	mW	

#### Note

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only
and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification
is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.



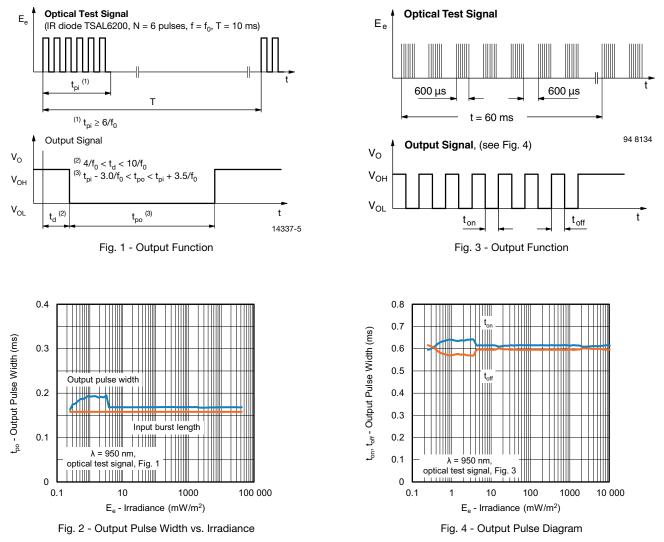
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ELECTRICAL AND OPTICAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply voltage		Vs	2.0	-	5.5	V
Supply current	$V_{S} = 3.3 V, E_{v} = 0$	I <sub>SD</sub>	0.25	0.35	0.45	mA
Supply current	$E_v = 40 \text{ klx}, \text{ sunlight}$	I <sub>SH</sub>	-	0.45	-	mA
Transmission distance	$E_v = 0$ , IR diode TSAL6200, $I_F = 50$ mA, test signal see Fig. 1	d	-	15	-	m
Output voltage low	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2,$ test signal see Fig. 1	V <sub>OSL</sub>	-	-	100	mV
Minimum irradiance	Test signal: RC5 code	E <sub>e min.</sub>	-	0.25	0.5	mW/m <sup>2</sup>
	Test signal: XMP code	E <sub>e min.</sub>	-	0.3	0.6	mW/m <sup>2</sup>
Maximum irradiance	$\label{eq:tpi} \begin{array}{l} t_{pi} \text{ - } 3/f_0 < t_{po} < t_{pi} + 3.5/f_0, \\ \text{test signal see Fig. 1} \end{array}$	E <sub>e max.</sub>	30	-	-	W/m <sup>2</sup>
Directivity	Angle of half transmission distance	φ1/2	-	± 75	-	o





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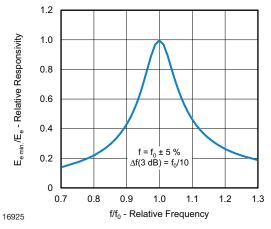
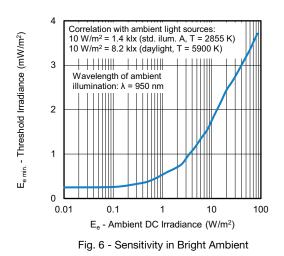


Fig. 5 - Frequency Dependance of Responsivity



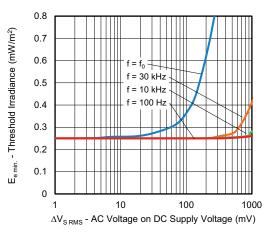


Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

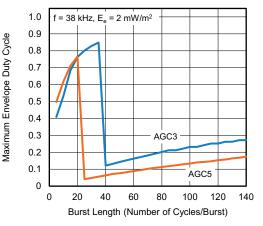
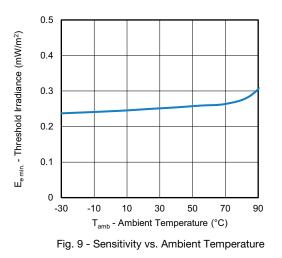


Fig. 8 - Max. Envelope Duty Cycle vs. Burst Length



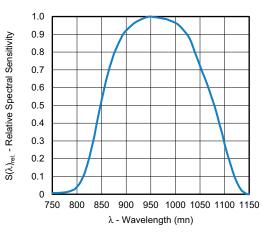


Fig. 10 - Relative Spectral Sensitivity vs. Wavelength

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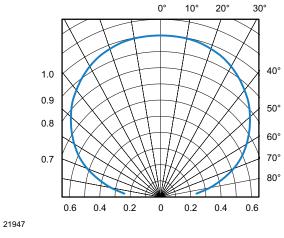
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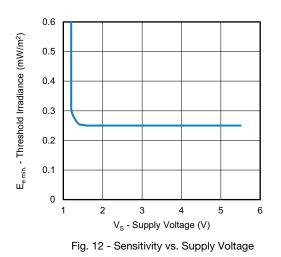
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### Datasheet Values Refer to PCN-OPT-1308-2024



## TSOP373..H, TSOP375..H

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### SUITABLE DATA FORMAT

The TSOP373..H, TSOP375..H series is designed to suppress spurious output pulses due to noise or disturbance signals. The devices can distinguish data signals from noise due to differences in frequency, burst length, and envelope duty cycle. The data signal should be close to the device's band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the TSOP373..H, TSOP375..H in the presence of a disturbance, the sensitivity of the receiver is automatically reduced by the AGC to insure that no spurious pulses are present at the receiver's output. Some examples which are suppressed are:

- DC light (e.g. from tungsten bulbs sunlight)
- · Continuous signals at any frequency
- Strongly or weakly modulated patterns from fluorescent lamps with electronic ballasts (see Fig. 13 or Fig. 14)

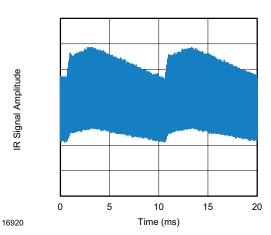


Fig. 13 - IR Signal from Fluorescent Lamp With Low Modulation

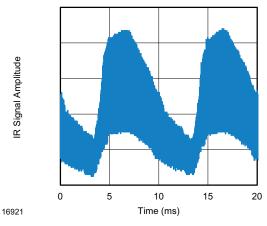


Fig. 14 - IR Signal from Fluorescent Lamp With High Modulation

	TSOP373H	TSOP375H
Minimum burst length	6 cycles/burst	6 cycles/burst
After each burst of length a minimum gap time is required of	6 to 35 cycles ≥ 10 cycles	6 to 20 cycles ≥ 10 cycles
For bursts greater than	35 cycles	20 cycles
a minimum gap time in the data stream is needed of	> 9 x burst length	> 25 x burst length
Maximum number of continuous short bursts/second	2000	2000
MCIR code	Preferred	No
RCMM code	Preferred	Yes
XMP code	Preferred	Yes
RECS-80 code	Preferred	Yes
r-map code	Preferred	Yes
Suppression of interference from fluorescent lamps	Fig. 13 and Fig. 14	Fig. 13 and Fig. 14

#### Note

• For data formats with long bursts (more than 10 carrier cycles) please see the datasheet for TSOP372..H, TSOP374..H

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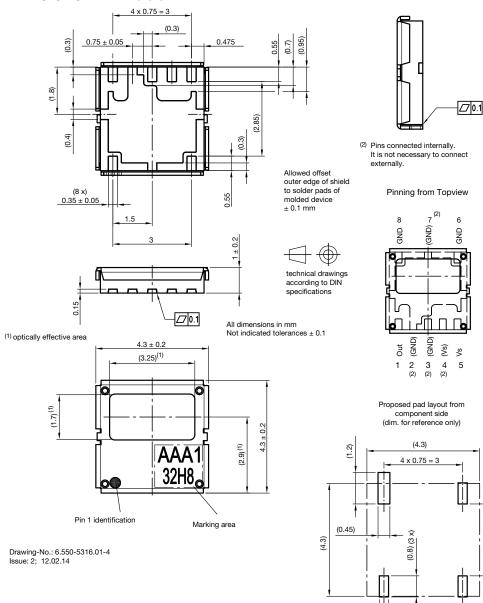


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### **ASSEMBLY INSTRUCTIONS**

#### **Reflow Soldering**

- Reflow soldering must be done within 168 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Exercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

(2.75)

#### Manual Soldering

(3 x)(0.35)

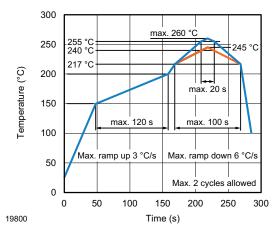
- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- · Handle products only after the temperature has cooled off

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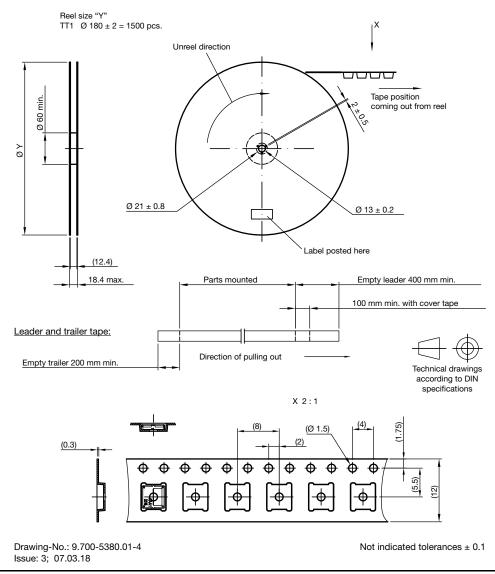
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#### VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE



### TAPING VERSION TSOP37..H DIMENSIONS in millimeters

Tape and reel dimensions:



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### Datasheet Values Refer to PCN-OPT-1308-2024

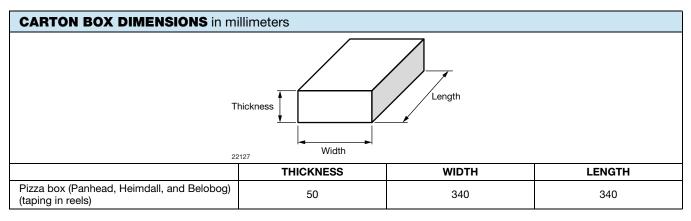


## TSOP373..H, TSOP375..H

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#### **OUTER PACKAGING**

The sealed reel is packed into a pizza box.



#### LABEL

#### Standard bar code labels for finished goods

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

PLAIN WRITING	ABBREVIATION	LENGTH
Item-description	-	18
Item-number	INO	8
Selection-code	SEL	3
LOT-/serial-number	BATCH	10
Data-code	COD	3 (YWW)
Plant-code	PTC	2
Quantity	QTY	8
Accepted by	ACC	-
Packed by	PCK	-
Mixed code indicator	MIXED CODE	-
Origin	xxxxxx+	Company logo
LONG BAR CODE TOP	ТҮРЕ	LENGTH
Item-number	Ν	8
Plant-code	Ν	2
Sequence-number	Х	3
Quantity	Ν	8
Total length	-	21
SHORT BAR CODE BOTTOM	ТҮРЕ	LENGTH
Selection-code	Х	3
Data-code	Ν	3
Batch-number	Х	10
Filter	-	1
Total length	_	17

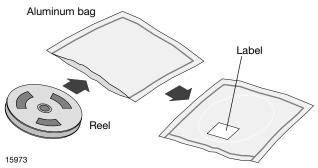
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### **Vishay Semiconductors**

### **DRY PACKING**

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



#### **FINAL PACKING**

The sealed reel is packed into a cardboard box.

#### **RECOMMENDED METHOD OF STORAGE**

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity  $\leq$  60 % RH max.

After more than 168 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40  $^{\circ}\text{C}$  + 5  $^{\circ}\text{C}$  / - 0  $^{\circ}\text{C}$  and < 5 % RH (dry air / nitrogen) or

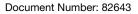
96 h at 60 °C + 5 °C and < 5 % RH for all device containers or 24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC<sup>®</sup> standard J-STD-020 level 3 label is included on all dry bags.

	Caution This bag contains MOISTURE-SENSITIVE DEVICES
	<ol> <li>Calculated shelf life in sealed bag: 12 months at &lt;40°C and &lt;90% relative humidity (RH)</li> </ol>
	2. Peak package body temperature: $260$ °C
	<ol> <li>After bag is opened, devices that will be subjected to reflow solder or other high temperature process must be</li> </ol>
	a) Mounted within: <u>168</u> hours of factory conditions <sup>(7)</sup> blank, see adjacent bar code label <30°C/60% RH. or
	b) Stored per J-STD-033
	4. Devices require bake, before mounting, if:
	a) Humidity Indicator Card reads $>10\%$ for level 2a - 5a devices or $>\!60\%$ for level 2 devices when read at $23{\pm}5^\circ C$
	b) 3a or 3b are not met
	5. If baking is required, refer to IPC/JEDEC J-STD-033 for bake procedure
	Bag Seal Date:
	If blank, see adjacent bar code label
_	Note: Level and body temperature defined by IPC/JEDEC J-STD-020
50 I L	

EIA JEDEC standard J-STD-020 level 3 label is included on all dry bags

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ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

#### VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

### BAR CODE PRODUCT LABEL (example)





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