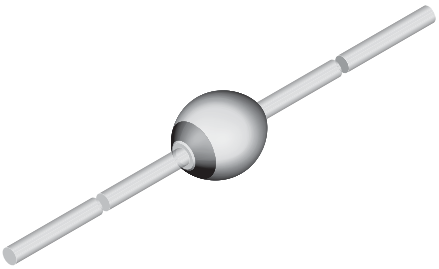
BYT52A, BYT52B, BYT52D, BYT52G, BYT52J, BYT52K, BYT52M

Fast Avalanche Sinterglass Diode



FEATURES

- Glass passivated junction
- Hermetically sealed package
- Low reverse current
- Soft recovery characteristics
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

RoHS COMPLIANT
HALOGEN FREE

APPLICATIONS

- Fast rectification and switching diode

DESIGN SUPPORT TOOLS

949539
click logo to get started

3D Models Available

MECHANICAL DATA

Case: SOD-57
Terminals: plated axial leads, solderable per MIL-STD-750, method 2026
Polarity: color band denotes cathode end
Mounting position: any
Weight: approx. 369 mg

ORDERING INFORMATION (Example)

DEVICE NAME	ORDERING CODE	TAPED UNITS	MINIMUM ORDER QUANTITY
BYT52M	BYT52M-TR	5000 per 10" tape and reel	25 000
BYT52M	BYT52M-TAP	5000 per ammopack	25 000

PARTS TABLE

PART	TYPE DIFFERENTIATION	PACKAGE
BYT52A	V_R = 50 V; $I_{F(AV)}$ = 1.4 A	SOD-57
BYT52B	V_R = 100 V; $I_{F(AV)}$ = 1.4 A	SOD-57
BYT52D	V_R = 200 V; $I_{F(AV)}$ = 1.4 A	SOD-57
BYT52G	V_R = 400 V; $I_{F(AV)}$ = 1.4 A	SOD-57
BYT52J	V_R = 600 V; $I_{F(AV)}$ = 1.4 A	SOD-57
BYT52K	V_R = 800 V; $I_{F(AV)}$ = 1.4 A	SOD-57
BYT52M	V_R = 1000 V; $I_{F(AV)}$ = 1.4 A	SOD-57

ABSOLUTE MAXIMUM RATINGS (T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
Reverse voltage = repetitive peak reverse voltage	See electrical characteristics	BYT52A	$V_R = V_{RRM}$	50	V
		BYT52B	$V_R = V_{RRM}$	100	V
		BYT52D	$V_R = V_{RRM}$	200	V
		BYT52G	$V_R = V_{RRM}$	400	V
		BYT52J	$V_R = V_{RRM}$	600	V
		BYT52K	$V_R = V_{RRM}$	800	V
		BYT52M	$V_R = V_{RRM}$	1000	V
Peak forward surge current	t_p = 10 ms, half sine wave		I_{FSM}	50	A
Average forward current	l = 10 mm		$I_{F(AV)}$	1.4	A
	On PC board		$I_{F(AV)}$	0.85	A
Non repetitive reverse avalanche energy	$I_{(BR)R}$ = 0.4 A	BYT52J	E_R	10	mJ
		BYT52K	E_R	10	mJ
		BYT52M	E_R	10	mJ
Junction and storage temperature range			$T_j = T_{stg}$	-55 to +175	°C

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MAXIMUM THERMAL RESISTANCE (T_{amb} = 25 °C, unless otherwise specified)				
PARAMETER	**TEST CONDITION**	**SYMBOL**	**VALUE**	**UNIT**
Junction ambient	Lead length l = 10 mm, T_L = constant	R_{thJA}	45	K/W
	On PC board with spacing 25 mm	R_{thJA}	100	K/W

ELECTRICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)							
PARAMETER	**TEST CONDITION**	**PART**	**SYMBOL**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Forward voltage	I_F = 1 A		V_F	-	-	1.3	V
Reverse current	$V_R = V_{RRM}$		I_R	-	-	5	µA
	$V_R = V_{RRM}$, T_j = 150 °C		I_R	-	-	150	µA
Reverse recovery time	I_F = 0.5 A, I_R = 1 A, i_R = 0.25 A		t_{rr}	-	-	200	ns

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

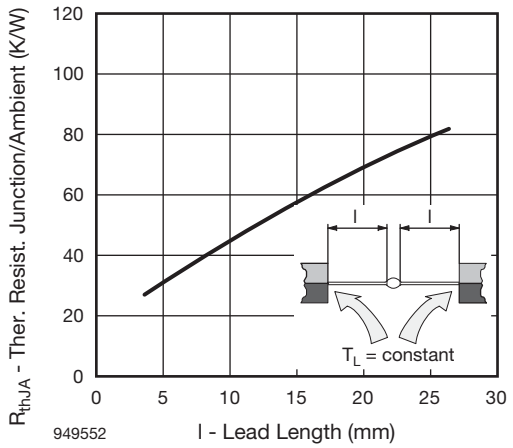


Fig. 1 - Max. Thermal Resistance vs. Lead Length

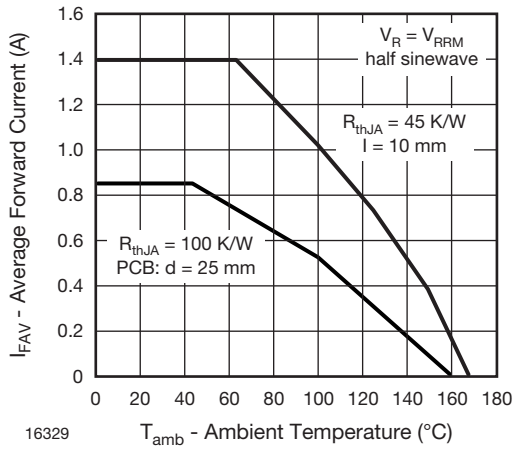


Fig. 3 - Max. Average Forward Current vs. Ambient Temperature

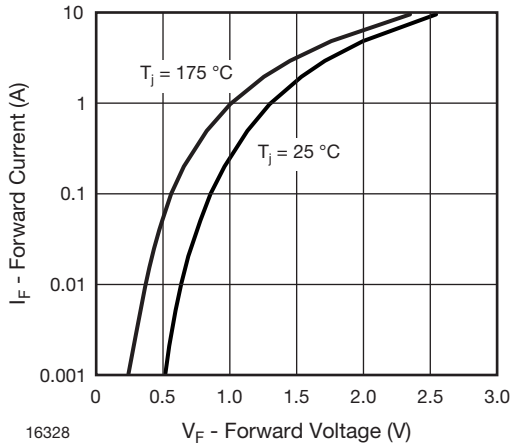


Fig. 2 - Max. Forward Current vs. Forward Voltage

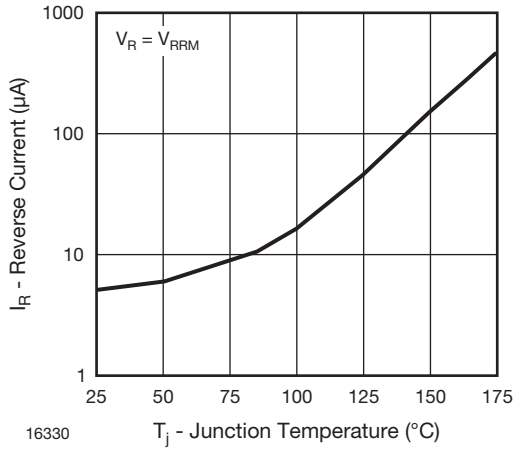


Fig. 4 - Max. Reverse Current vs. Junction Temperature

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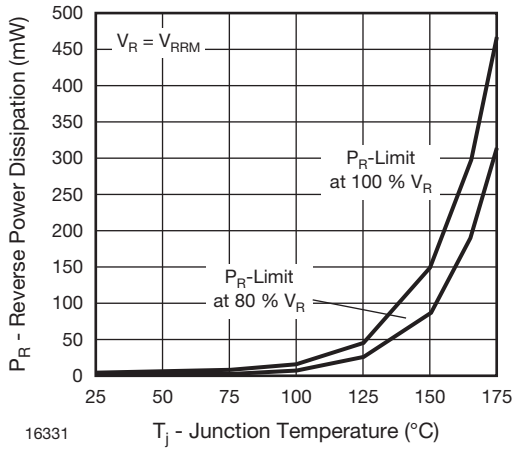


Fig. 5 - Max. Reverse Power Dissipation vs. Junction Temperature

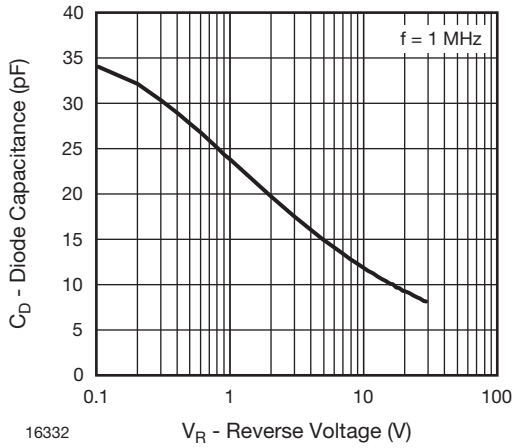
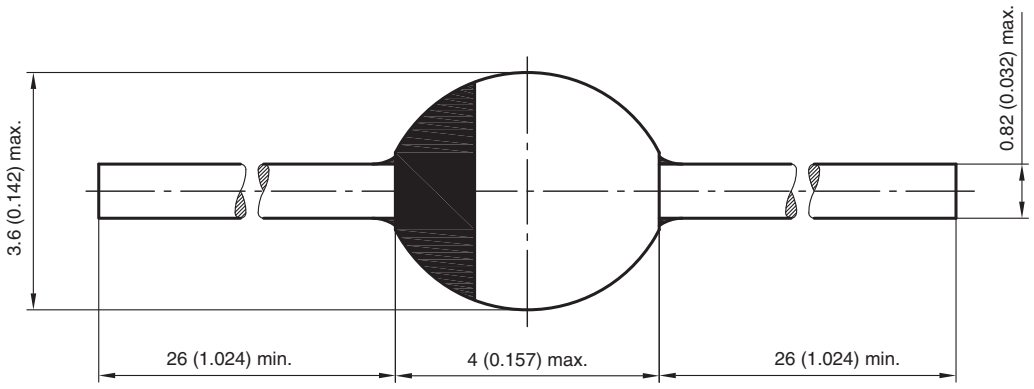


Fig. 6 - Diode Capacitance vs. Reverse Voltage

PACKAGE DIMENSIONS in millimeters (inches): **SOD-57**



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