V20PL60-M3

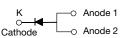
Vishay General Semiconductor

High Current Density Surface Mount TMBS[®] (Trench MOS Barrier Schottky) Rectifier

Ultra Low $V_F = 0.29$ V at $I_F = 5$ A

eSMP[®] Series

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LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	20 A			
V _{RRM}	60 V			
I _{FSM}	240 A			
V_F at I_F = 20 A (T_A = 125 °C)	0.46 V			
T _J max.	150 °C			
Package	SMPC (TO-277A)			
Circuit configuration	Single			

FEATURES

- Very low profile typical height of 1.1 mm
- Ideal for automated placement
- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in low voltage high frequency DC/DC converters, freewheeling, and polarity protection applications.

MECHANICAL DATA

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V20PL60	UNIT	
Device marking code		20L6		
Maximum repetitive peak reverse voltage	V _{RRM}	60	V	
Maximum average forward rectified current (fig. 1)	I _F ⁽¹⁾	20	^	
	I _F ⁽²⁾	5.5	— A	
Maximum DC reverse voltage	V _{DC}	45	V	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	240	A	
Operating junction and storage temperature range	T _J , T _{STG}	-40 to +150	°C	

Notes

⁽¹⁾ Mounted on 30 mm x 30 mm pad areas aluminum PCB

⁽²⁾ Free air, mounted on recommended copper pad area

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COMPLIANT

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V20PL60-M3

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ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 5.0 A	T _A = 25 °C	V _F ⁽¹⁾	0.40	-	- V
	I _F = 10 A			0.45	-	
	I _F = 20 A			0.51	0.59	
	I _F = 5.0 A	T _A = 125 °C		0.29	-	
	I _F = 10 A			0.36	-	
	I _F = 20 A			0.46	0.54	
Reverse current	$V_{\rm D} = 45 V$	T _A = 25 °C	I _R ⁽²⁾	0.025	-	mA
		T _A = 125 °C		17	-	
	$V_{\rm D} = 60 V$	T _A = 25 °C		-	4	
		T _A = 125 °C		35	100	mA

Notes

SHAY

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V20PL60	UNIT	
Typical thermal resistance	R _{0JA} (1)(2)	68	°C/W	
Typical thermal resistance	R _{0JM} ⁽³⁾	4	C/ W	

Notes

 $^{(1)}$ Free air, mounted on recommended copper pad area; thermal resistance $R_{\theta JA}$ - junction to ambient

 $^{(2)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$

⁽³⁾ Mounted on 30 mm x 30 mm 2 oz. pad PCB; thermal resistance R_{0JM} - junction to mount measured at cathode side

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V20PL60-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel	
V20PL60-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel	



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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

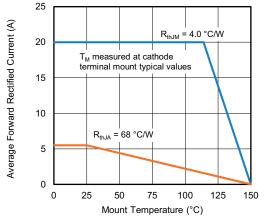


Fig. 1 - Maximum Forward Current Derating Curve

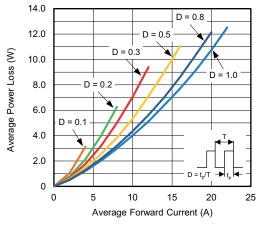
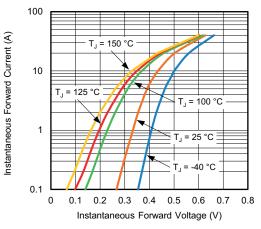
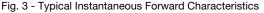


Fig. 2 - Forward Power Loss Characteristics





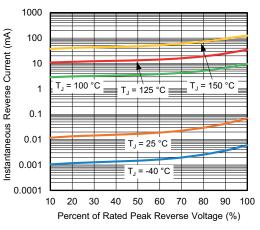


Fig. 4 - Typical Reverse Leakage Characteristics

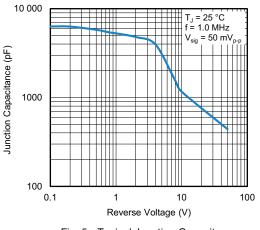


Fig. 5 - Typical Junction Capacitance

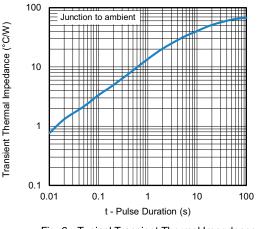


Fig. 6 - Typical Transient Thermal Impedance

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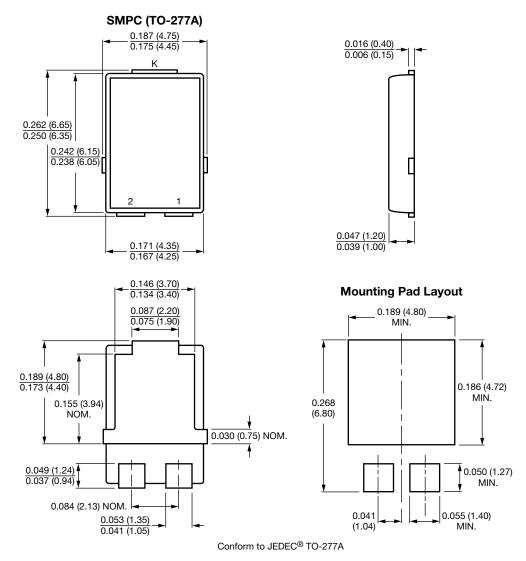
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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