V20PL60-M3

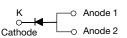
Vishay General Semiconductor

# High Current Density Surface Mount TMBS<sup>®</sup> (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.29$  V at  $I_F = 5$  A

# eSMP<sup>®</sup> Series

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# LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	20 A			
V <sub>RRM</sub>	60 V			
I <sub>FSM</sub>	240 A			
$V_F$ at $I_F$ = 20 A ( $T_A$ = 125 °C)	0.46 V			
T <sub>J</sub> max.	150 °C			
Package	SMPC (TO-277A)			
Circuit configuration	Single			

### FEATURES

- Very low profile typical height of 1.1 mm
- Ideal for automated placement
- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

# **TYPICAL APPLICATIONS**

For use in low voltage high frequency DC/DC converters, freewheeling, and polarity protection applications.

# **MECHANICAL DATA**

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V20PL60	UNIT	
Device marking code		20L6		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	60	V	
Maximum average forward rectified current (fig. 1)	I <sub>F</sub> <sup>(1)</sup>	20	^	
	I <sub>F</sub> <sup>(2)</sup>	5.5	— A	
Maximum DC reverse voltage	V <sub>DC</sub>	45	V	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	240	A	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C	

### Notes

<sup>(1)</sup> Mounted on 30 mm x 30 mm pad areas aluminum PCB

<sup>(2)</sup> Free air, mounted on recommended copper pad area

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# V20PL60-M3

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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25$ °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.40	-	- V
	I <sub>F</sub> = 10 A			0.45	-	
	I <sub>F</sub> = 20 A			0.51	0.59	
	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 125 °C		0.29	-	
	I <sub>F</sub> = 10 A			0.36	-	
	I <sub>F</sub> = 20 A			0.46	0.54	
Reverse current	$V_{\rm D} = 45 V$	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	0.025	-	mA
		T <sub>A</sub> = 125 °C		17	-	
	$V_{\rm D} = 60 V$	T <sub>A</sub> = 25 °C		-	4	
		T <sub>A</sub> = 125 °C		35	100	mA

### Notes

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 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V20PL60	UNIT	
Typical thermal resistance	R <sub>0JA</sub> (1)(2)	68	°C/W	
Typical thermal resistance	R <sub>0JM</sub> <sup>(3)</sup>	4	C/ W	

### Notes

 $^{(1)}$  Free air, mounted on recommended copper pad area; thermal resistance  $R_{\theta JA}$  - junction to ambient

 $^{(2)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

<sup>(3)</sup> Mounted on 30 mm x 30 mm 2 oz. pad PCB; thermal resistance R<sub>0JM</sub> - junction to mount measured at cathode side

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V20PL60-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel	
V20PL60-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel	



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# RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

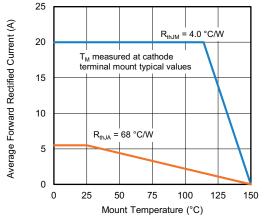


Fig. 1 - Maximum Forward Current Derating Curve

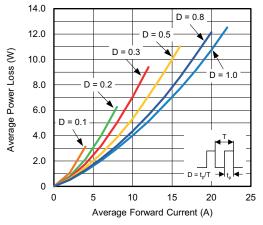
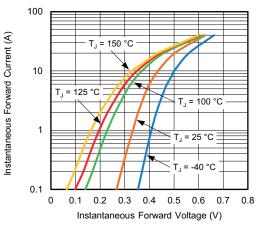
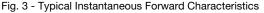


Fig. 2 - Forward Power Loss Characteristics





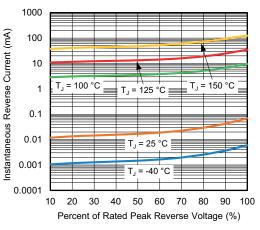


Fig. 4 - Typical Reverse Leakage Characteristics

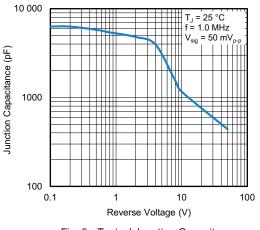


Fig. 5 - Typical Junction Capacitance

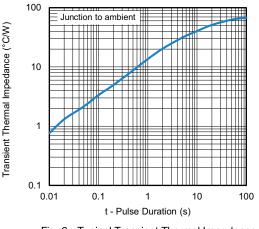


Fig. 6 - Typical Transient Thermal Impedance

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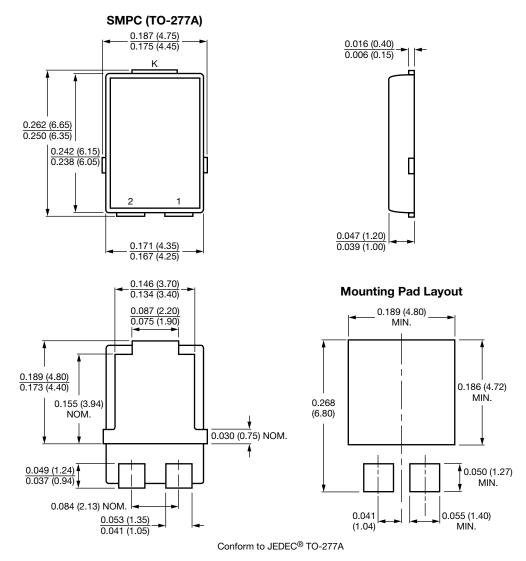
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# PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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