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## Vishay General Semiconductor

# High Current Density Surface Mount TMBS® (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.29 \text{ V}$  at  $I_F = 5 \text{ A}$ 



#### **FEATURES**

- Very low profile typical height of 1.1 mm
- · Ideal for automated placement
- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### TYPICAL APPLICATIONS

For use in low voltage high frequency DC/DC converters, freewheeling, and polarity protection applications.

#### **MECHANICAL DATA**

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test

#### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS			
I <sub>F(AV)</sub>	25 A		
V <sub>RRM</sub>	60 V		
I <sub>FSM</sub>	240 A		
$V_F$ at $I_F = 25$ A $(T_A = 125  ^{\circ}C)$	0.5 V		
T <sub>J</sub> max.	150 °C		
Package	SMPC (TO-277A)		
Circuit configuration	Single		

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V25PL60	UNIT	
Device marking code		25L6		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	60	V	
Maximum average forward rectified current (fig. 1)	I <sub>F</sub> <sup>(1)</sup>	25	Α	
	I <sub>F</sub> <sup>(2)</sup>	5.5		
Maximum DC reverse voltage	V <sub>DC</sub>	45	V	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	240	А	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C	

#### Notes

<sup>(1)</sup> Mounted on 30 mm x 30 mm pad areas aluminum PCB

<sup>(2)</sup> Free air, mounted on recommended copper pad area



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CO	NDITIONS	SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.40	-	. V
	I <sub>F</sub> = 12.5 A			0.46	-	
	I <sub>F</sub> = 25 A			0.54	0.63	
	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 125 °C		0.29	-	
	I <sub>F</sub> = 12.5 A			0.39	-	
	I <sub>F</sub> = 25 A			0.5	0.59	
Reverse current	V - 45 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	0.025	-	mA
	$V_R = 45 \text{ V}$	T <sub>A</sub> = 125 °C		17	-	
	$V_D = 60 \text{ V}$	T <sub>A</sub> = 25 °C		-	4	- mA
		T <sub>A</sub> = 125 °C		35	100	

#### **Notes**

 $^{(1)}$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

 $^{(2)}$  Pulse test: pulse width  $\leq 5$  ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V25PL60	UNIT	
Typical thermal registance	R <sub>0</sub> JA (1)(2)	68	°C/W	
Typical thermal resistance	R <sub>0JM</sub> (3)	4		

#### **Notes**

 $^{(1)}\,$  Free air, mounted on recommended copper pad area; thermal resistance  $R_{\theta JA}$  - junction to ambient

 $^{(2)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

(3) Mounted on 30 mm x 30 mm 2 oz. pad PCB; thermal resistance R<sub>0JM</sub> - junction to mount measured at cathode side

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V25PL60-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel	
V25PL60-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel	

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## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

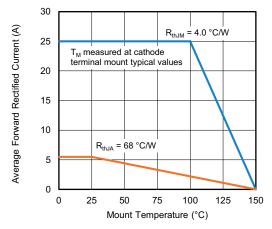


Fig. 1 - Maximum Forward Current Derating Curve

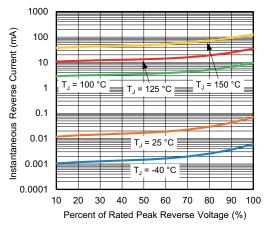


Fig. 4 - Typical Reverse Leakage Characteristics

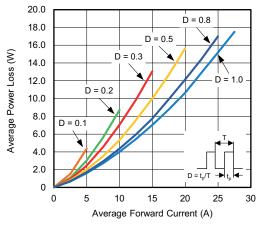


Fig. 2 - Forward Power Loss Characteristics

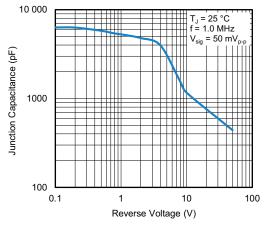


Fig. 5 - Typical Junction Capacitance

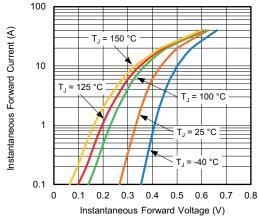


Fig. 3 - Typical Instantaneous Forward Characteristics

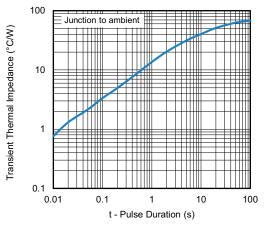
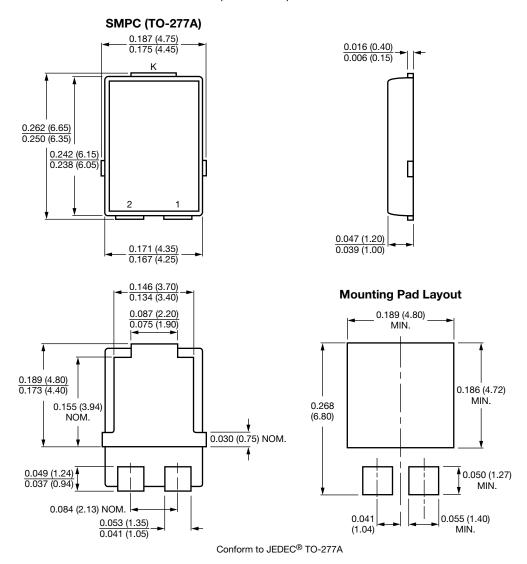


Fig. 6 - Typical Transient Thermal Impedance



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## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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