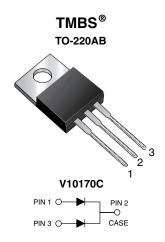
FREE



### Vishay General Semiconductor

## **Dual High-Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.57 \text{ V}$  at  $I_F = 2.5 \text{ A}$ 



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 5 A			
$V_{RRM}$	170 V			
I <sub>FSM</sub>	80 A			
V <sub>F</sub> at I <sub>F</sub> = 5.0 A	0.65 V			
T <sub>J</sub> max.	175 °C			
Package	TO-220AB			
Circuit configuration	Common cathode			

#### **FEATURES**

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses

RoHS · High efficiency operation HALOGEN

Solder dip 275 °C max. 10 s, per JESD 22-B106

• Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

#### **MECHANICAL DATA**

Case: TO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and

commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V10170C	UNIT	
Maximum repetitive peak reverse voltage		$V_{RRM}$	170	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub>	10	^	
	per diode		5	_ A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	80	А	
Voltage rate of change (rated V <sub>R</sub> )		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-40 to +175	°C	



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	$I_F = 2.5 A$	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.74	-	V	
	$I_F = 5.0 A$			0.84	1.03		
	$I_F = 2.5 A$	T <sub>A</sub> = 125 °C		0.57	-		
	$I_F = 5.0 A$			0.65	0.74		
Reverse current per diode	V <sub>R</sub> = 136 V	T <sub>A</sub> = 25 °C T <sub>A</sub> = 125 °C	I <sub>R</sub> <sup>(2)</sup>	0.3	-	μΑ	
		T <sub>A</sub> = 125 °C		0.9	-	mA	
	V <sub>R</sub> = 170 V	T <sub>A</sub> = 25 °C		-	90	μΑ	
		T <sub>A</sub> = 125 °C		1.3	10	mA	

#### Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 20 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V10170C	UNIT	
Typical thermal resistance	per diode	$R_{ heta JC}$	3.0	°C/W	
	per device		1.7		

ORDERING INFORMATION (Example)							
PACKAGE	AGE PREFERRED P/N UNIT WEIGHT (g) PACKAGE CODE		BASE QUANTITY	DELIVERY MODE			
TO-220AB	V10170C-M3/4W	1.87	4W	50/tube	Tube		

### **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

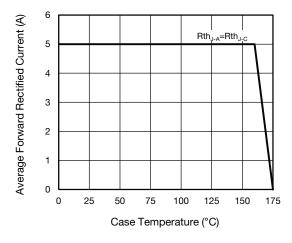


Fig. 1 - Maximum Forward Current Derating Curve

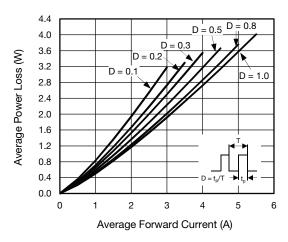


Fig. 2 - Forward Power Loss Characteristics Per Diode



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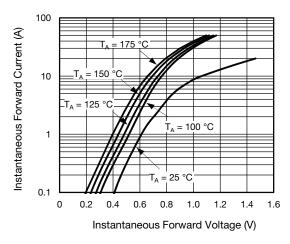


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

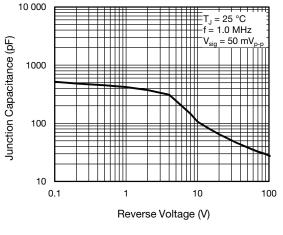


Fig. 5 - Typical Junction Capacitance Per Diode

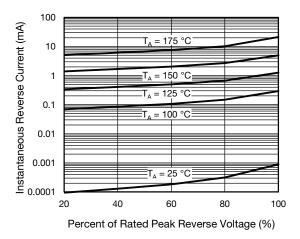


Fig. 4 - Typical Reverse Characteristics Per Diode

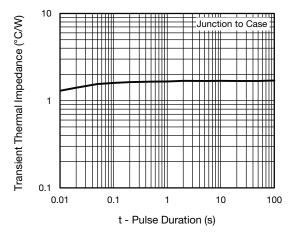
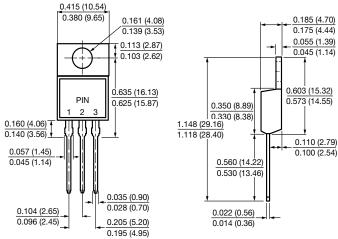


Fig. 6 - Typical Transient Thermal Impedance Per Device

#### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

## TO-220AB





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