# IRFP140

Vishay Siliconix



**TO-247AC** 

**PRODUCT SUMMARY** 

V<sub>DS</sub> (V)

R<sub>DS(on)</sub> (Ω)

Q<sub>qs</sub> (nC)

Q<sub>gd</sub> (nC)

Q<sub>a</sub> (max.) (nC)

Configuration

# **Power MOSFET**

S

N-Channel MOSFET

0.077

100

72

11

32

Single

 $V_{GS} = 10 V$ 

### FEATURES

- Dynamic dV/dt rating
- · Repetitive avalanche rated
- · Isolated central mounting hole
- 175 °C operating temperature
- Fast switching
- · Ease of paralleling
- Simple drive requirements
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

#### Note

\* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

#### DESCRIPTION

Third generation Power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-247AC package is preferred for commercial-industrial applications where higher power levels preclude the use of TO-220AB devices. The TO-247AC is similar but superior to the earlier TO-218 package because of its isolated mounting hole. It also provides greater creepage distance between pins to meet the requirements of most safety specifications.

ORDERING INFORMATION	
Package	TO-247AC
Lead (Pb)-free	IRFP140PbF

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub>	= 25 °C, unless otherwis	se noted)		
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V <sub>DS</sub>	100	V
Gate-source voltage		V <sub>GS</sub>	± 20	V
Continuous drain current	$V_{GS}$ at 10 V $T_C = 25 \degree C$ $T_C = 100 \degree C$	L_	31	
Continuous drain current	ID	22	А	
Pulsed drain current <sup>a</sup>		I <sub>DM</sub>	120	
Linear derating factor		1.2	W/°C	
Single pulse avalanche energy <sup>b</sup>		E <sub>AS</sub>	100	mJ
Repetitive avalanche current <sup>a</sup>		I <sub>AR</sub>	31	А
Repetitive avalanche energy <sup>a</sup>		E <sub>AR</sub>	18	mJ
Maximum power dissipation	T <sub>C</sub> = 25 °C	PD	180	W
Peak Diode Recovery dV/dt c		dV/dt	5.5	V/ns
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C
Soldering recommendations (peak temperature)	for 10 s		300 <sup>d</sup>	
Mounting torquo	6-32 or M3 screw		10	lbf ∙ in
Mounting torque	0-32 OF WIS SCIEW		1.1	N · m

#### Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)

b.  $V_{DD}$  = 25 V, starting T<sub>J</sub> = 25 °C, L = 156 µH, R<sub>g</sub> = 25  $\Omega$ , I<sub>AS</sub> = 31 A (see fig. 12)

c.  $I_{SD} \le 28$  A, dl/dt  $\le 170$  A/µs,  $V_{DD} \le V_{DS}$ ,  $T_J \le 175$  °C

d. 1.6 mm from case

S22-0045-Rev. C, 24-Jan-2022

1



THERMAL RESISTANCE RAT	INGS			
PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum junction-to-ambient	R <sub>thJA</sub>	-	40	
Case-to-sink, flat, greased surface	R <sub>thCS</sub>	0.24	-	°C/W
Maximum junction-to-case (drain)	R <sub>thJC</sub>	-	0.83	

<b>SPECIFICATIONS</b> ( $T_J = 25 \text{ °C}$ , u	Inless otherw	rise noted)					
PARAMETER	SYMBOL	TEST	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static	•			•		•	•
Drain-source breakdown voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = 2$	50 µA	100	-	-	V
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$	Reference to 25	°C, I <sub>D</sub> = 1 mA	-	0.13	-	V/°C
Gate-source threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 2$	250 μΑ	2.0	-	4.0	V
Gate-source leakage	I <sub>GSS</sub>	$V_{GS} = \pm 20 V$		-	-	± 100	nA
Zere gete veltege drein eurrent		$V_{DS} = 100 \text{ V}, \text{ V}_{GS}$	<sub>S</sub> = 0 V	-	-	25	
Zero gate voltage drain current	IDSS	$V_{DS} = 80 \text{ V}, \text{ V}_{GS}$	= 0 V, T <sub>J</sub> = 150 °C	-	-	250	μA
Drain-source on-state resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 19 A <sup>b</sup>	-	-	0.077	Ω
Forward transconductance	9 <sub>fs</sub>	$V_{DS} = 50 \text{ V}, \text{ I}_{D} =$	19 A <sup>b</sup>	9.8	-	-	S
Dynamic							
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V,		-	1700	-	
Output capacitance	C <sub>oss</sub>	$V_{DS} = 25 V,$		-	550	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1.0 MHz, see	e fig. 5	-	110	-	
Total gate charge	Qg			-	-	72	
Gate-source charge	Q <sub>gs</sub>	V <sub>GS</sub> = 10 V	$I_D = 17 \text{ A}, V_{DS} = 80 \text{ V}$ see fig. 6 and 13 <sup>b</sup>	-	-	11	nC
Gate-drain charge	Q <sub>gd</sub>		obo ng. o unu ro	-	-	32	
Turn-on delay time	t <sub>d(on)</sub>			-	11	-	
Rise time	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, \text{ I}_{D} =$		-	44	-	
Turn-off delay time	t <sub>d(off)</sub>	$R_{g} = 9.1 \Omega, R_{D} =$	$2.9 \Omega$ , see fig. 10 <sup>b</sup>	-	53	-	ns
Fall time	t <sub>f</sub>			-	43	-	
Internal drain inductance	L <sub>D</sub>	Between lead,	۵ لر	-	5.0	-	
Internal source inductance	L <sub>S</sub>	6 mm (0.25") fro package and ce die contact		-	13	-	nH
Drain-Source Body Diode Characteristics	•			•		•	•
Continuous source-drain diode current	I <sub>S</sub>	MOSFET symbo		-	-	31	
Pulsed diode forward current <sup>a</sup>	I <sub>SM</sub>	showing the integral reverse p - n junction die	ode	-	-	120	A
Body diode voltage	V <sub>SD</sub>	T <sub>J</sub> = 25 °C, I <sub>S</sub> = 3	31 A, V <sub>GS</sub> = 0 V <sup>b</sup>	-	-	2.5	V
Body diode reverse recovery time	t <sub>rr</sub>			-	180	360	ns
Body diode reverse recovery charge	Q <sub>rr</sub>	$I_{\rm J} = 25$ °C, $I_{\rm F} = -$	17 A, dl/dt = 100 A/µs <sup>b</sup>	-	1.3	2.8	μC
Forward turn-on time	t <sub>on</sub>	Intrinsic turn-on	time is neglegible (turn-o	n is domir	nated by I	_s and Ln	)

#### Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)

b. Pulse width  $\leq$  300 µs; duty cycle  $\leq$  2 %

2



## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

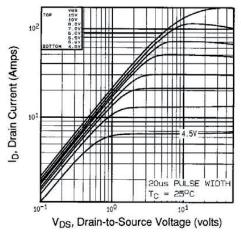


Fig. 1 - Typical Output Characteristics, T<sub>C</sub> = 25 °C

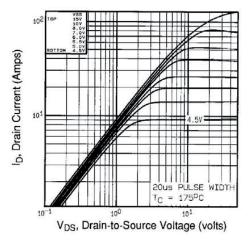


Fig. 2 - Typical Output Characteristics,  $T_C = 175$  °C

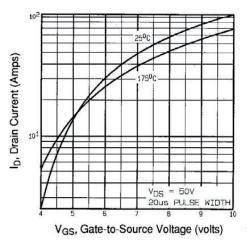


Fig. 3 - Typical Transfer Characteristics

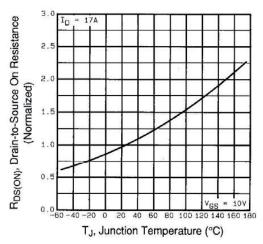


Fig. 4 - Normalized On-Resistance vs. Temperature

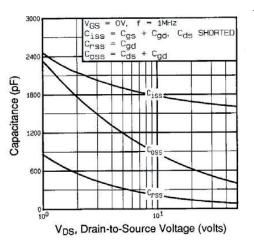


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

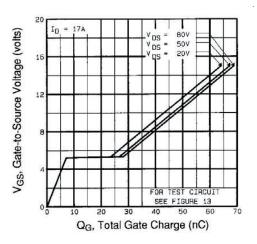
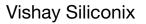


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

## IRFP140



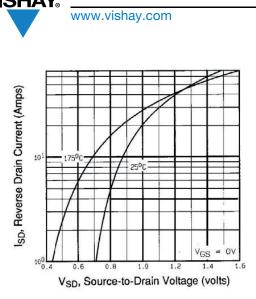


Fig. 7 - Typical Source-Drain Diode Forward Voltage

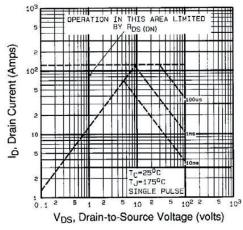


Fig. 8 - Maximum Safe Operating Area

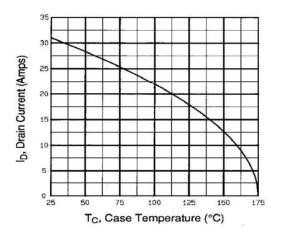


Fig. 9 - Maximum Drain Current vs. Case Temperature

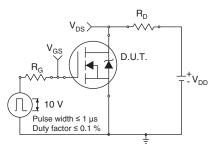


Fig. 10a - Switching Time Test Circuit

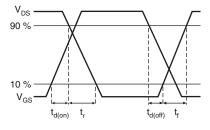


Fig. 10b - Switching Time Waveforms

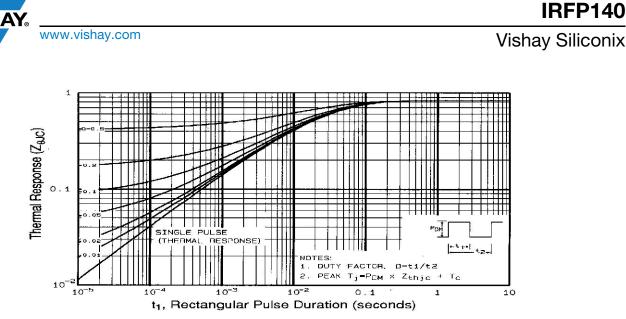


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

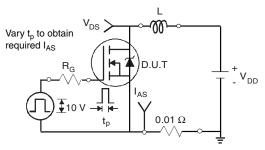


Fig. 12a - Unclamped Inductive Test Circuit

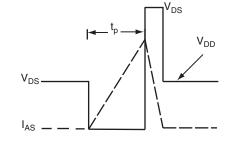


Fig. 12b - Unclamped Inductive Waveforms

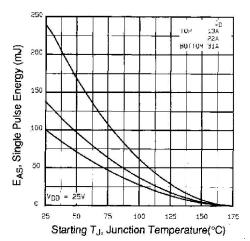


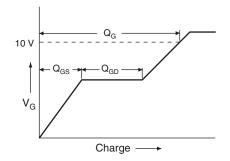
Fig. 12c - Maximum Avalanche Energy vs. Drain Current

5



IRFP140

# Vishay Siliconix



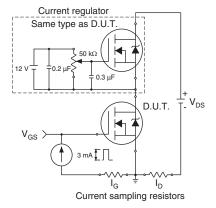


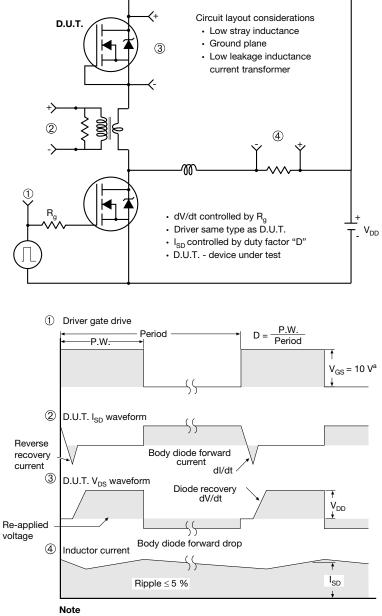
Fig. 13a - Basic Gate Charge Waveform

Fig. 13b - Gate Charge Test Circuit





### Peak Diode Recovery dV/dt Test Circuit



a.  $V_{GS}$  = 5 V for logic level devices

#### Fig. 14 - For N-Channel

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg291202">www.vishay.com/ppg291202</a>.

7





**TO-247AC (High Voltage)** 

## VERSION 1: FACILITY CODE = 9





(	

	М	ILLIMETERS		
DIM.	MIN.	NOM.	MAX.	NOTES
А	4.83	5.02	5.21	
A1	2.29	2.41	2.55	
A2	1.17	1.27	1.37	
b	1.12	1.20	1.33	
b1	1.12	1.20	1.28	
b2	1.91	2.00	2.39	6
b3	1.91	2.00	2.34	
b4	2.87	3.00	3.22	6, 8
b5	2.87	3.00	3.18	
С	0.40	0.50	0.60	6
c1	0.40	0.50	0.56	
D	20.40	20.55	20.70	4

		MILLIMETERS	S	
DIM.	MIN.	NOM.	MAX.	NOTES
D1	16.46	16.76	17.06	5
D2	0.56	0.66	0.76	
E	15.50	15.70	15.87	4
E1	13.46	14.02	14.16	5
E2	4.52	4.91	5.49	3
е		5.46 BSC		
L	14.90	15.15	15.40	
L1	3.96	4.06	4.16	6
ØР	3.56	3.61	3.65	7
Ø P1	7.19 ref.			
Q	5.31	5.50	5.69	
S		5.51 BSC		

### Notes

- <sup>(1)</sup> Package reference: JEDEC<sup>®</sup> TO247, variation AC
- (2) All dimensions are in mm
- <sup>(3)</sup> Slot required, notch may be rounded
- <sup>(4)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm per side. These dimensions are measured at the outermost extremes of the plastic body
- <sup>(5)</sup> Thermal pad contour optional with dimensions D1 and E1
- (6) Lead finish uncontrolled in L1
- (7) Ø P to have a maximum draft angle of 1.5° to the top of the part with a maximum hole diameter of 3.91 mm
- (8) Dimension b2 and b4 does not include dambar protrusion. Allowable dambar protrusion shall be 0.1 mm total in excess of b2 and b4 dimension at maximum material condition



## VERSION 2: FACILITY CODE = Y



	MILLIN	IETERS	
DIM.	MIN.	MAX.	NOTES
A	4.58	5.31	
A1	2.21	2.59	
A2	1.17	2.49	
b	0.99	1.40	
b1	0.99	1.35	
b2	1.53	2.39	
b3	1.65	2.37	
b4	2.42	3.43	
b5	2.59	3.38	
С	0.38	0.86	
c1	0.38	0.76	
D	19.71	20.82	
D1	13.08	-	

	MILLIN	IETERS	
DIM.	MIN.	MAX.	NOTES
D2	0.51	1.30	
E	15.29	15.87	
E1	13.72	-	
е	5.46	BSC	
Øk	0.2	254	
L	14.20	16.25	
L1	3.71	4.29	
ØР	3.51	3.66	
Ø P1	-	7.39	
Q	5.31	5.69	
R	4.52	5.49	
S	5.51	BSC	

### Notes

- <sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994
- <sup>(2)</sup> Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- <sup>(4)</sup> Thermal pad contour optional with dimensions D1 and E1
- <sup>(5)</sup> Lead finish uncontrolled in L1
- <sup>(6)</sup> Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- <sup>(7)</sup> Outline conforms to JEDEC outline TO-247 with exception of dimension c

For technical questions, contact: <u>hvm@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



## VERSION 3: FACILITY CODE = N



	MILLIMETERS			MILLIMETERS	
DIM.	MIN.	MAX.	DIM.	MIN.	MAX
А	4.65	5.31	D2	0.51	1.35
A1	2.21	2.59	E	15.29	15.87
A2	1.17	1.37	E1	13.46	-
b	0.99	1.40	e	5.46	BSC
b1	0.99	1.35	k	0.:	254
b2	1.65	2.39	L	14.20	16.10
b3	1.65	2.34	L1	3.71	4.29
b4	2.59	3.43	N	7.62	BSC
b5	2.59	3.38	Р	3.56	3.66
С	0.38	0.89	P1	-	7.39
c1	0.38	0.84	Q	5.31	5.69
D	19.71	20.70	R	4.52	5.49
D1	13.08	-	S	5.51	BSC

Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994

<sup>(2)</sup> Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

<sup>(4)</sup> Thermal pad contour optional with dimensions D1 and E1

<sup>(5)</sup> Lead finish uncontrolled in L1

<sup>(6)</sup> Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")



Vishay

# Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

© 2024 VISHAY INTERTECHNOLOGY, INC. ALL RIGHTS RESERVED

Revision: 01-Jan-2024