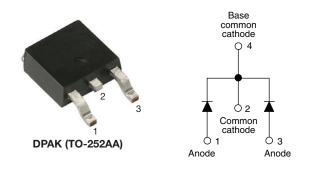
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High Performance Schottky Rectifier, 2 x 6 A



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PRIMARY CHARACTERISTICS							
I _{F(AV)}	2 x 6 A						
V _R	60 V						
V _F at I _F	0.57 V						
I _{RM}	35 mA at 125 °C						
T _J max.	150 °C						
E _{AS}	7 mJ						
Package	DPAK (TO-252AA)						
Circuit configuration	Common cathode						

FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- Popular DPAK outline
- Center tap configuration
- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-12CWQ06FN-M3 surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	12	А						
V _{RRM}		60	V						
I _{FSM}	t _p = 5 μs sine	320	А						
V _F	6 A _{pk} , T _J = 125 °C (per leg)	0.57	V						
TJ	Range	-55 to +150	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-12CWQ06FN-M3	UNITS					
Maximum DC reverse voltage	V _R	60	V					
Maximum working peak reverse voltage	V _{RWM}	00	v					

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDI	TEST CONDITIONS						
Maximum average per l	•	50 % duty cycle at T _C = 131 °C	6	٥					
See fig. 5 per devi	l _{F(AV)}	30% duty cycle at $1C = 131%$ C	12	A					
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	320	A				
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	105					
Non-repetitive avalanche energy per leg	ergy per leg E_{AS} $T_J = 25 \text{ °C}, I_{AS} = 1.2 \text{ A}, L = 10 \text{ mH}$		7	mJ					
Repetitive avalanche current per leg	ent per leg I_{AR} Current decaying linearly to zero in 1 µs Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.8	А					

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COMPLIANT HALOGEN





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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
		6 A	T ₁ = 25 °C	0.61	V			
Maximum forward voltage drop per leg	V (1)	12 A	1j=23 0	0.79				
See fig. 1	V _{FM} ⁽¹⁾	6 A	T _J = 125 °C	0.57				
		12 A	1j = 125 C	0.72				
Maximum reverse	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B} = \text{Rated } V_{\rm B}$	3	mA			
leakage current per leg See fig. 2	IRM ("	T _J = 125 °C	VR = naleu VR	35				
Threshold voltage	V _{F(TO)}	T T maximum	·		V			
Forward slope resistance	r _t	$T_J = T_J maximum$	24.14	mΩ				
Typical junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$, (test signal ran	360	pF				
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 5.0 nH						

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T _J ⁽¹⁾ , T _{Stg}		-55 to +150	°C			
Maximum thermal resistance,	per leg	P	DC operation See fig. 4	3.0	°C/W			
junction to case	per device	R _{thJC}		1.5				
				0.3	g			
Approximate weight				0.01	oz.			
Marking device	device Case style DPAK (TO-252AA)		Case style DPAK (TO-252AA)	12CWQ06FN				

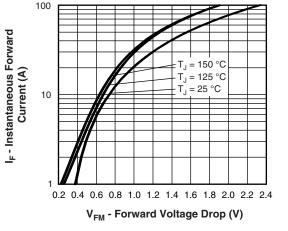
Note

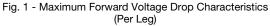
(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

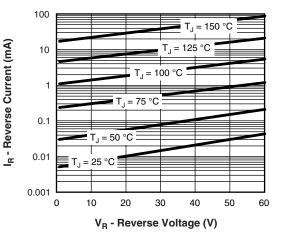


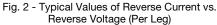
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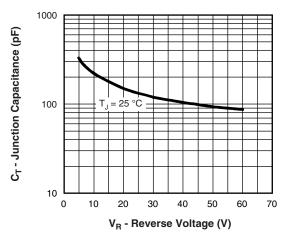
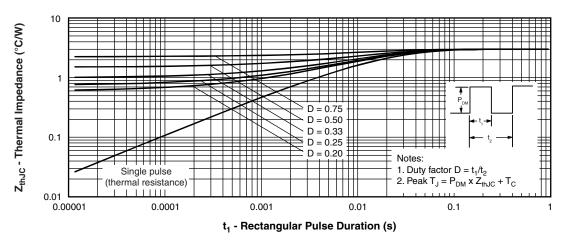


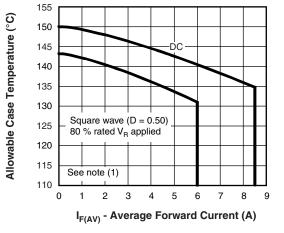
Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

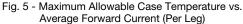


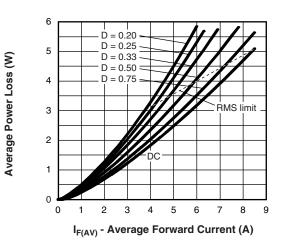


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Fig. 6 - Forward Power Loss Characteristics (Per Leg)

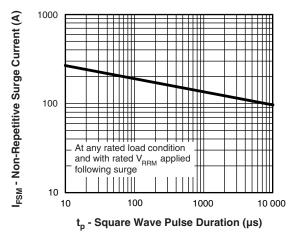


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

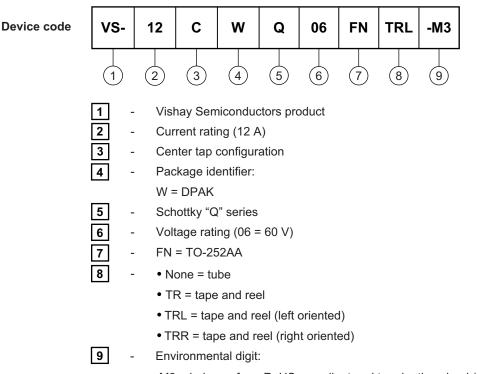
 $^{(1)}$ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$ Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = inverse power loss = $V_{R1} \times I_R (1 - D); I_R$ at $V_{R1} = 80 \%$ rated V_R

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ΊSHΔ



-M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-12CWQ06FN-M3	75	3000	Antistatic plastic tube						
VS-12CWQ06FNTR-M3	2000	2000	13" diameter reel						
VS-12CWQ06FNTRL-M3	3000	3000	13" diameter reel						
VS-12CWQ06FNTRR-M3	3000	3000	13" diameter reel						

LINKS TO RELATED DOCUMENTS							
Dimensions	www.vishay.com/doc?95627						
Part marking information	www.vishay.com/doc?95176						
Packaging information	www.vishay.com/doc?95033						
SPICE model	www.vishay.com/doc?95278						





D-PAK (TO-252AA) "M"

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	NOTES	ES NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STNIDUL	MIN.	MAX.	MIN.	MAX.	NOTES		STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES		
А	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	BSC			
A1	-	0.13	-	0.005			Н	9.40	10.41	0.370	0.410			
b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.070			
b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.			
b3	4.95	5.46	0.195	0.215	3		L2	0.51	BSC	0.020	BSC			
С	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.050	3		
c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.040			
D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.060	2		
D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°			
E	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°			
E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°			

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Section C - C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip

(5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁶⁾ Dimension b1 and c1 applied to base metal only

⁽⁷⁾ Datum A and B to be determined at datum plane H

⁽⁸⁾ Outline conforms to JEDEC[®] outline TO-252AA



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