

Vishay Semiconductors

RoHS

COMPLIANT

HALOGEN

FREE

Ultrafast Rectifier, 8 A FRED Pt®



TO-220 FullPAK 2L

PRIMARY CHARACTERISTICS				
I _{F(AV)}	8 A			
V_{R}	600 V			
V _F at I _F	0.84 V			
t _{rr} (typ.)	65 ns			
T _J max.	175 °C			
Package	TO-220 FullPAK 2L			
Circuit configuration	Single			

FEATURES

- State of the art low forward voltage drop
- · Ultrafast recovery time
- 175 °C operating junction temperature
- · Low leakage current
- Fully isolated package (V_{INS} = 2500 V_{RMS})
- True 2 pin package
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION

State of the art, ultralow V_F , soft-switching ultrafast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

APPLICATIONS

AC-DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units and DVD AC/DC power supplies.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V_{RRM}		600	V
Average rectified forward current in DC	I _{F(AV)}	T _C = 134 °C	8	^
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	120	A
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	600	-	-	.,
Converd veltage	V	I _F = 8 A	-	0.97	1.07	V
Forward voltage	V _F	I _F = 8 A, T _J = 150 °C	-	0.84	0.90	
Developed Inches of a surround	,	$V_R = V_R$ rated	ī	0.01	9	
Reverse leakage current	I _R	$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	5	50	μΑ
Junction capacitance	C _T	V _R = 600 V - 6 -		рF		
Series inductance	L _S	Measured lead to lead 5 mm from package body - 8 -		nΗ		

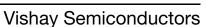




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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 A, dI_F/dt = 100$	$A/\mu s$, $V_R = 30 V$	-	65	100	
Develope to the second	$I_F = 8 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	150	250		
Reverse recovery time	t _{rr}	T _J = 25 °C		-	180	-	ns
		T _J = 125 °C		-	240	-	
Peak recovery current I _{RRM}	T _J = 25 °C	$I_F = 8 A$,	I _F = 8 A, dI _F /dt = 200 A/µs, V _R = 390 V	15	-	Α	
				-	19	-	_ ^
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	1500	-	nC
	T _J = 125 °C		-	2400	-	i iic	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C
Thermal resistance, junction-to-case	R _{thJC}		-	4.6	5.5	
Thermal resistance, junction-to-ambient	R _{thJA}	Typical socket mount	-	-	70	°C/W
Typical thermal resistance, case-to-heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-	
Weight			-	2	-	g
vveignt			-	0.07	-	oz.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-220 FullPAK 2L		ETL08	306FP	





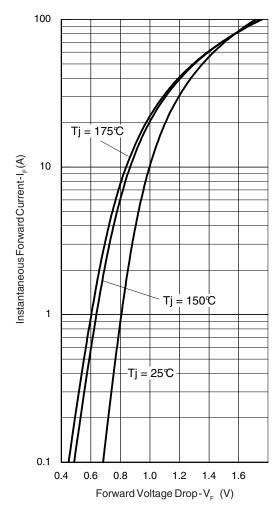


Fig. 1 - Typical Forward Voltage Drop Characteristics

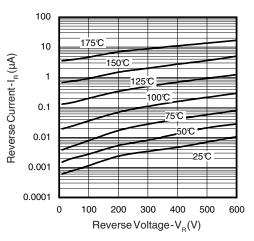


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

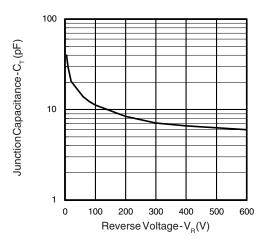


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

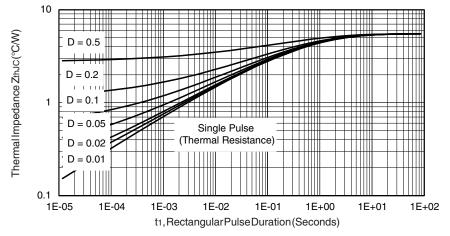


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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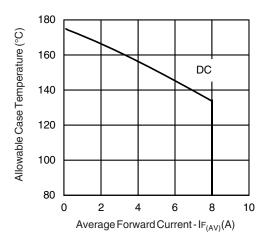


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

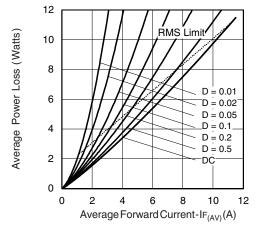


Fig. 6 - Forward Power Loss Characteristics

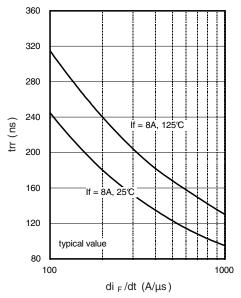


Fig. 7 - Typical Reverse Recovery vs. dI_F/dt

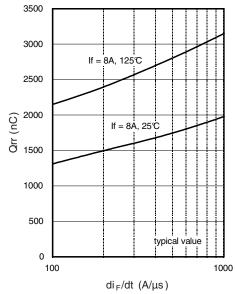
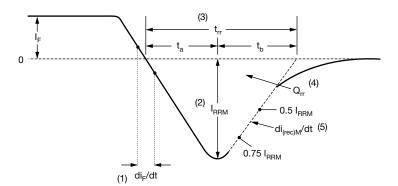


Fig. 8 - Typical Stored Charge vs. dl_F/dt

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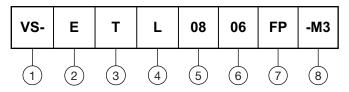


- (1) di_F/dt rate of change of current through zero crossing
- (4) Q_{rr} area under curve defined by t_{rr} and I_{RRM}
- (2) I_{RRM} peak reverse recovery current
- $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (5) di_{(rec)M}/dt peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Circuit configuration:

E = single

- **3** T = TO-220
- L = hyperfast recovery time
- 5 Current code: 08 = 8 A
- Voltage code: 06 = 600 V
- 7 FP = TO-220 FullPAK 2L
- 8 Environmental digit:
 - -M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-ETL0806FP-M3	50	1000	Antistatic plastic tube		

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?96157			
Part marking information	www.vishay.com/doc?95392			



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