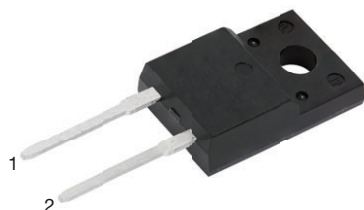
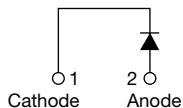


Ultrafast Rectifier, 15 A FRED Pt®



TO-220 FullPAK 2L



FEATURES

- Low forward voltage drop
- Ultrafast soft recovery time
- 175 °C operating junction temperature
- Low leakage current
- Fully isolated package ($V_{INS} = 2500 V_{RMS}$)
- True 2 pin package
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	15 A
V_R	600 V
V_F at I_F	1.1 V
t_{rr} (typ.)	24 ns
T_J max.	175 °C
Package	TO-220 FullPAK 2L
Circuit configuration	Single

DESCRIPTION

State of the art, ultralow V_F , soft-switching ultrafast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

APPLICATIONS

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units and DVD AC/DC power supplies.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V_{RRM}		600	V
Average rectified forward current in DC	$I_{F(AV)}$	$T_C = 103\text{ °C}$	15	A
Non-repetitive peak surge current	I_{FSM}	$T_J = 25\text{ °C}$	160	
Operating junction and storage temperatures	T_J, T_{Stg}		-65 to +175	°C

ELECTRICAL SPECIFICATIONS ($T_J = 25\text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_R	$I_R = 100\text{ }\mu\text{A}$	600	-	-	V
Forward voltage	V_F	$I_F = 15\text{ A}$	-	1.35	1.9	
		$I_F = 15\text{ A}, T_J = 150\text{ °C}$	-	1.1	1.3	
Reverse leakage current	I_R	$V_R = V_R$ rated	-	0.01	15	μA
		$T_J = 150\text{ °C}, V_R = V_R$ rated	-	20	200	
Junction capacitance	C_T	$V_R = 600\text{ V}$	-	12	-	pF
Series inductance	L_S	Measured lead to lead 5 mm from package body	-	8	-	nH

**DYNAMIC RECOVERY CHARACTERISTICS** ($T_J = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t_{rr}	$I_F = 1\text{ A}$, $dI_F/dt = 100\text{ A}/\mu\text{s}$, $V_R = 30\text{ V}$	-	24	28	ns
		$I_F = 15\text{ A}$, $dI_F/dt = 100\text{ A}/\mu\text{s}$, $V_R = 30\text{ V}$	-	36	47	
		$T_J = 25\text{ }^{\circ}\text{C}$	-	40	-	
		$T_J = 125\text{ }^{\circ}\text{C}$	-	87	-	
Peak recovery current	I_{RRM}	$T_J = 25\text{ }^{\circ}\text{C}$	-	5	-	A
		$T_J = 125\text{ }^{\circ}\text{C}$	-	9	-	
Reverse recovery charge	Q_{rr}	$T_J = 25\text{ }^{\circ}\text{C}$	-	107	-	nC
		$T_J = 125\text{ }^{\circ}\text{C}$	-	430	-	
Reverse recovery time	t_{rr}	$T_J = 125\text{ }^{\circ}\text{C}$ $I_F = 15\text{ A}$, $dI_F/dt = 800\text{ A}/\mu\text{s}$, $V_R = 390\text{ V}$	-	53	-	ns
Peak recovery current	I_{RRM}		-	25	-	A
Reverse recovery charge	Q_{rr}		-	730	-	nC

THERMAL - MECHANICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T_J , T_{Stg}		-65	-	175	$^{\circ}\text{C}$
Thermal resistance, junction-to-case	R_{thJC}		-	3.7	4.3	$^{\circ}\text{C}/\text{W}$
Thermal resistance, junction-to-ambient	R_{thJA}	Typical socket mount	-	-	70	
Typical thermal resistance, case-to-heatsink	R_{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	2	-	g
			-	0.07	-	oz.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-220 FullPAK 2L	ETU1506FP			

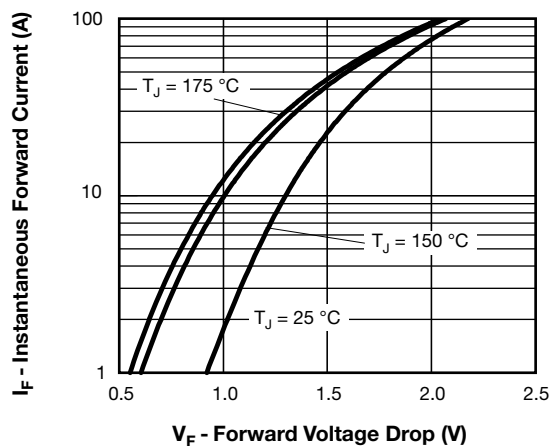


Fig. 1 - Typical Forward Voltage Drop Characteristics

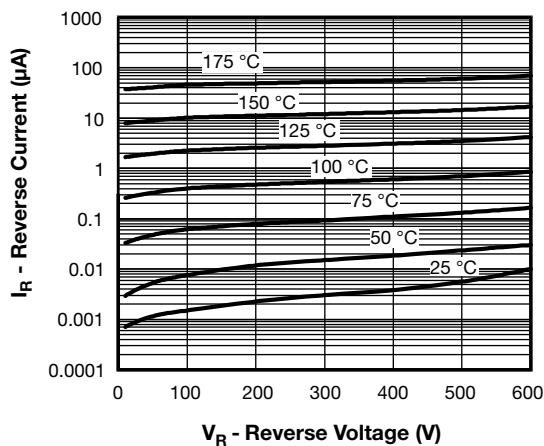


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

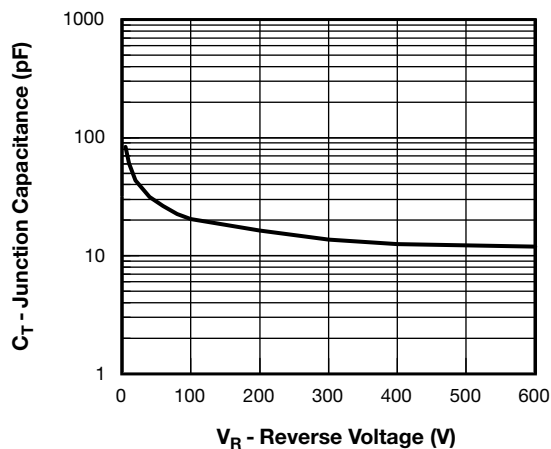
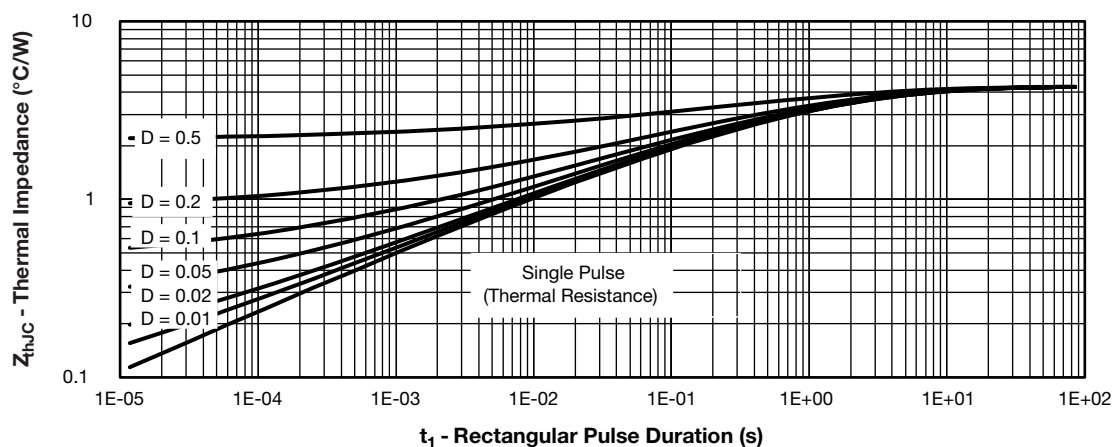


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

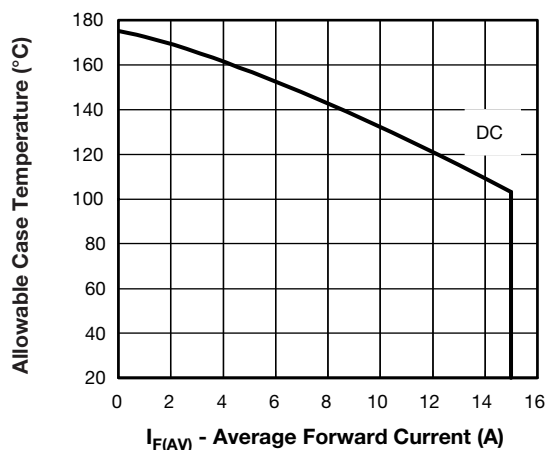


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

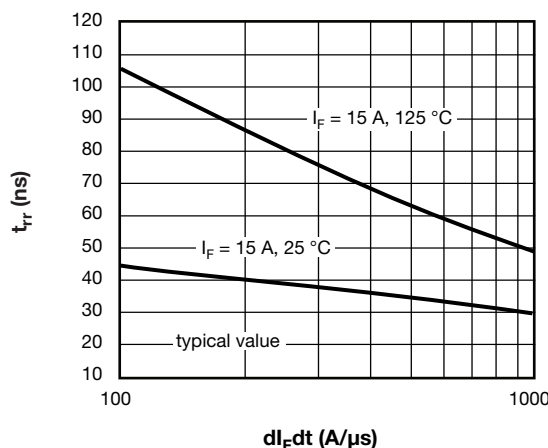


Fig. 7 - Typical Reverse Recovery vs. dI_F/dt

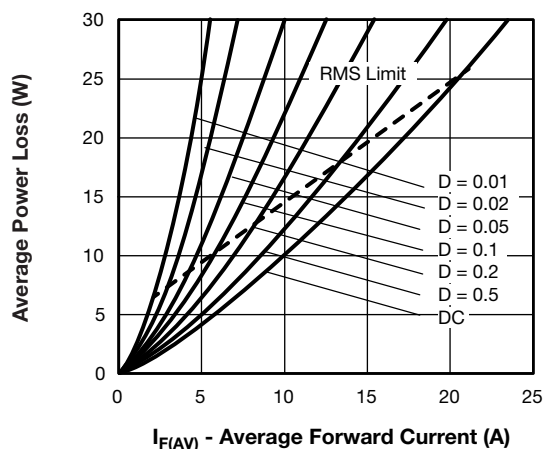


Fig. 6 - Forward Power Loss Characteristics

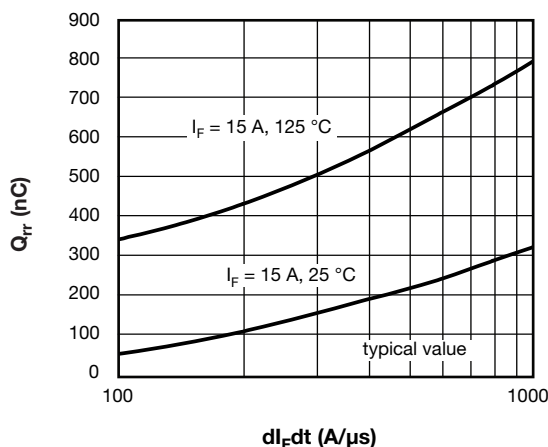


Fig. 8 - Typical Stored Charge vs. dI_F/dt

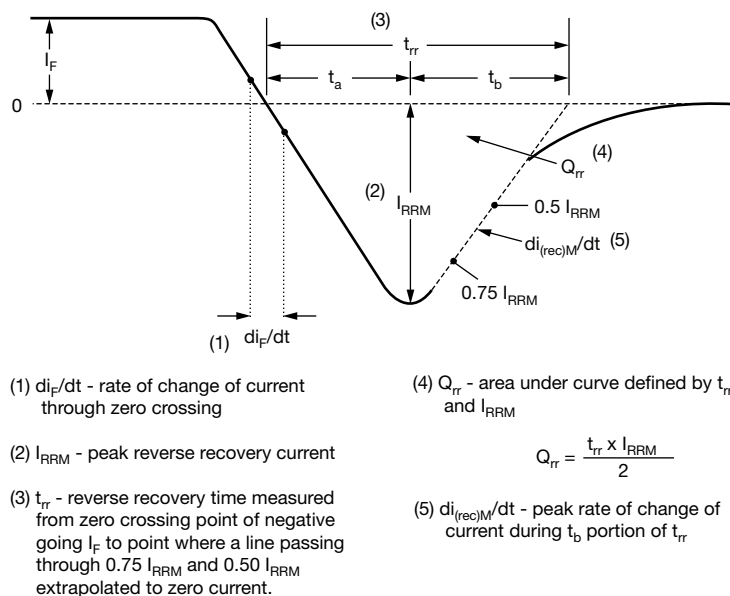


Fig. 9 - Reverse Recovery Waveform and Definitions

**ORDERING INFORMATION TABLE**

Device code	VS-	E	T	U	15	06	FP	-M3
	1	2	3	4	5	6	7	8

- 1** - Vishay Semiconductors product
- 2** - Circuit configuration:
E = single
- 3** - T = TO-220
- 4** - U = hyperfast recovery time
- 5** - Current code: 15 = 15 A
- 6** - Voltage code: 06 = 600 V
- 7** - FP = TO-220 FullPAK 2L
- 8** - Environmental digit:
-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)

PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-ETU1506FP-M3	50	1000	Antistatic plastic tube

LINKS TO RELATED DOCUMENTS

Dimensions	www.vishay.com/doc?96157
Part marking information	www.vishay.com/doc?95392
SPIICE model	www.vishay.com/doc?96131



2L TO-220 FullPAK

DIMENSIONS in millimeters





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