VS-UFB130FA60

Vishay Semiconductors

Insulated Ultrafast Rectifier Module, 130 A



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PRIMARY CHARACTERISTICS							
V _R	600 V						
$I_{F(AV)}$ per module at $T_C = 92 \text{ °C}$	130 A						
t _{rr}	37 ns						
Туре	Modules - diode FRED Pt®						
Package	SOT-227						

FEATURES

- Two fully independent diodes
- · Fully insulated package
- Ultrafast, soft reverse recovery, with high operation junction temperature (T₁ max. = $175 \,^{\circ}$ C)
- Low forward voltage drop
- Optimized for power conversion: welding and industrial SMPS applications
- · Easy to use and parallel
- Industry standard outline
- UL approved file E78996
- Designed and qualified for industrial level
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

The VS-UFB130FA60 insulated modules integrate two state of the art ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The diodes structure, and its life time control, provide an ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, DC/DC converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Cathode to anode voltage	V _R		600	V		
Continuous forward current per diode	l _F	T _C = 85 °C	82	А		
Single pulse forward current per diode	I _{FSM}	$T_{\rm C} = 25 \ ^{\circ}{\rm C}$	750	A		
Maximum power dissipation per module	PD	T _C = 85 °C	246	W		
RMS isolation voltage	VISOL	Any terminal to case, t = 1 min	2500	V		
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C		



RoHS COMPLIANT



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ELECTRICAL SPECIFICATIONS PER DIODE ($T_J = 25 \ ^{\circ}C$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	I _R = 100 μA	600	-	-	
	M	I _F = 60 A	-	1.43	1.80	
Forward voltage		I _F = 60 A, T _J = 125 °C	-	1.23	1.48	V
Torward voltage	V _{FM}	I _F = 120 A	-	1.66	2.10	
			-	1.50	1.82	
Reverse leakage current		$V_{R} = V_{R}$ rated	-	0.1	50	μA
	I _{RM}	$T_J = 175 \text{ °C}, V_R = V_R \text{ rated}$	-	0.20	1	mA
Junction capacitance	CT	V _R = 600 V	-	43	-	pF

DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CC	MIN.	TYP.	MAX.	UNITS	
		I _F = 1.0 A, dI _F /dt = 200 A/μs, V _R = 30 V		-	37	-	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	79	-	ns A
		T _J = 125 °C		-	164	-	
Dealement		T _J = 25 °C	I _F = 50 A dI _F /dt = 200 A/μs V _R = 200 V	-	6	-	
Peak recovery current	I _{RRM}	T _J = 125 °C		-	15	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	230	-	nC
		T _J = 125 °C		-	1220	-	no

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction-to-case, single leg conducting	D		-	-	0.73	
Junction-to-case, both leg conducting	R _{thJC}		-	-	0.365	°C/W
Case to heatsink	R _{thCS}	Flat, greased surface	-	0.10	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style				SOT-227		

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V_R - Reverse Voltage (V)

Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

 Revision: 18-Sep-2018
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 Document Number: 93657

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Fig. 6 - Forward Power Loss Characteristics (Per Leg)





Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt







Fig. 9 - Typical Stored Current vs. dl_F/dt

Note

Average Power Loss (W)

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 Pd_{REV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = 80 % rated V_R

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Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6);

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Fig. 10 - Reverse Recovery Parameter Test Circuit



Fig. 11 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

Device code	VS-	UF	В	130	F	Α	60
		2	3	4	5	6	7
	1 -	- Vishay Semiconductors product					
	2 -	- Ultrafast rectifier					
	3 -	- Ultrafast Pt diffused					
	4 -	Cur	rent rati	ng (130	= 130 A	۸)	
	5 -	Circ	uit conf	iguratior	n (two s	eparate	diodes
	6 -	Pac	kage in	dicator (SOT-22	27 stanc	lard ins
	7 -	Vol	age rati	ng (60 =	= 600 V)		

CIRCUIT CONFI	GURATION	
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING
Two separate diodes, parallel pin-out	F	Lead Assignment

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95423				
Packaging information	www.vishay.com/doc?95425				

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SOT-227 Generation 2

DIMENSIONS in millimeters (inches)



Note

• Controlling dimension: millimeter



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Revision: 01-Jan-2025

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