

COMPLIANT

High Performance Schottky Rectifier, 220 A



PRIMARY CHARACTERISTICS					
I _{F(AV)}	220 A				
V_{R}	30 V				
Package	TO-244				
Circuit configuration	Two diodes common cathode				

FEATURES

- 150 °C T_J operation
- · Center tap module
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- UL approved file E222165
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

The VS-220CNQ.. center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature.

The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES				
I _{F(AV)}	Rectangular waveform	220	Α			
V _{RRM}		30	V			
I _{FSM}	t _p = 5 μs sine	18 000	Α			
V _F	110 A _{pk} , T _J = 125 °C (per leg)	0.41	V			
T _J	Range	-55 to +150	°C			

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-220CNQ030PbF	UNITS	
Maximum DC reverse voltage	V_R	30	V	
Maximum working peak reverse voltage	V _{RWM}	30	V	

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward current	per leg		EO 9/ duty quale et T 100 90 vector guilar yapuafave		50 % data analo at T = 100 % anatom da una aform		
See fig. 5	per device	I _{F(AV)}	50 % duty cycle at T _C = 122 °C, rectangular waveform		^		
Maximum peak one cycle non-repetitive			5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	18 000	- A	
surge current per leg See fig. 7		IFSM	10 ms sine or 6 ms rect. pulse		1950		
Non-repetitive avalanche	energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 15 A, L = 1 mH		99	mJ	
Repetitive avalanche cur	rent per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		22	Α	



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	110 A	T _{.1} = 25 °C	0.49	V
Maximum forward voltage drop per leg		220 A	1j=25 C	0.59	
See fig. 1		110 A	T _ 105 °C	0.41	
		220 A	T _J = 125 °C	0.55	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm R}$ = Rated $V_{\rm R}$	10	- mA
See fig. 2		T _J = 125 °C	v _R = nateu v _R	650	
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		7400	pF
Typical series inductance per leg	L _S	From top of terminal hole to mounting plane		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}	-55	-	150	°C	
Thermal registeres innetion to once	per leg	В	-	-	0.38	°C/W	
Thermal resistance, junction to case	per module	R_{thJC}	-	-	0.19		
Thermal resistance, case to heatsink		R _{thCS}	-	0.10	-	-	
Weight				68		g	
			-	2.4	-	OZ.	
Mounting torque Mounting torque center hole Terminal torque			35.4 (4)	-	53.1 (6)		
			30 (3.4)	-	40 (4.6)	lbf ⋅ in (N ⋅ m)	
			30 (3.4)	-	44.2 (5)	,	
Vertical pull			-	-	80	llef in	
2" lever pull			-	-	35	lbf ⋅ in	

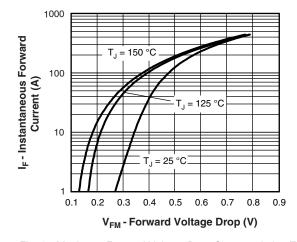


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

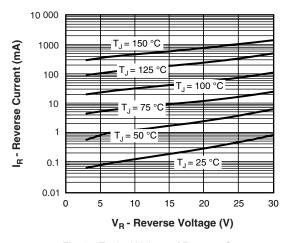


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

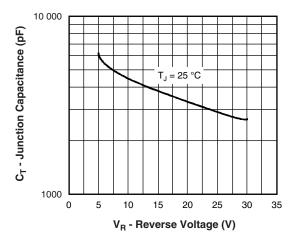


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

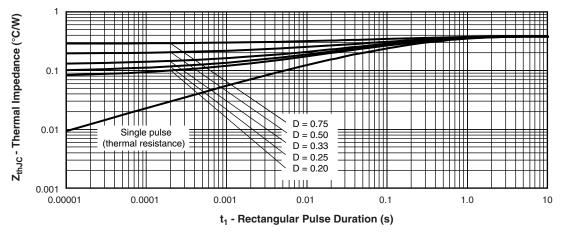


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

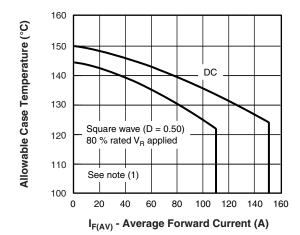


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

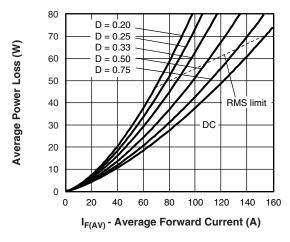
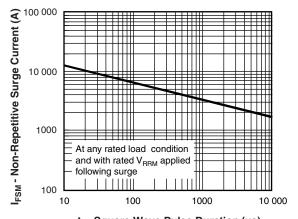


Fig. 6 - Forward Power Loss Characteristics (Per Leg)



t_p - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

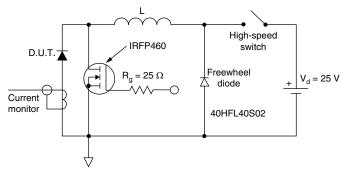


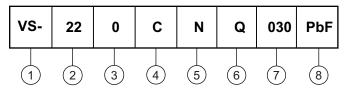
Fig. 8 - Unclamped Inductive Test Circuit

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

ORDERING INFORMATION TABLE

Device code



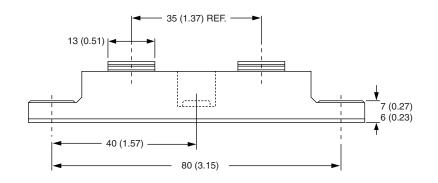
- 1 Vishay Semiconductors product
- Average current rating (x 10)
- Product silicon identification
- C = circuit configuration
- 5 N = not isolated
- Q = Schottky rectifier diode
- 7 Voltage rating (30 V)
- 8 Lead (Pb)-free

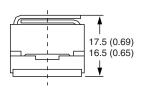
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95021			

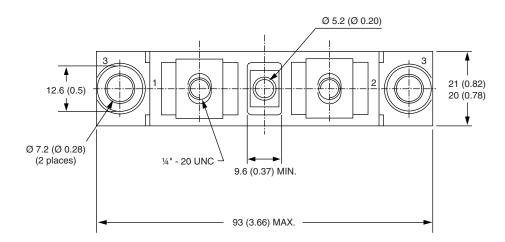


TO-244

DIMENSIONS in millimeters (inches)









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