

# High Performance Schottky Rectifier, 2 A



SMA (DO-214AC)



## FEATURES

- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)


RoHS  
COMPLIANT

## LINKS TO ADDITIONAL RESOURCES



3D Models

## PRIMARY CHARACTERISTICS

$I_{F(AV)}$	2 A
$V_R$	100 V
$V_F$ at $I_F$	0.72 V
$I_{RM}$ max.	1 mA at 125 °C
$T_J$ max.	150 °C
$E_{AS}$	1.0 mJ
Package	SMA (DO-214AC)
Circuit configuration	Single

## DESCRIPTION

The VS-20MQ100NTRPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

## MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	2	A
$V_{RRM}$		100	V
$I_{FSM}$	$t_p = 5 \mu s$ sine	120	A
$V_F$	$2 A_{pk}$ , $T_J = 125 \text{ }^\circ\text{C}$	0.72	V
$T_J$	Range	-55 to +150	°C

## VOLTAGE RATINGS

PARAMETER	SYMBOL	VS-20MQ100NTRPbF	UNITS
Maximum DC reverse voltage	$V_R$	100	V
Maximum working peak reverse voltage	$V_{RWM}$		

## ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current See fig. 4	$I_{F(AV)}$	50 % duty cycle at $T_C = 113 \text{ }^\circ\text{C}$ , rectangular waveform On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad area)	2.1	A
		50 % duty cycle at $T_C = 116 \text{ }^\circ\text{C}$ , rectangular waveform On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad area)	2	
Maximum peak one cycle non-repetitive surge current See fig. 6	$I_{FSM}$	5 $\mu s$ sine or 3 $\mu s$ rect. pulse	120	A
		10 ms sine or 6 ms rect. pulse	30	
Non-repetitive avalanche energy	$E_{AS}$	$T_J = 25 \text{ }^\circ\text{C}$ , $I_{AS} = 0.5 \text{ A}$ , $L = 8 \text{ mH}$	1.0	mJ
Repetitive avalanche current	$I_{AR}$		0.5	A

**ELECTRICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum forward voltage drop See fig. 1	$V_{FM}^{(1)}$	2 A	0.91	V
		1.5 A	0.85	
		1 A	0.78	
		2 A	0.72	
		1.5 A	0.68	
		1 A	0.63	
Maximum reverse leakage current See fig. 2	$I_{RM}^{(1)}$	$T_J = 25\text{ }^{\circ}\text{C}$	0.1	mA
		$T_J = 125\text{ }^{\circ}\text{C}$	1	
Threshold voltage	$V_{F(TO)}$	$T_J = T_J \text{ maximum}$	0.52	V
Forward slope resistance	$r_t$		78.4	m $\Omega$
Typical junction capacitance	$C_T$	$V_R = 10\text{ V}_{DC}$ , $T_J = 25\text{ }^{\circ}\text{C}$ , test signal = 1 MHz	38	pF
Typical series inductance	$L_S$	Measured lead to lead 5 mm from package body	2.0	nH
Maximum voltage rate of change	dV/dt	Rated $V_R$	10 000	V/ $\mu$ s

**Note**(1) Pulse width < 300  $\mu$ s, duty cycle < 2 %**THERMAL - MECHANICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	$T_J^{(1)}$ , $T_{Stg}$		-55 to +150	$^{\circ}\text{C}$
Maximum thermal resistance, junction to ambient	$R_{thJA}$	DC operation	80	$^{\circ}\text{C/W}$
Approximate weight			0.07	g
			0.002	oz.
Marking device		Case style SMA (DO-214AC) (similar D-64)	2J	

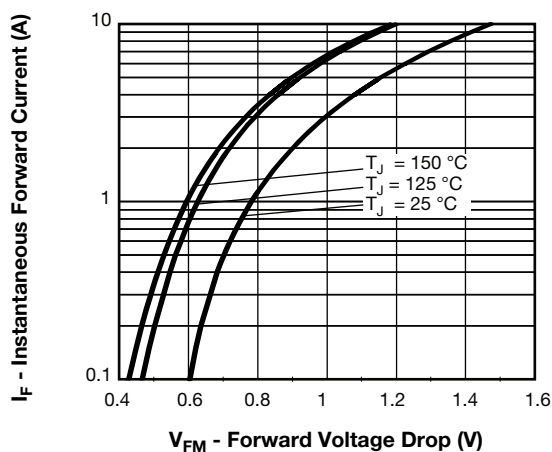
**Note**(1)  $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink

Fig. 1 - Maximum Forward Voltage Drop Characteristics

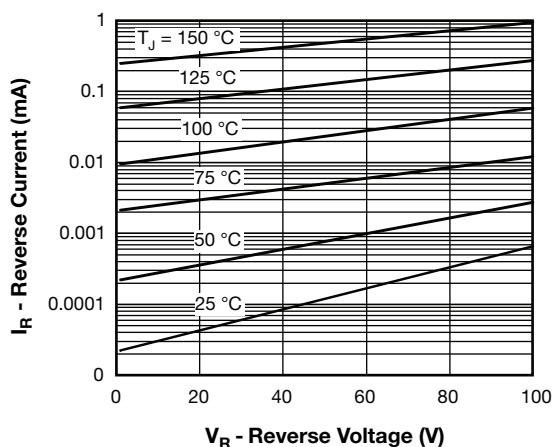


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

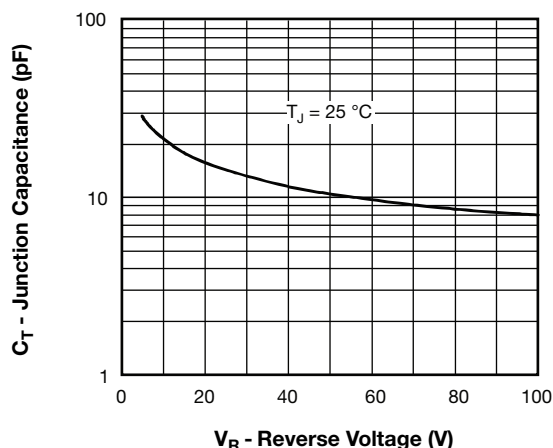


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

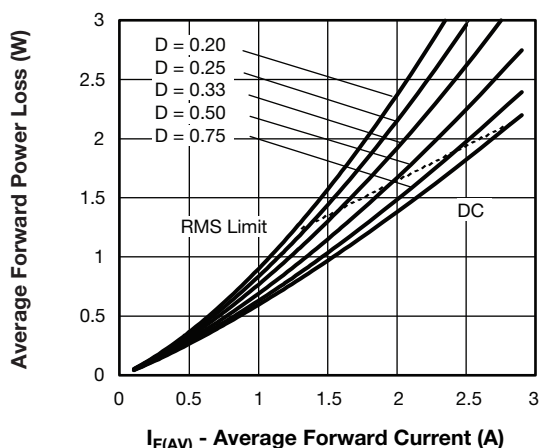


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current

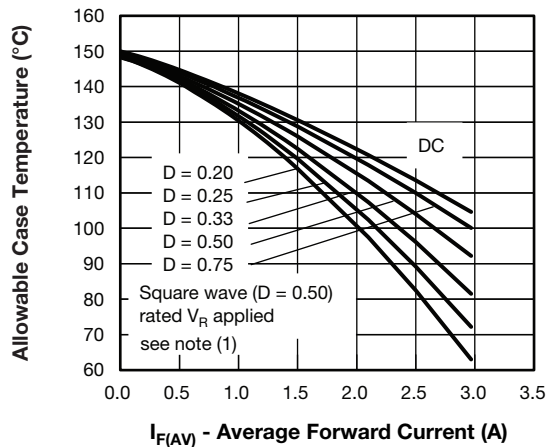


Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature

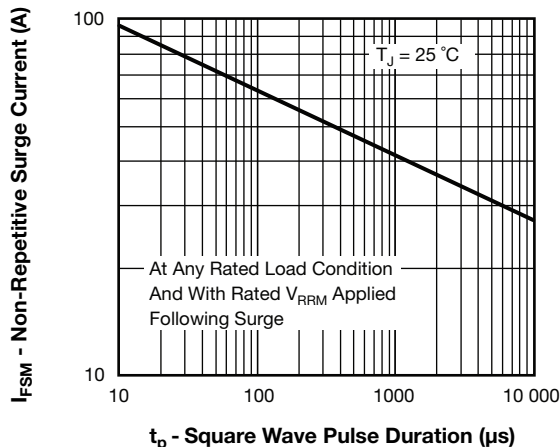


Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

#### Note

- (1) Formula used:  $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$ ;  
 $P_d$  = forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  
 $P_{dREV}$  = inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 80\%$  rated  $V_R$

## ORDERING INFORMATION TABLE

Device code	<b>VS-</b>	<b>20</b>	<b>M</b>	<b>Q</b>	<b>100</b>	<b>N</b>	<b>TR</b>	<b>PbF</b>
	1	2	3	4	5	6	7	8

- 1 - Vishay Semiconductors product
- 2 - Current rating
- 3 - M = SMA
- 4 - Q = Schottky "Q" series
- 5 - Voltage rating (100 = 100 V)
- 6 - N = new SMA
- 7 - TR = tape and reel (7500 pieces)
- 8 - PbF = lead (Pb)-free

### ORDERING INFORMATION (Example)

PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-20MQ100NTRPbF	5AT	7500	13" diameter plastic tape and reel

## LINKS TO RELATED DOCUMENTS

LINKS TO RELATED DOCUMENTS	
Dimensions	<a href="http://www.vishay.com/doc?95400">www.vishay.com/doc?95400</a>
Part marking information	<a href="http://www.vishay.com/doc?95403">www.vishay.com/doc?95403</a>
Packaging information	<a href="http://www.vishay.com/doc?95404">www.vishay.com/doc?95404</a>
SPICE model	<a href="http://www.vishay.com/doc?97121">www.vishay.com/doc?97121</a>

## SMA

**DIMENSIONS** in inches (millimeters)

**DO-214AC (SMA)**



**Mounting Pad Layout**





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