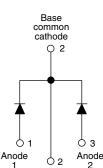
VS-HFA50PA60CHN3

Vishay Semiconductors

HEXFRED[®] Ultrafast Soft Recovery Diode, 2 x 25 A



www.vishay.com



Common cathode

PRODUCT SUMMARY							
Package	TO-247AC						
I _{F(AV)}	2 x 25 A						
V _R	600 V						
V _F at I _F	1.3 V						
t _{rr} typ.	23 ns						
T _J max.	150 °C						
Diode variation	Single die						

FEATURES

- Ultrafast and ultrasoft recovery
- Very low I_{RRM} and Q_{rr}
- AEC-Q101 qualified, meets JESD 201 class 1A whisker test
- Material categorization:
- for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



RoHS COMPLIANT HALOGEN FREE

BENEFITS

- Reduced RFI and EMI
- · Reduced power loss in diode and switching transistor
- Higher frequency operation
- Reduced snubbing
- Reduced parts count

DESCRIPTION

VS-HFA50PA60CHN3 is a state of the art center tap ultrafast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. With basic ratings of 600 V and 25 A per leg continuous current, the VS-HFA50PA60CHN3 is especially well suited for use as the companion diode for IGBTs and MOSFETs. In addition to ultrafast recovery time, the HEXFRED[®] product line features extremely low values of peak recovery current (IRRM) and does not exhibit any tendency to "snap-off" during the t_b portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED VS-HFA50PA60CHN3 is ideally suited for applications in power supplies and power conversion systems (such as inverters), motor drives, and many other similar applications where high speed, high efficiency is needed.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Cathode to anode voltage	V _R		600	V					
Maximum continuous forward current per leg	I_	T _C = 100 °C	25						
per device	IF	1C = 100 C	50	А					
Single pulse forward current	I _{FSM}		225	A					
Maximum repetitive forward current	I _{FRM}		100						
Maximum power dissipation	р	T _C = 25 °C	150	W					
	P _D	T _C = 100 °C	60	VV					
Operating junction and storage temperature range	T _J , T _{Stg}		-55 to +150	°C					

Revision: 15-Jul-15

Document Number: 94678

For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>

www.vishay.com

VISHAY

VS-HFA50PA60CHN3

Vishay Semiconductors

ELECTRICAL SPECIFICATIONS PER LEG ($T_J = 25 \text{ °C}$ unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS		
Cathode to anode breakdown voltage	V _{BR}	I _R = 100 μA		600	-	-			
Maximum forward voltage		I _F = 25 A		-	1.3	1.7	v		
	V _{FM}	I _F = 50 A	See fig. 1	-	1.5	2.0			
		I _F = 25 A, T _J = 125 °C		-	1.3	1.7			
Maximum reverse	1	$V_R = V_R$ rated	Coofig 0	-	1.5	20			
leakage current	IRM	$T_J = 125 \text{ °C}, V_R = 0.8 \text{ x } V_R \text{ rated}$	See fig. 2	-	600	2000	μA		
Junction capacitance	CT	V _R = 200 V See fig. 3		-	55	100	pF		
Series inductance	L _S	Measured lead to lead 5 mm from p	ackage body	-	12	-	nH		

DYNAMIC RECOVERY CHARACTERISTICS (T_J = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS			
	t _{rr}	$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 200$	A/μs, V _R = 30 V	-	23	-			
Reverse recovery time See fig. 5, 10	t _{rr1}	T _J = 25 °C		-	50	-	ns		
	t _{rr2}	T _J = 125 °C	I _F = 25 A dI _F /dt = 200 A/μs V _R = 200 V	-	105	-			
Peak recovery current See fig. 6	I _{RRM1}	T _J = 25 °C		-	4.5	-	A		
	I _{RRM2}	T _J = 125 °C		-	8.0	-			
Reverse recovery charge	Q _{rr1}	T _J = 25 °C		-	112	-	nC		
See fig. 7	Q _{rr2}	T _J = 125 °C		-	420	-	nc		
Peak rate of fall of recovery	dl _{(rec)M} /dt1	T _J = 25 °C		-	250	-	A∕µs		
current during t _b See fig. 8	dl _{(rec)M} /dt2	T _J = 125 °C		-	160	-	-ν μο		

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Lead temperature	T _{lead}	0.063" from case (1.6 mm) for 10 s	-	-	300	°C			
Junction to case, single leg conducting	P		-	-	0.83				
Junction to case, both legs conducting	— R _{thJC}		-	-	0.42				
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	40	K/W			
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.25	-				
Maight			-	6.0	-	g			
Weight			-	0.21	-	oz.			
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)			
Marking device		Case style TO-247AC		HFA50	PA60CH				

Revision: 15-Jul-15

2

Document Number: 94678



VS-HFA50PA60CHN3

Vishay Semiconductors

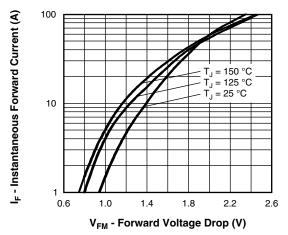
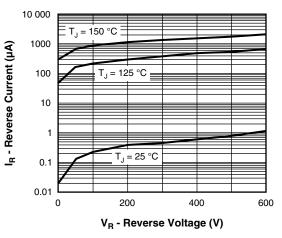
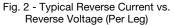


Fig. 1 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current (Per Leg)





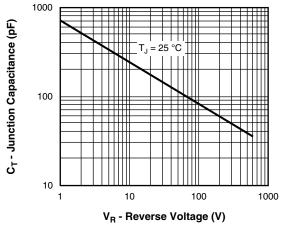
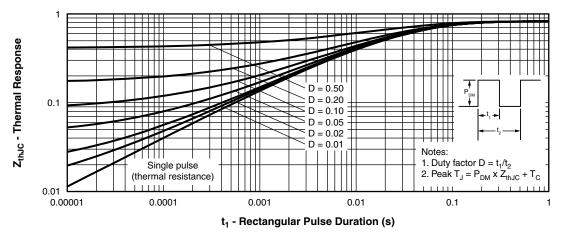


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





 Revision: 15-Jul-15
 3
 Document Number: 94678

 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com
 THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

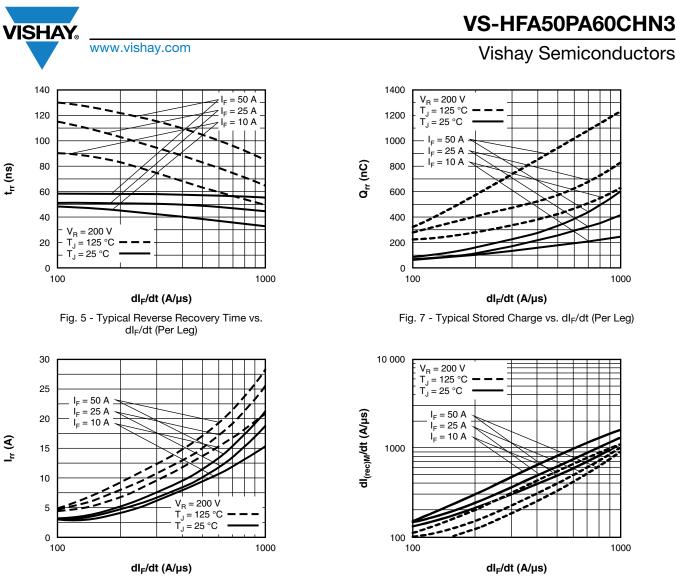


Fig. 6 - Typical Recovery Current vs. dl_F/dt (Per Leg)



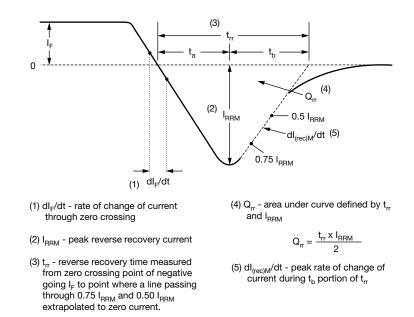


Fig. 9 - Reverse Recovery Waveform and Definitions

Revision: 15-Jul-15	4	Document Number: 94678
For technical questions within your region	: DiodesAmericas@vishay.com, DiodesAsia@vishay.co	m, <u>DiodesEurope@vishay.com</u>
	E WITHOUT NOTICE. THE PRODUCTS DESCRIBED I CIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com</u>	

evice code	VS-	HF	A	50	PA	60	С	н	N3
	1	2	3	4	5	6	7	8	9
	 Vishay Semiconductors product HEXFRED[®] family Electron irradiated Current rating (50 = 50 A) PA = TO-247AC Voltage rating: (60 = 600 V) Circuit configuration 								
	8 - 9 -	C = H = Env	commo AEC-Q ironmer	on catho 101 qua ntal digit en-free,	de lified :	complia	nt, and f	totally le	ad (Pb)

ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-HFA50PA60CHN3	25	500	Antistatic plastic tube					

LINKS TO RELATED DOCUMENTS							
Dimensions		www.vishay.com/doc?95223					
Part marking information	TO-247AC-N3	www.vishay.com/doc?95007					

VISHAY www.vishay.com **ORDERING INFORMATION TABLE** Dev

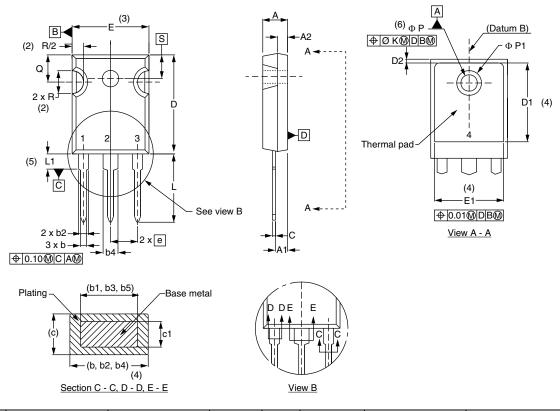
Vishay Semiconductors



Vishay Semiconductors

TO-247AC

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INC	INCHES		OTES SYMBOL		MILLIN	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209			D2	0.51	1.30	0.020	0.051	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.50	2.49	0.059	0.098			E1	13.46	-	0.530	-	
b	0.99	1.40	0.039	0.055			e	5.46	BSC	0.215	5 BSC	
b1	0.99	1.35	0.039	0.053			ØК	0.2	254	0.0	010	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			ØР	3.56	3.66	0.14	0.144	
b5	2.59	3.38	0.102	0.133			Ø P1	-	6.98	-	0.275	
С	0.38	0.89	0.015	0.035			Q	5.31	5.69	0.209	0.224	
c1	0.38	0.84	0.015	0.033			R	4.52	5.49	0.178	0.216	
D	19.71	20.70	0.776	0.815	3		S	S 5.51 BSC 0.217 BSC		' BSC		
D1	13.08	-	0.515	-	4							

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁴⁾ Thermal pad contour optional with dimensions D1 and E1

⁽⁵⁾ Lead finish uncontrolled in L1

⁽⁶⁾ Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

 $^{(7)}\,$ Outline conforms to JEDEC $^{\tiny (\! R)}$ outline TO-247 with exception of dimension c

Revision: 11-Dec-2019

1



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

© 2025 VISHAY INTERTECHNOLOGY, INC. ALL RIGHTS RESERVED

Revision: 01-Jan-2025

1