

RoHS

COMPLIANT

HALOGEN

FREE

## Ultrafast Rectifier, 8 A FRED Pt®



TO.	.220	Ful	ΙΡΔΚ	21

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	8 A			
$V_{R}$	500 V			
V <sub>F</sub> at I <sub>F</sub>	0.9 V			
t <sub>rr</sub> (typ.)	28 ns			
T <sub>J</sub> max.	175 °C			
Package	TO-220 FullPAK 2L			
Circuit configuration	Single			

#### **FEATURES**

- Low forward voltage drop
- · Ultrafast soft recovery time
- 175 °C operating junction temperature
- · Low leakage current
- Fully isolated package (V<sub>INS</sub> = 2500 V<sub>RMS</sub>)
- True 2 pin package
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **DESCRIPTION**

Ultralow  $V_F$ , soft-switching ultrafast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

#### **APPLICATIONS**

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units and DVD AC/DC power supplies.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	$V_{RRM}$		500	V	
Average rectified forward current in DC	I <sub>F(AV)</sub>	T <sub>C</sub> = 124 °C	8	۸	
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	110	A	
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C	

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	L TEST CONDITIONS		TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	500	-	-	.,
Forward voltage	V	I <sub>F</sub> = 8 A	=.	1.05	1.25	V
	$V_{F}$	I <sub>F</sub> = 8 A, T <sub>J</sub> = 150 °C	=.	0.9	1.03	
Reverse leakage current		$V_R = V_R$ rated	-	0.005	9	
	IR	$T_J = 150 ^{\circ}\text{C},  V_R = V_R  \text{rated}$	-	5	50	μA
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 500 V	-	6	-	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body -		8	-	nH

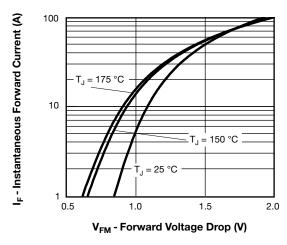




<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t <sub>rr</sub>	$I_F = 1 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		1	28	-	
		$I_F = 8 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		ı	54	-	
		T <sub>J</sub> = 25 °C	I <sub>F</sub> = 8 A, dI <sub>F</sub> /dt = 200 A/μs, V <sub>R</sub> = 200 V,	-	50	-	ns
		T <sub>J</sub> = 125 °C		-	90	-	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	7.0	-	۸
		T <sub>J</sub> = 125 °C		-	10	-	A
Reverse recovery charge	0	T <sub>J</sub> = 25 °C		-	180	-	nC
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	450	-	IIC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C
Thermal resistance, junction-to-case	R <sub>thJC</sub>		-	4.4	5.5	
Thermal resistance, junction-to-ambient	R <sub>thJA</sub>	Typical socket mount	-	-	50	°C/W
Typical thermal resistance, case-to-heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-	
Waight			-	2.0	-	g
Weight			-	0.007	-	OZ.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-220 FullPAK 2L	ETU0805FP			







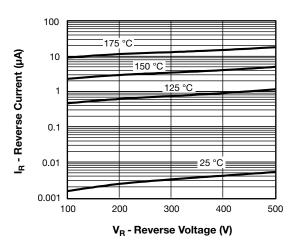


Fig. 2 - Typical Values of Reverse Current vs.
Reverse Voltage

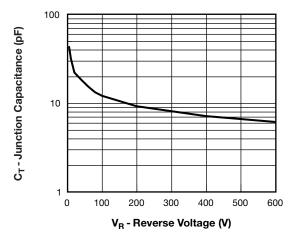


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

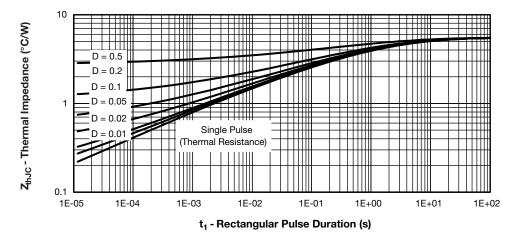


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

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## Vishay Semiconductors

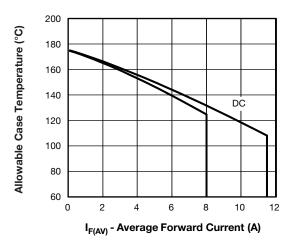


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

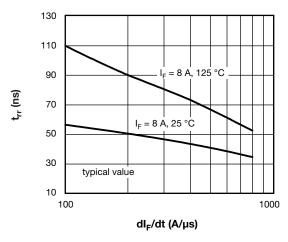


Fig. 7 - Typical Reverse Recovery vs.  $dI_F/dt$ 

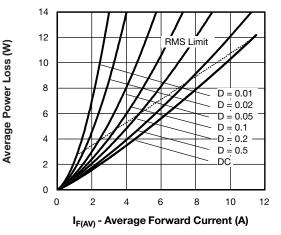


Fig. 6 - Forward Power Loss Characteristics

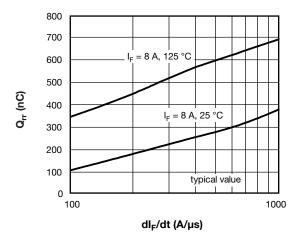
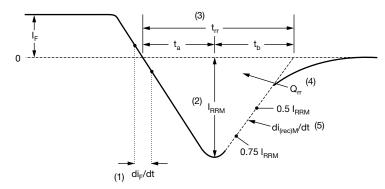


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3) t<sub>rr</sub> reverse recovery time measured from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75 I<sub>RRM</sub> and 0.50 I<sub>RRM</sub> extrapolated to zero current.
- (4)  $\mathbf{Q}_{rr}$  area under curve defined by  $\mathbf{t}_{rr}$  and  $\mathbf{I}_{RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

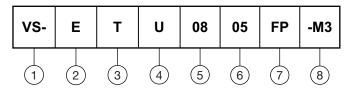
(5)  $di_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

Fig. 9 - Reverse Recovery Waveform and Definitions



#### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Circuit configuration:

E = single

**3** - T = TO-220

U = hyperfast recovery time

5 - Current code: 08 = 8 A

6 - Voltage code: 05 = 500 V

7 - FP = TO-220 FullPAK 2L

8 - Environmental digit:

-M3 = halogen-free, RoHS compliant, and terminations lead (Pb)-free

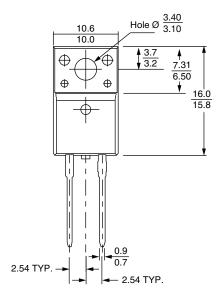
ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-ETU0805FP-M3	50	1000	Antistatic plastic tube		

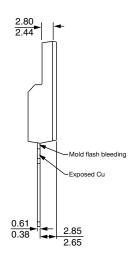
LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?96157</u>					
Part marking information	www.vishay.com/doc?95392				

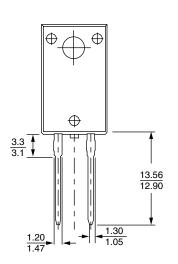


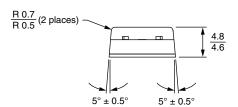
### 2L TO-220 FullPAK

#### **DIMENSIONS** in millimeters









Bottom view



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Vishay

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