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Vishay Semiconductors

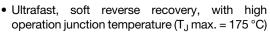
Insulated Ultrafast Rectifier Module, 170 A



PRIMARY CHARACTERISTICS						
V_R	600 V					
$I_{F(AV)}$ per module at $T_C = 76 ^{\circ}C$	170 A					
t _{rr}	60 ns					
Type	Modules - diode FRED Pt®					
Package	SOT-227					

FEATURES

- Two fully independent diodes
- Fully insulated package





RoHS COMPLIANT

- Very low forward voltage drop
- Optimized for power conversion: welding and industrial SMPS applications
- Easy to use and parallel
- · Industry standard outline
- UL approved file E78996
 - --
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

The VS-UFB170FA60 insulated modules integrate two state of the art ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The diodes structure, and its life time control, provide an ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, DC/DC converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

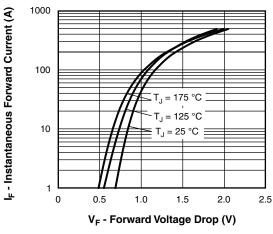
ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Cathode to anode voltage	V _R		600	V
Continuous forward current per diode	l _F	T _C = 90 °C	94	۸
Single pulse forward current per diode	I _{FSM}	T _C = 25 °C	850	Α
Maximum power dissipation per module	P _D	T _C = 90 °C	233	W
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C



ELECTRICAL SPECIFICATIONS PER DIODE (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	I _R = 100 μA	600	-	-	
Forward voltage, per leg	V _{FM}	I _F = 50 A	-	1.02	1.19	
		I _F = 50 A, T _J = 175 °C	-	0.87	1	V
		I _F = 100 A	-	1.17	1.43	
		I _F = 100 A, T _J = 175 °C	-	1.03	-	
Reverse leakage current, per leg	I _{RM}	V _R = V _R rated	-	0.5	50	μA
		T _J = 175 °C, V _R = V _R rated	-	0.1	2	mA
Junction capacitance, per leg	C _T	V _R = 600 V	-	43	-	pF

DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$T_J = 25 ^{\circ}\text{C}, \ I_F = 1 \text{A dI}_F / \text{dt} = 200 \text{A} / \mu \text{s} V_R = 30 \text{V}$		-	60	-	
Reverse recovery time, per leg	t _{rr}	T _J = 25 °C		-	170	-	ns
		T _J = 125 °C		-	270	-	
Peak recovery current, per leg		T _J = 25 °C	$I_F = 25 \text{ A}$ $dI_F/dt = 500 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	40	-	A
	I _{RRM}	T _J = 125 °C		-	54	-	
D	Q _{rr}	T _J = 25 °C		-	3.4	-	μС
Reverse recovery charge, per leg		T _J = 125 °C		-	6.8	-	
Boyaraa raaayary tima par lag	t _{rr}	T _J = 25 °C	$I_F = 50 \text{ A}$ $dI_F/dt = 500 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	220	-	- ns
Reverse recovery time, per leg		T _J = 125 °C		-	300	-	
Peak recovery current, per leg	I _{RRM}	T _J = 25 °C		-	47	-	Α
		T _J = 125 °C		-	61	-	
Reverse recovery charge, per leg	Q _{rr}	T _J = 25 °C		-	5.2	-	μС
		T _J = 125 °C		-	9.1	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single leg conducting	D		-	-	0.73	
Junction to case, both leg conducting	R_{thJC}		-	-	0.365	°C/W
Case to heatsink	R _{thCS}	Flat, greased surface	-	0.10	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style				S	OT-227	





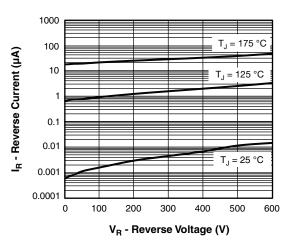


Fig. 2 - Typical Reverse Current vs. Reverse Voltage (Per Leg)

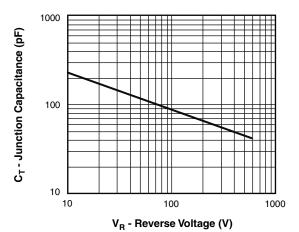


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

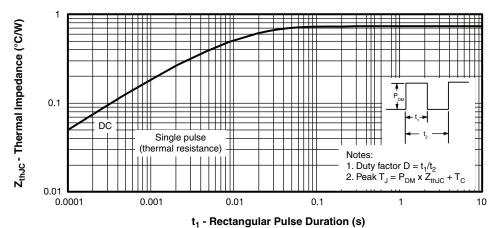


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



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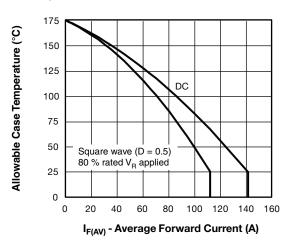
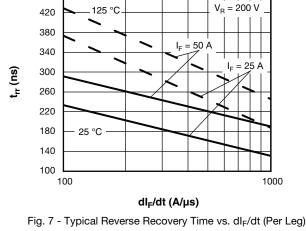


Fig. 5 - Allowable Case Temperature vs. Average Forward Current (Per Leg)



460

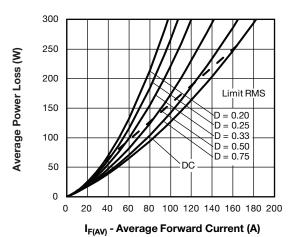


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

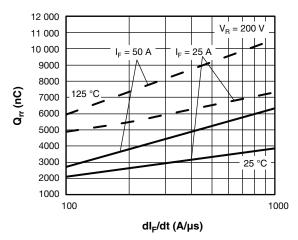


Fig. 8 - Typical Stored Charge vs. dl_F/dt (Per Leg)

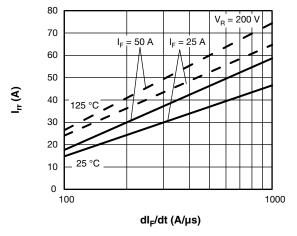


Fig. 9 - Typical Recovery Current vs. dl_F/dt (Per Leg)

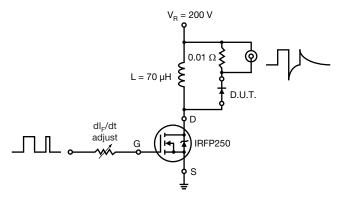
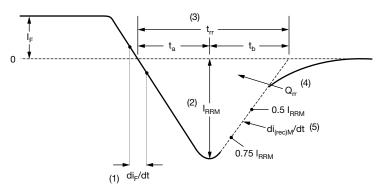


Fig. 10 - Reverse Recovery Parameter Test Circuit



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $t_{\rm rr}$ reverse recovery time measured from zero crossing point of negative going $I_{\rm F}$ to point where a line passing through 0.75 $I_{\rm RRM}$ and 0.50 $I_{\rm RRM}$ extrapolated to zero current.
- (4) $\rm Q_{rr}$ area under curve defined by $\rm t_{rr}$ and $\rm I_{RRM}$

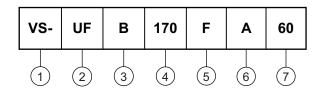
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 11 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Ultrafast rectifier

3 - Ultrafast FRED Pt® diffused

Current rating (170 = 170 A)

5 - Circuit configuration (two separate diodes, parallel pin-out)

6 - Package indicator (SOT-227 standard insulated base)

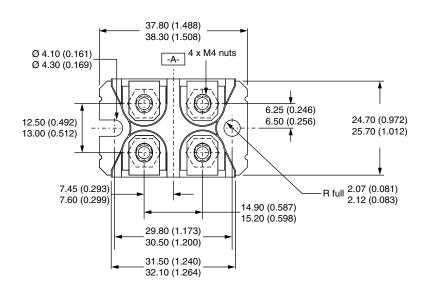
7 - Voltage rating (60 = 600 V)

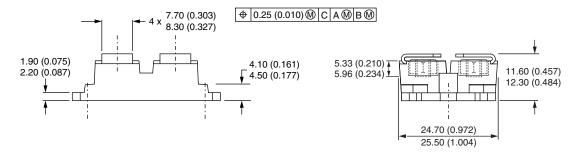
CIRCUIT CONFIGURATION						
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING				
Two separate diodes, parallel pin-out	F	Lead Assignment 4 0 0 3 4 1 0 0 0 2 1				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95423				
Packaging information	www.vishay.com/doc?95425				

SOT-227 Generation 2

DIMENSIONS in millimeters (inches)





Note

· Controlling dimension: millimeter



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