Vishay Semiconductors

Hyperfast Rectifier, 2 A FRED Pt®



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Cathode O Anode

LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 A				
V _R	200 V				
V _F at I _F	0.72 V				
t _{rr}	25 ns				
T _J max.	175 °C				
Package	SlimSMA (DO-221AC)				
Circuit configuration	Single				

FEATURES

- Hyperfast recovery time, reduced Q_{rr}, and soft recovery
- 175 °C maximum operating junction temperature
- Low forward voltage drop
- Low leakage current
- Specific for output and snubber operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, as high frequency rectifiers and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

MECHANICAL DATA

Case: SlimSMA (DO-221AC)

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per J-STD-002

Polarity: color band denotes cathode end

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Peak repetitive reverse voltage	V _{RRM}		200	V		
Average rectified forward current	I _{F(AV)}	$T_{C} = 155 \ ^{\circ}C^{(1)}$	2	А		
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	65	A		
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C		

Note

⁽¹⁾ Device on PCB with 8 mm x 16 mm soldering lands

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	200	-	-	
Forward voltage	V _F	I _F = 2 A	-	0.85	0.93	V
i orward voltage		I _F = 2 A, T _J = 125 °C	-	0.72	0.77	
Reverse leakage current	I _R	$V_R = V_R$ rated	-	-	2	
neverse leakage current		$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	1	8	μA
Junction capacitance	CT	V _R = 200 V	-	10	-	pF

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COMPLIANT

HALOGEN



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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS		
			0 A/µs, V _R = 30 V	-	25	-		
Reverse recovery time	+	I _F = 0.5 A, I _R = 1 A, I _{rr} = 0.25 A		-	-	25	ns	
Reverse recovery time t _{rr}	T _J = 25 °C		-	17	-			
		T _J = 125 °C		-	24	-		
Deals receivers ourrent		T _J = 25 °C	$I_F = 2 A$	-	2	-	А	
Peak recovery current I _{RRM}	T _J = 125 °C	dl _F /dt = 200 A/µs V _B = 160 V	-	3	-	A .		
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	17	-		
	T _J = 125 °C		-	37	-	nC		

THERMAL - MECHANICAL SPECIFICATIONS (T _J = 25 $^{\circ}$ C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	TEST CONDITIONS MIN. TYP. MAX.			UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C	
Thermal resistance, junction to mount	R _{thJM}	Device mounted on PCB with 8 mm x 16 mm soldering lands	-	-	12	°C/W	
Thermal resistance, junction to ambient	R _{thJA}	Device mounted on PCB with 2 mm x 3.5 mm soldering lands	-	-	115	C/VV	
Approximate weight				0.03		g	
Approximate weight				0.0011		oz.	
Marking device		Case style SlimSMA (DO-221AC)		21	H2		

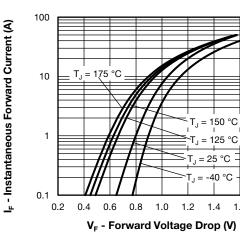


Fig. 1 - Typical Forward Voltage Drop Characteristics

-40 °C =

1.6

1.8

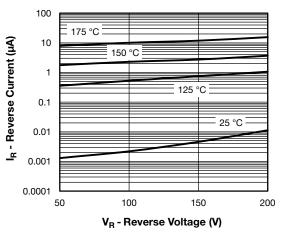
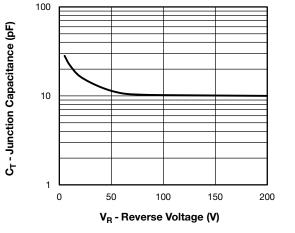


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



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Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

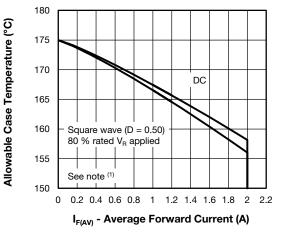


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

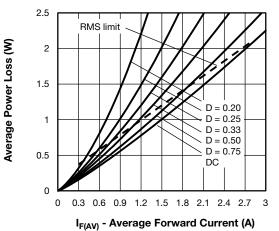


Fig. 5 - Forward Power Loss Characteristics

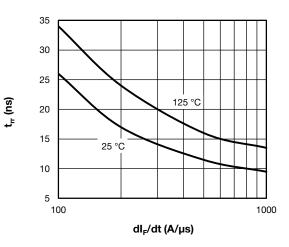


Fig. 6 - Typical Reverse Recovery Time vs. dI_F/dt

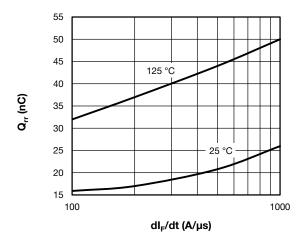


Fig. 7 - Typical Stored Charge vs. dl_F/dt

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{Fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (1 - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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VS-2EJH02-M3

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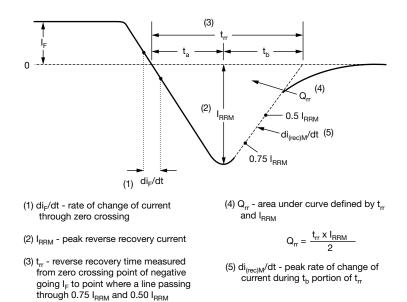


Fig. 8 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

SHAY

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Device code	vs-	2	Е	J	Н	02	-M3
	1	2	3	4	5	6	7
	1	- Visl	nay Sen	niconduo	ctors pro	oduct	
	2 ·	- Cur	rent rati	ng (2 = 2	2 A)		
	3 -	- Circ	cuit conf	iguratior	ר:		
		E =	single o	liode			
	4	- J=	SlimSM	A packa	ige		
	5	- Process type,					
	_	H =	hyperfa	ist recov	very		
	6	- Vol	age coo	de (02 =	200 V)		
	7.	M3	s = halog	gen-free	, RoHS	complia	ant, and

extrapolated to zero current.

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-2EJH02-M3/6A	3500	3500	7"diameter plastic tape and reel				
VS-2EJH02-M3/6B	14 000	14 000	13" diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95571					
Part marking information	www.vishay.com/doc?95562				
Packaging information	www.vishay.com/doc?88869				
SPICE model	www.vishay.com/doc?96861				

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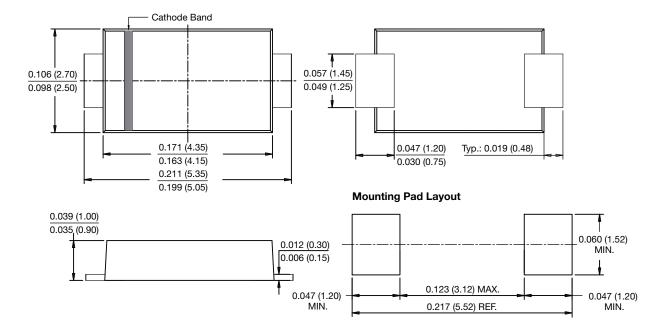
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DO-221AC (SlimSMA)

DIMENSIONS in inches (millimeters)





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