Vishay Semiconductors

High Performance Schottky Rectifier, 1 A



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SMB (DO-214AA)

PRIMARY CHARACTERISTICS						
I _{F(AV)}	1 A					
V _R	100 V					
V _F at I _F	0.59 V					
I _{RM}	1 mA at 125 °C					
T _J max.	175 °C					
E _{AS}	1.0 mJ					
Package	SMB (DO-214AA)					
Circuit configuration	Single					

FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-10BQ100HM3 surface-mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS VALUES UN					
I _{F(AV)}	Rectangular waveform	1	А			
V _{RRM}		100	V			
I _{FSM}	t _p = 5 μs sine	780	Α			
V _F	1.0 A _{pk} , T _J = 125 °C	0.59	V			
TJ	Range	-55 to +175	°C			

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-10BQ100HM3	UNITS			
Maximum DC reverse voltage	V _R	100	V			
Maximum working peak reverse voltage	V _{RWM}	100	v			

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDI	TEST CONDITIONS		
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_L = 143 °C, rectangular waveform		1.0	А
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated	780	
non-repetitive surge current	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	38	A
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 0.5 \text{ A}, L = 8 \text{ mH}$ 1.0		mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical 0.5		А	

FREE

RoHS

COMPLIANT

HALOGEN



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CC	VALUES	UNITS	
	V _{FM} ⁽¹⁾	1 A	T _{.1} = 25 °C	0.75	V
Maximum forward voltage drop See fig. 1		2 A	1j=25 C	0.82	
		1 A	T.I = 125 °C	0.59	
		2 A	1j = 125 0	0.65	
Maximum reverse leakage current		T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	0.5	mA
See fig. 2	I _{RM}	T _J = 125 °C	V _R = naleu V _R	1	
Typical junction capacitance	CT	$V_R = 5 V_{DC}$, (test signal rai	65	pF	
Typical series inductance	L _S	Measured lead to lead 5 mm from package body 2.0 nH			nH
Maximum voltage rate of charge	dV/dt	Rated V _R 10 000 V/µs			V/µs

Note

⁽¹⁾ Pulse width = 300 μ s, duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-55 to +175	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation	36	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}		80	C/W
Approvimente uneight			0.10	g
Approximate weight			0.003	oz.
Marking device		Case style SMB (DO-214AA)	1	J

Notes

(1)

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

(2) Mounted 1" square PCB



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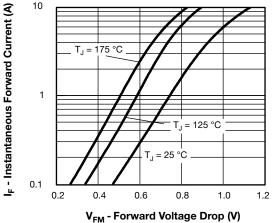


Fig. 1 - Maximum Forward Voltage Drop Characteristics

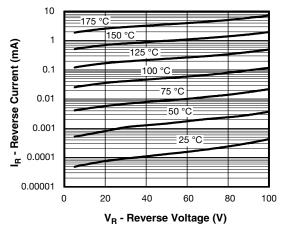


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

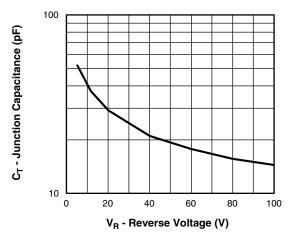


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

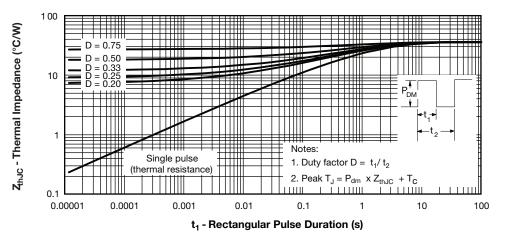


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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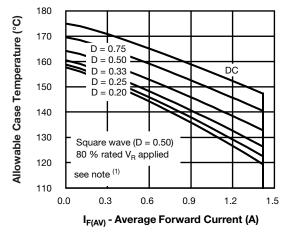
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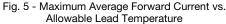
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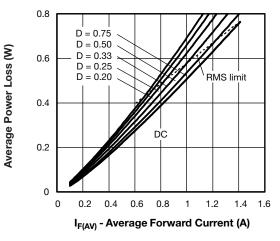


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

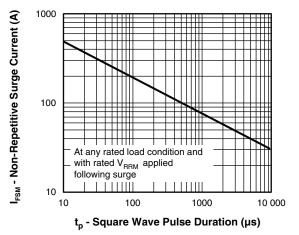


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ x \ \mathsf{V_{FM}} \ at \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ x \ \mathsf{I}_{\mathsf{R}} \ (1 \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ at \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$





ORDERING INFORMATION TABLE

Device and		10	_	•	100			
Device code	VS-	10	В	Q	100	Н	М3	
		2	3	4	5	6	7	
	1	- Vishay Semiconductors product						
	2 ·	- Cur	Current rating					
	3 ·	- B=	B = SMB (DO-214AA)					
	4	- Q =	Q = Schottky "Q" series					
	5	- Vol	Voltage rating (100 = 100 V)					
	6	• H=	H = AEC-Q101 qualified					
	7	- Env	Environmental digit:					
		М3	= halog	en-free,	RoHS of	complia	nt, and	

ORDERING INFORMATION (Example) **PREFERRED P/N** PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION VS-10BQ100HM3/5BT 5BT 3200 13" diameter plastic tape and reel

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95401					
Part marking information	www.vishay.com/doc?95403				
Packaging information	www.vishay.com/doc?95404				
SPICE model	www.vishay.com/doc?96603				

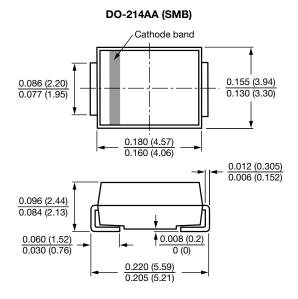


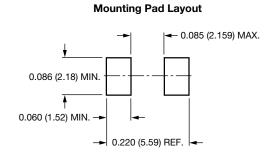
Outline Dimensions

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SMB

DIMENSIONS in inches (millimeters)







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