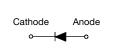
RoHS COMPLIANT

**Vishay Semiconductors** 

High Performance Schottky Rectifier, 1.0 A



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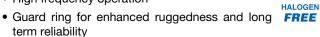


SMB (DO-214AA)

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	1.0 A			
V <sub>R</sub>	30 V			
V <sub>F</sub> at I <sub>F</sub>	0.420 V			
I <sub>RM</sub> max.	15 mA at 125 °C			
T <sub>J</sub> max.	150 °C			
E <sub>AS</sub>	3.0 mJ			
Package	SMB (DO-214AA)			
Circuit configuration	Single			

### **FEATURES**

- · Small foot print, surface mountable
- · Very low forward voltage drop
- High frequency operation



- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **DESCRIPTION / APPLICATIONS**

The VS-MBRS130-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I <sub>F(AV)</sub>	Rectangular waveform	1.0	A				
V <sub>RRM</sub>		30	V				
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	230	A				
V <sub>F</sub>	1.0 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.30	V				
TJ	Range	-55 to +125	°C				

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-MBRS130-M3	UNITS
Maximum DC reverse voltage	V <sub>R</sub>	30	V
Maximum working peak reverse voltage	V <sub>RWM</sub>		v

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDIT	TEST CONDITIONS				
Maximum average forward current	I <sub>F(AV)</sub>	$I_{F(AV)}$ 50 % duty cycle at T <sub>L</sub> = 106 °C, rectangular waveform		1.0			
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated	230	A		
non-repetitive surge current, see fig. 6	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	load condition and with rated V <sub>RRM</sub> applied	50			
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1 A, L = 6 mH		3.0	mJ		
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu s$ Frequency limited by $T_J$ maximum $V_A$ = 1.5 x $V_R$ typical		1.0	А		

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS			
	V <sub>FM</sub> <sup>(1)</sup>	1 A	T.I = 25 °C	0.420	V	
Maximum fanward voltage drop		2 A	$1_{\rm J} = 25$ C	0.470		
Maximum forward voltage drop		1 A	T 105.00	0.300		
		2 A	− T <sub>J</sub> = 125 °C	0.370		
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C		0.5	mA	
		T <sub>J</sub> = 100 °C	V <sub>R</sub> = Rated V <sub>R</sub>	5.0		
		T <sub>J</sub> = 125 °C	-	15		
Maximum junction capacitance	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal ran	200	pF		
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body 2.			nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V/µs			V/µs	

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction temperature range	T <sub>J</sub> <sup>(1)</sup>		-55 to +125	°C	
Maximum storage temperature range	T <sub>Stg</sub>		-55 to +150	C	
Maximum thermal resistance, junction to lead	R <sub>thJL</sub> <sup>(2)</sup>		25	°C/W	
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	80		
A			0.10	g	
Approximate weight			0.003	oz.	
Marking device		Case style SMB (DO-214AA)	13		

#### Notes

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink (1)

(2) Mounted 1" square PCB



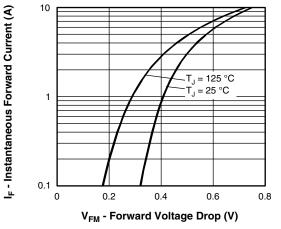


Fig. 1 - Maximum Forward Voltage Drop Characteristics

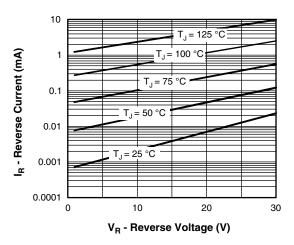


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

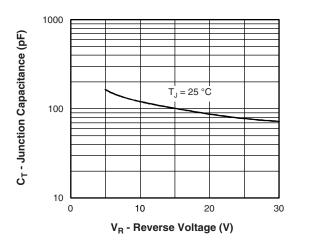


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



(1) Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

0.6 0.8

DC

Square wave (D = 0.50) Rated V<sub>R</sub> applied

See note (1)

130

120

110

100

90

80

70

0 0.2 0.4

Allowable Lead Temperature (°C)

I<sub>F(AV)</sub> - Average Forward Current (A)

1.0 1.2 1.4 16

Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature

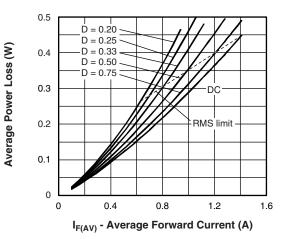
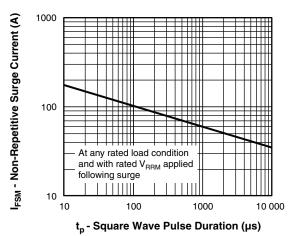


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current





## VS-MBRS130-M3

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D = 0.20

∕D = 0.25

≻D = 0.33 D = 0.50 D = 0.75

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# Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

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VISHAY

Device code	VS-	MBR	S	1	30	-M3
		(2)	(3)	4	(5)	6
	Ľ.		5	niconduc		oduct
	2 · 3 ·					
	4 -		Current rating (1 = 1 A)			
	5 -	· Volt	age rati	ng (30 =	= 30 V)	
	6 -	-M3	= halog	gen-free	, RoHS	-complia

ORDERING INFORMATION (Example)							
PREFERRED P/N	PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-MBRS130-M3/5BT	5BT	3200	13" diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95401			
Part marking information	www.vishay.com/doc?95403			
Packaging information	www.vishay.com/doc?95404			

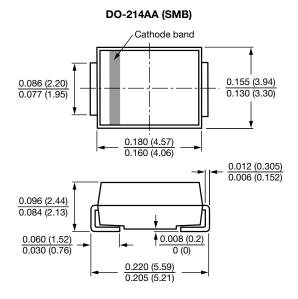


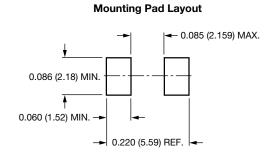
# **Outline Dimensions**

**Vishay Semiconductors** 

**SMB** 

### **DIMENSIONS** in inches (millimeters)







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