RoHS

COMPLIANT HALOGEN

FREE

Vishay Semiconductors

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Hyperfast Rectifier, 2 x 8 A FRED Pt®



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 8 A				
V _R	200 V				
V _F at I _F	0.77 V				
t _{rr}	27 ns				
T _J max.	175 °C				
Package	SMPD (TO-263AC)				
Circuit configuration	Common cathode				

FEATURES

- Hyperfast recovery time, reduced Q_{rr}, and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- · Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, telecom, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

MECHANICAL DATA

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating Halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Peak repetitive reverse voltage		V _{RRM}		200	V		
Average rectified forward current	per device	I _{F(AV)}	T _{solder pad} = 155 °C	16	А		
Average rectilied forward current	per diode			8			
Non repetitive peak aurge aurgent	per device		T_J = 25 °C, 6 ms square pulse	190			
Non-repetitive peak surge current	per diode	IFSM		100			

ELECTRICAL SPECIFICATIONS (T _J = 25 $^{\circ}$ C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	200	-	-		
Forward voltage, per diode	V _F	I _F = 8 A	-	0.93	1.03	V	
		I _F = 8 A, T _J = 150 °C	-	0.77	0.87		
	I _R	$V_{R} = V_{R}$ rated	-	-	2		
Reverse leakage current, per diode		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	6	100	μA	
Junction capacitance, per diode	CT	V _R = 200 V	-	23	-	pF	

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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS		
	t _{rr}	$I_F = 1 \text{ A}, dI_F/dt = 50$	$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$			-		
Reverse recovery time		I _F = 0.5 A, I _R = 1 A,	-	-	25			
Neverse recovery time		T _J = 25 °C		-	23	-	- ns	
		T _J = 125 °C	I _F = 8 A, dI _F /dt = 200 A/μs,	-	35	-		
Pool recovery ourrent	I _{RRM}	T _J = 25 °C		-	2.8	-	А	
Peak recovery current		T _J = 125 °C	$V_{\rm B} = 160 \text{ V}$	-	5	-		
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	30	-	nC	
		T _J = 125 °C		-	90	-		

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	+175	°C	
Thermal resistance, per diode junction to mount	R _{thJM}		-	1.8	2.5	°C/W	
Approximate weight				0.55		g	
Approximate weight				0.02		oz.	
Marking device		Case style SMPD (TO-263AC)		16CI	DH02		



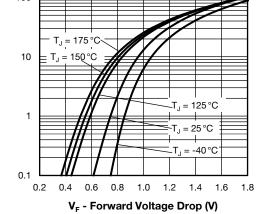


Fig. 1 - Typical Forward Voltage Drop Characteristics

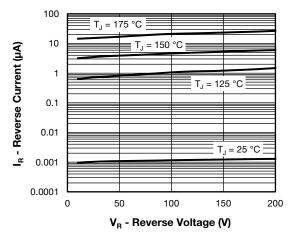


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



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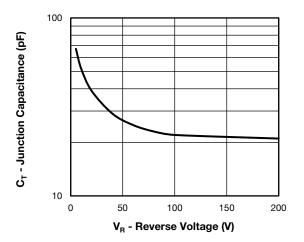


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

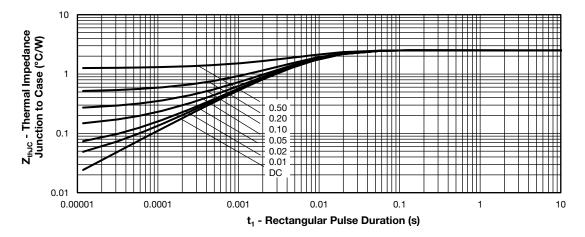
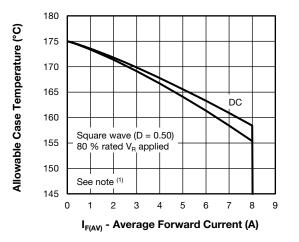
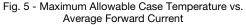


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

Average Power Loss (W)



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Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{5}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

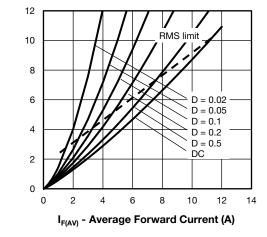


Fig. 6 - Forward Power Loss Characteristics

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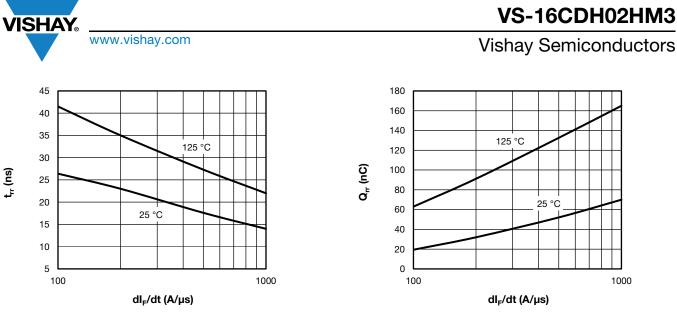


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

Fig. 8 - Typical Stored Charge vs. dl_F/dt

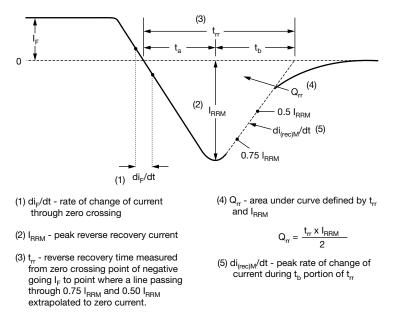


Fig. 9 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

Device code		10	•	_		00		
Device code	VS-	16	С	D	н	02	н	М3
	1	2	3	4	5	6	7	8
	1	- Vis	nay Sen	nicondu	ctors pr	oduct		
	2	- Cur	rent rati	ing (16 A	4)			
	3	- Circ	cuit con	figuratio	n:			
		C =	commo	on catho	de			
	4	- D =	SMPD	packag	е			
	5	- Pro	cess typ	ce,				
		H =	hyperfa	ast recov	very			
	6	- Vol	tage co	de (02 =	200 V)			
	7	- H=	AEC-Q	101 qua	alified			
	8	- M3	= halog	en-free,	RoHS-	complia	ant, and	termina

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER REEL MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-16CDH02HM3/I	2000	2000	13" diameter plastic tape and reel				

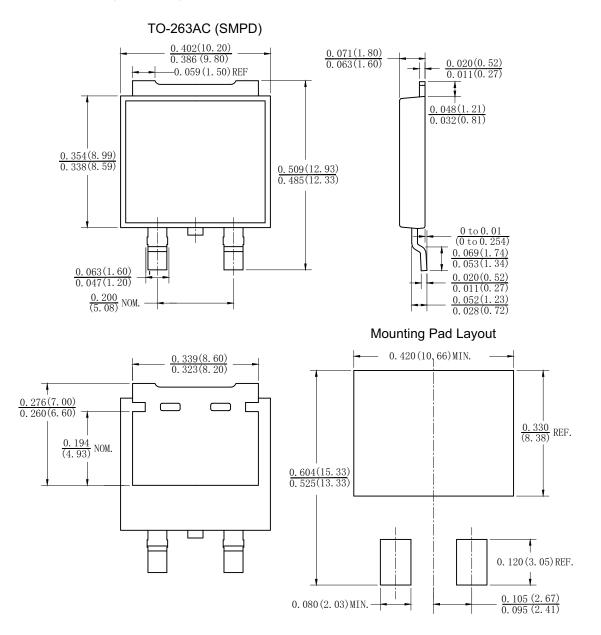
LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95604				
Part marking information	www.vishay.com/doc?95566				
Packaging information	www.vishay.com/doc?88869				





TO-263AC (SMPD)

DIMENSIONS in inches (millimeters)





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