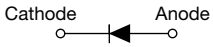
Hyperfast Rectifier, 1 A FRED Pt®



SMA (DO-214AC)



FEATURES

- Hyperfast recovery time, reduced Q_{rr}, and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

RoHS COMPLIANT
HALOGEN FREE

LINKS TO ADDITIONAL RESOURCES

3D Models

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, as high frequency rectifiers, and freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	1 A
V_R	100 V
V_F at I_F	0.68 V
t_{rr}	25 ns
T_J max.	175 °C
Package	SMA (DO-214AC)
Circuit configuration	Single die

MECHANICAL DATA

Case: SMA (DO-214AC)

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

Polarity: color band denotes cathode end

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V_{RRM}		100	V
Average rectified forward current	$I_{F(AV)}$	T_{Sp} = 158 °C	1	A
Non-repetitive peak surge current	I_{FSM}	T_J = 25 °C, 6 ms square pulse	50	
Operating junction and storage temperatures	T_J, T_{Stg}		-55 to +175	°C

For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com

THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

ELECTRICAL SPECIFICATIONS (T_J = 25 °C unless otherwise specified)						
PARAMETER	**SYMBOL**	**TEST CONDITIONS**	**MIN.**	**TYP.**	**MAX.**	**UNITS**
Breakdown voltage, blocking voltage	V_{BR}, V_R	I_R = 100 µA	100	-	-	V
Forward voltage, per diode	V_F	I_F = 1 A	-	0.82	0.90	V
		I_F = 1 A, T_J = 125 °C	-	0.68	0.76	V
Reverse leakage current, per diode	I_R	V_R = V_R rated	-	-	2	µA
		T_J = 125 °C, V_R = V_R rated	-	0.6	8	µA
Junction capacitance	C_T	V_R = 100 V	-	8.5	-	pF

DYNAMIC RECOVERY CHARACTERISTICS (T_J = 25 °C unless otherwise specified)							
PARAMETER	**SYMBOL**	**TEST CONDITIONS**		**MIN.**	**TYP.**	**MAX.**	**UNITS**
Reverse recovery time	t_{rr}	I_F = 1.0 A, dI_F/dt = 50 A/µs, V_R = 30 V		-	24	-	ns
		I_F = 0.5 A, I_R = 1 A, I_{rr} = 0.25 A		-	-	25	ns
		T_J = 25 °C	I_F = 1 A, dI_F/dt = 200 A/µs, V_R = 160 V	-	15.2	-	ns
		T_J = 125 °C		-	21	-	ns
Peak recovery current	I_{RRM}	T_J = 25 °C		-	1.38	-	A
		T_J = 125 °C		-	2	-	A
Reverse recovery charge	Q_{rr}	T_J = 25 °C		-	10.6	-	nC
		T_J = 125 °C		-	21	-	nC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	**SYMBOL**	**TEST CONDITIONS**	**MIN.**	**TYP.**	**MAX.**	**UNITS**
Maximum junction and storage temperature range	T_J, T_{Stg}		-55	-	175	°C
Thermal resistance, junction to lead	R_{thJL}	Device mounted on PCB with 2 x 3.5 mm soldering lands	-	11	21	°C/W
Thermal resistance, junction to ambient	R_{thJA}	Device mounted on PCB with recommended pad size	-	-	125	°C/W
Approximate weight			0.07			g
			0.002			oz.
Marking device		Case style SMA (DO-214AC)	1H1			

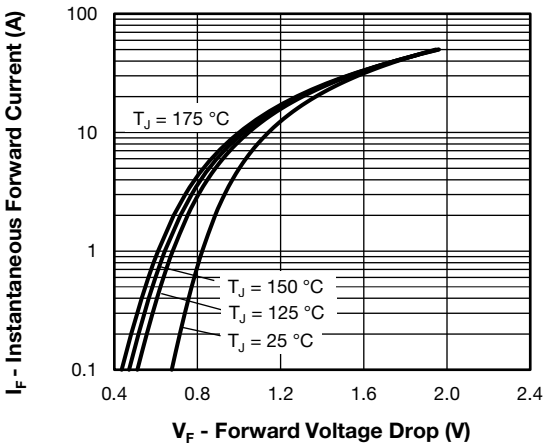


Fig. 1 - Typical Forward Voltage Drop Characteristics

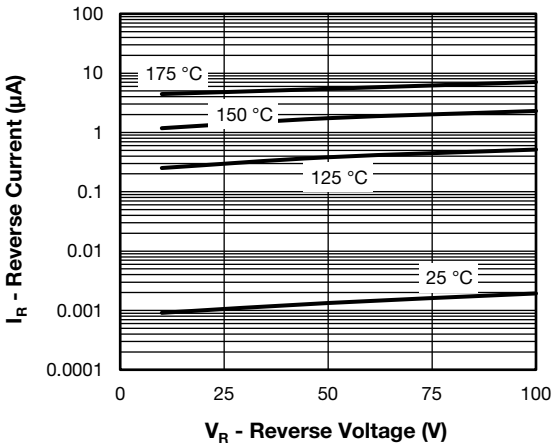


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com
THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

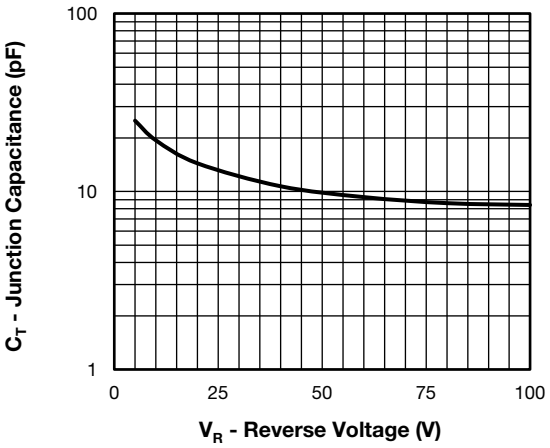


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

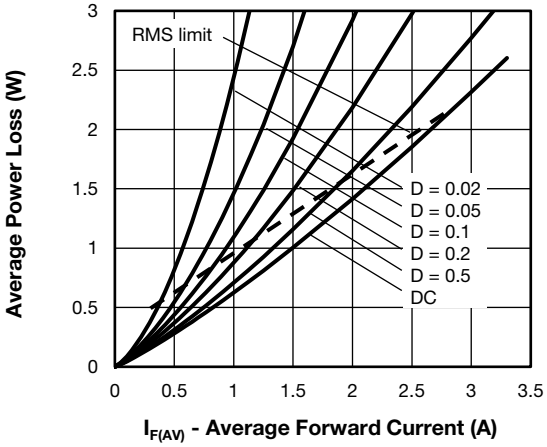


Fig. 5 - Forward Power Loss Characteristics

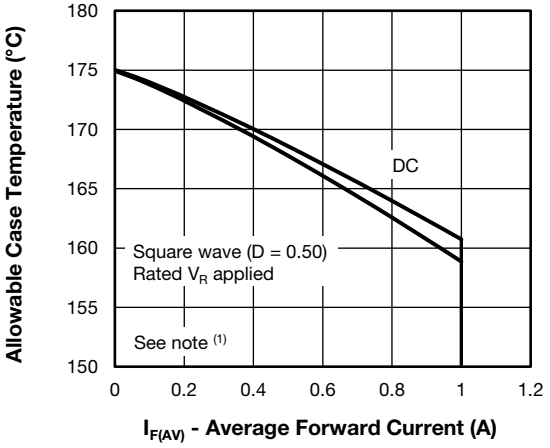


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

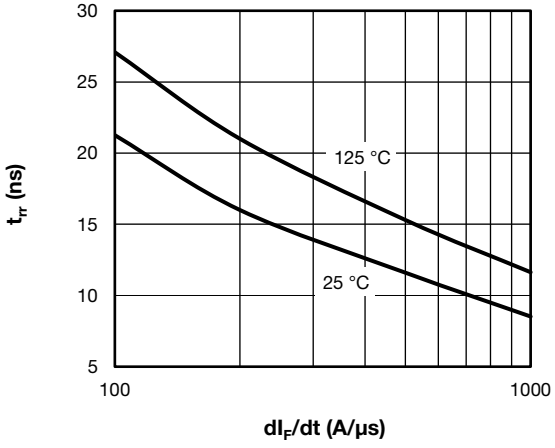


Fig. 6 - Typical Reverse Recovery Time vs. dI_F/dt

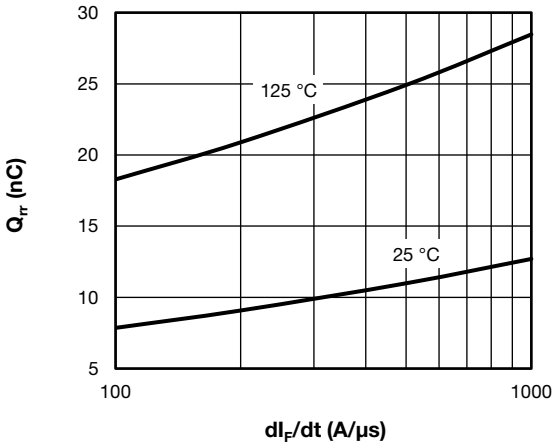
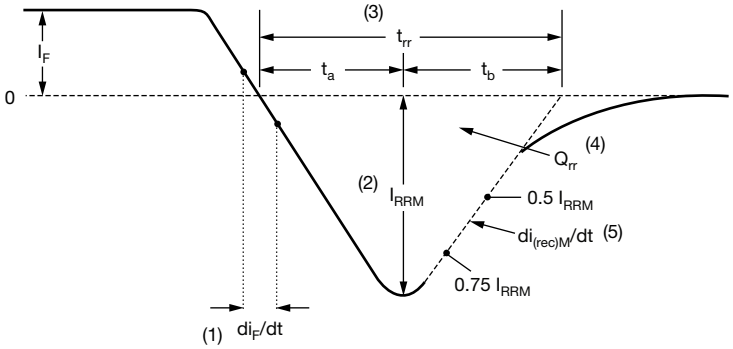


Fig. 7 - Typical Stored Charge vs. dI_F/dt

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;
Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 5);
Pd_{REV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = rated V_R

For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com
THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



(1) di_F/dt - rate of change of current through zero crossing

(2) I_{RRM} - peak reverse recovery current

(3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.

(4) Q_{rr} - area under curve defined by t_{rr} and I_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 8 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code

VS-	1	E	M	H	01	-M3
1	2	3	4	5	6	7

1 - Vishay Semiconductors product

2 - Current rating (1 = 1 A)

3 - Circuit configuration:
E = single diode

4 - M = SMA package

5 - Process type,
H = hyperfast recovery

6 - Voltage code (01 = 100 V)

7 - -M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)			
PREFERRED P/N	**QUANTITY PER REEL**	**MINIMUM ORDER QUANTITY**	**PACKAGING DESCRIPTION**
VS-1EMH01-M3/5AT	7500	7500	13"diameter plastic tape and reel

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95400
Part marking information	www.vishay.com/doc?95472
Packaging information	www.vishay.com/doc?95404
SPICE model	www.vishay.com/doc?96376

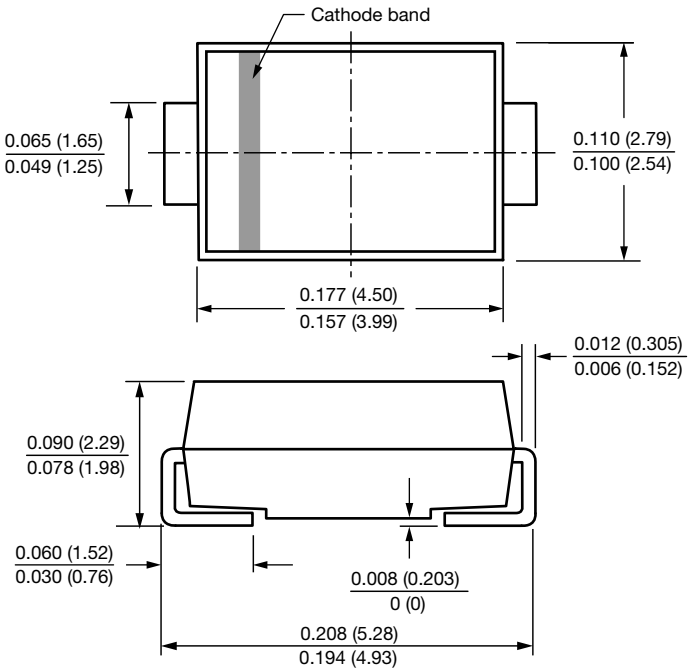
For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com

THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

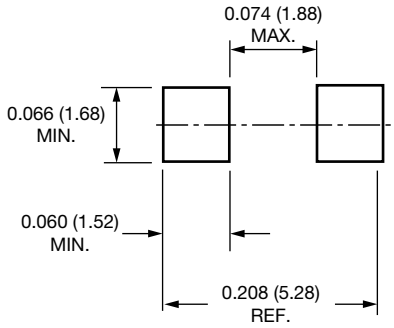
SMA

DIMENSIONS in inches (millimeters)

DO-214AC (SMA)



Mounting Pad Layout



Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, “Vishay”), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

© 2024 VISHAY INTERTECHNOLOGY, INC. ALL RIGHTS RESERVED