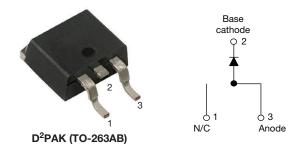
VS-12TQ035SHM3, VS-12TQ040SHM3, VS-12TQ045SHM3

Vishay Semiconductors

High Performance Schottky Rectifier, 15 A



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PRIMARY CHARACTERISTICS								
I _{F(AV)}	15 A							
V _R	35 V, 40 V, 45 V							
V _F at I _F	0.50 V							
I _{RM} typ.	70 mA at 125 °C							
T _J max.	150 °C							
E _{AS}	16 mJ							
Package	D ² PAK (TO-263AB)							
Circuit configuration	Single							

FEATURES

- 150 °C T_J operation
- · Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



- · Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201, class 1 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-12TQ...SHM3 Schottky rectifier series has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS VALUES U								
I _{F(AV)}	Rectangular waveform	15	А						
V _{RRM}	Range	35 to 45	V						
I _{FSM}	t _p = 5 μs sine	990	А						
V _F	15 A _{pk} , T _J = 125 °C	0.50	V						
TJ	Range	-55 to +150	°C						

VOLTAGE RATINGS							
PARAMETER SYMBOL VS-12TQ035SHM3 VS-12TQ040SHM3 VS-12TQ045SHM3 UNITS							
Maximum DC reverse voltage	V _R	35	40	45	V		
Maximum working peak reverse voltage	V _{RWM}	55	40	40	v		

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDI	VALUES	UNITS			
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 120 °C	15	А			
Maximum peak one cycle non-repetitive surge current	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	990	A		
See fig. 7		10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	250			
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 2.4 \text{ A}, L = 5.5 \text{ r}$	16	mJ			
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero Frequency limited by T _J maximu	2.4	А			

Revision: 28-Jul-2020

Document Number: 95853

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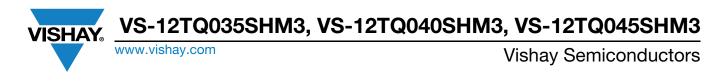
ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum forward voltage drop See fig. 1		15 A	T, = 25 °C	0.56			
	V _{FM} ⁽¹⁾	30 A	1j = 23 0	0.71	V		
	V FM (")	15 A	T, = 125 °C	0.50			
		30 A	1j = 125 C	0.64			
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = rated $V_{\rm B}$	1.75	mA		
Maximum reverse leakage current		T _J = 125 °C		110			
Typical reverse leakage current	I _{RM} ⁽¹⁾	T _J = 125 °C	$T_J = 125 \text{ °C}$ $V_R = rated V_R$		mA		
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		900	pF		
Typical series inductance	L _S	Measured lead to lead 5 r	8.0	nH			
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs			

Note

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 $^{(1)}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and st temperature range	orage	T _J , T _{Stg}		-55 to +150	°C			
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	2.0	°C/W			
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50				
Approximate weight				2	g			
Approximate weight				0.07	oz.			
Mounting torque	minimum			6 (5)	kgf ⋅ cm			
Mounting torque maxim		maximum		12 (10)	(lbf · in)			
Marking device			Case style D ² PAK	12TQ0 12TQ0 12TQ0	40SH			



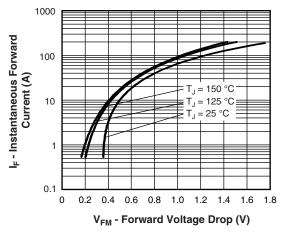


Fig. 1 - Maximum Forward Voltage Drop Characteristics

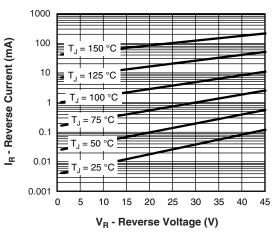


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

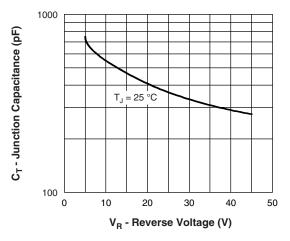


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

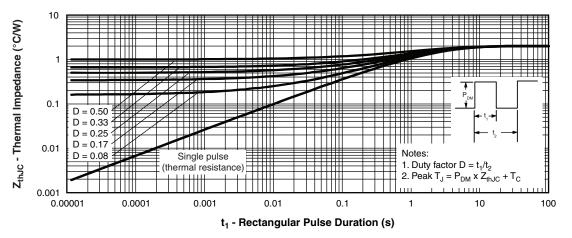
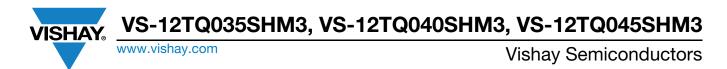


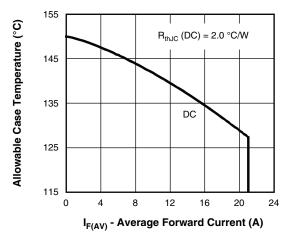
Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

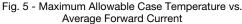
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Average Power Loss (W)





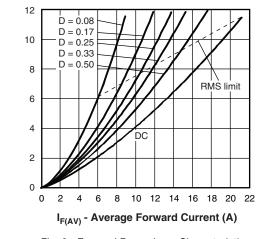
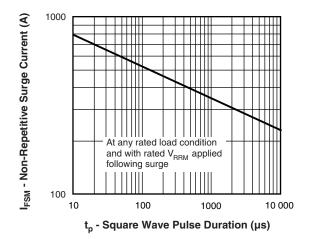


Fig. 6 - Forward Power Loss Characteristics





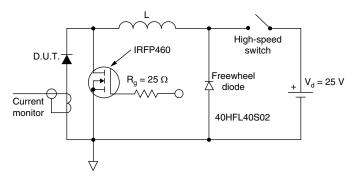


Fig. 8 - Unclamped Inductive Test Circuit

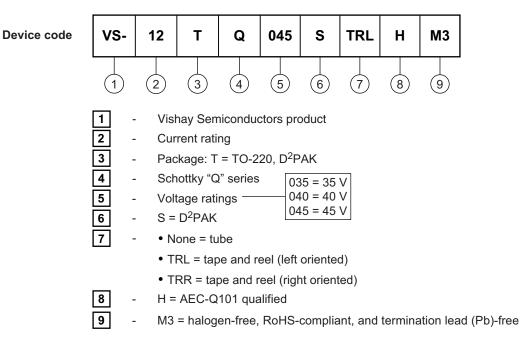
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Vishay Semiconductors

ORDERING INFORMATION TABLE

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ORDERING INFORMATION									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-12TQ035SHM3	50	1000	Antistatic plastic tubes						
VS-12TQ035STRRHM3	800	800	13" diameter reel						
VS-12TQ035STRLHM3	800	800	13" diameter reel						
VS-12TQ040SHM3	50	1000	Antistatic plastic tubes						
VS-12TQ040STRRHM3	800	800	13" diameter reel						
VS-12TQ040STRLHM3	800	800	13" diameter reel						
VS-12TQ045SHM3	50	1000	Antistatic plastic tubes						
VS-12TQ045STRRHM3	800	800	13" diameter reel						
VS-12TQ045STRLHM3	800	800	13" diameter reel						

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95046					
Part marking information	www.vishay.com/doc?95444					
Packaging information	www.vishay.com/doc?95032					

Outline Dimensions



D²PAK

DIMENSIONS in millimeters and inches

www.vishay.com

SHA



SYMBOL	MILLIM	IETERS	INC	HES	NOTES	NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 08-Jul-15

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