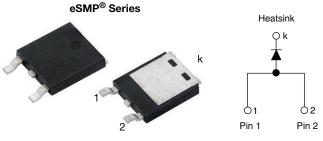
**Vishay Semiconductors** 



Hyperfast Rectifier, 8 A FRED Pt<sup>®</sup>



SlimDPAK (TO-252AE)

## LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	8 A			
V <sub>R</sub>	600 V			
V <sub>F</sub> at I <sub>F</sub>	1.3 V			
t <sub>rr</sub> (typ.)	16 ns			
T <sub>J</sub> max.	175 °C			
Package	SlimDPAK (TO-252AE)			
Circuit configuration	Single			

#### **FEATURES**

- Hyperfast recovery time, reduced Q<sub>rr</sub> and soft recovery
- For PFC CRM / CCM operation
- Low forward voltage drop, low power losses
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

## **TYPICAL APPLICATIONS**

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS inverters, or as freewheeling diodes. Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

## **MECHANICAL DATA**

Case: SlimDPAK (TO-252AE)

Molding compound meets UL 94 V-0 flammability rating

Base PN/-M3 - halogen-free, RoHS-compliant

**Terminals:** matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	V <sub>RRM</sub>		600	V	
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 144 °C	8		
Non-repetitive peak surge current	I <sub>FSM</sub>	$T_J = 25 \ ^{\circ}C$ , 10 ms sine pulse wave	90	A	
Operating junction and storage temperatures	TJ, T <sub>Stg</sub>		-55 to +175	°C	

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	$V_{BR}, V_{R}$	I <sub>R</sub> = 100 μA	600	-	-		
Forward valtage	V	I <sub>F</sub> = 8 A	-	1.6	2.40	V	
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 8 A, T <sub>J</sub> = 150 °C	-	1.3	1.7		
Reverse leakage current	I <sub>R</sub>	$V_{R} = V_{R}$ rated	-	-	20		
Reverse leakage current		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	500	μA	
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	12	-	pF	

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COMPLIANT HALOGEN



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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	21	-	ns
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 100 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	16	-	
Reverse recovery time	t <sub>rr</sub>	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{RR} = 0.25 \text{ A}$		-	-	25	
		T <sub>J</sub> = 25 °C		-	25	-	
		T <sub>J</sub> = 125 °C		-	65	-	
Pools rocovers ourrent	1	T <sub>J</sub> = 25 °C	$I_F = 8 A$	-	4.9	-	Α
Peak recovery current	IRRM	T <sub>J</sub> = 125 °C	dI <sub>F</sub> /dt = 500 A/µs V <sub>B</sub> = 400 V	-	8.2	-	A
Reverse recovery charge	0	T <sub>J</sub> = 25 °C		-	90	-	nC
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C	]	-	260	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C
Thermal resistance, junction to mount	R <sub>thJM</sub>		-	-	2.2	°C/W
Weight			-	0.20	-	g
Marking device		Case style SlimDPAK (TO-252AE)		8EV	H06	

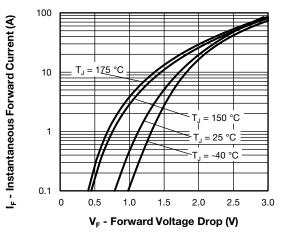


Fig. 1 - Typical Forward Voltage Drop Characteristics

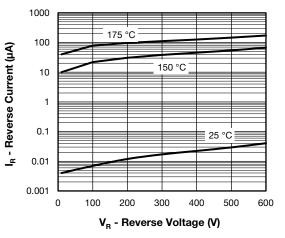


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

## **VS-8EVH06-M3**

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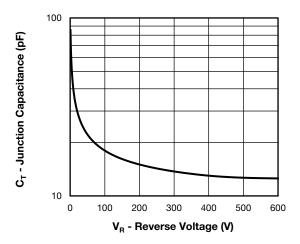


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

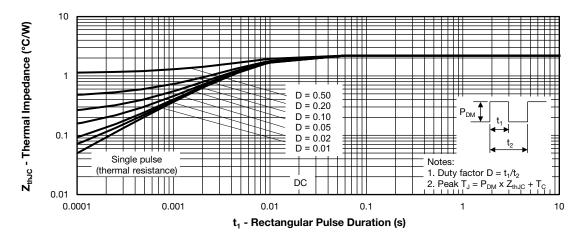
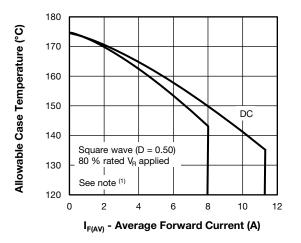
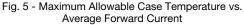


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics





#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mbox{Pd} = \mbox{forward power loss} = I_{F(AV)} \times V_{FM} \mbox{ at } (I_{F(AV)}/D) \mbox{ (see fig. 6);} \\ \mbox{Pd}_{REV} = \mbox{inverse power loss} = V_{R1} \times I_R \mbox{ (1 - D); } I_R \mbox{ at } V_{R1} = \mbox{rated } V_R \end{array}$ 

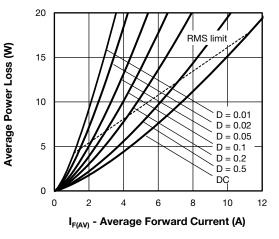


Fig. 6 - Forward Power Loss Characteristics

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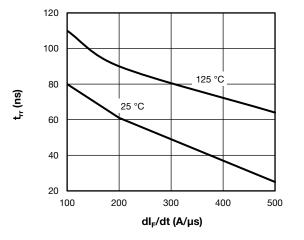
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Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

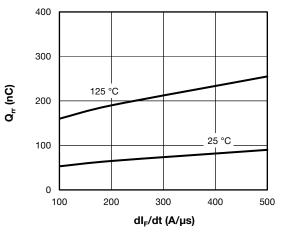


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

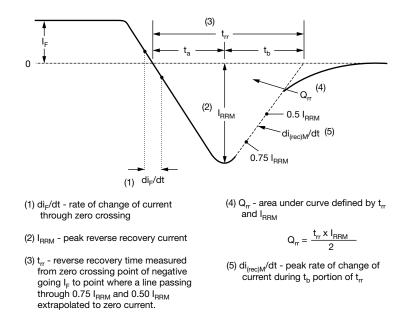
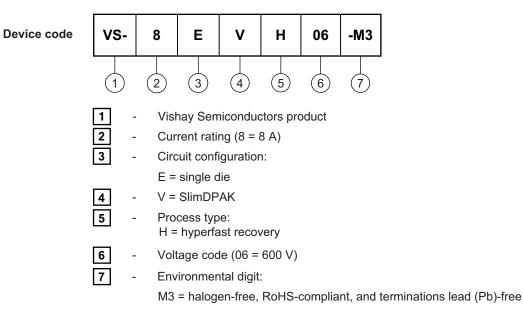


Fig. 9 - Reverse Recovery Waveform and Definitions



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### **ORDERING INFORMATION TABLE**



ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	PACKAGING DESCRIPTION	
VS-8EVH06-M3/I	0.20	I	4500	13"diameter plastic tape and reel	

LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?96081				
Part marking information	www.vishay.com/doc?96085			
Packaging information	www.vishay.com/doc?88869			
SPICE model	www.vishay.com/doc?97200			





SlimDPAK

### **DIMENSIONS** in inches (millimeters)





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