

Vishay Semiconductors

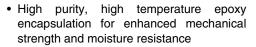
High Performance Schottky Rectifier, 7.5 A



PRIMARY CHARACTERISTICS								
I _{F(AV)}	7.5 A							
V_{R}	35 V, 45 V							
V _F at I _F	0.57 V							
I _{RM} max.	15 mA at 125 °C							
T _J max.	150 °C							
E _{AS}	7 mJ							
Package	TO-220AC 2L							
Circuit configuration	Single							

FEATURES

- 150 °C T_J operation
- · High frequency operation
- · Low forward voltage drop





- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-MBR7... Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	VALUES	UNITS					
I _{F(AV)}	Rectangular waveform	7.5	Α				
V _{RRM}		35/45	V				
I _{FSM}	t _p = 5 μs sine	690	Α				
V _F	7.5 A _{pk} , T _J = 125 °C	0.57	V				
T _J	Range	-65 to +150	°C				

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS-MBR735-M3	VS-MBR745-M3	UNITS					
Maximum DC reverse voltage	V_R	35	45	V					
Maximum working peak reverse voltage	V_{RWM}	33	45	V					

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CON	TEST CONDITIONS						
Maximum average forward current	I _{F(AV)}	T _C = 131 °C, rated V _R	T _C = 131 °C, rated V _R						
Non-repetitive peak surge current	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	690	A				
	1 0141	Surge applied at rated load of single phase 60 Hz	150						
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 2 \text{A}, L = 3.5 ^{\circ}$	7	mJ					
Repetitive avalanche current	I _{AR}	Current decaying linearly to Frequency limited by T _J max	2	Α					



VS-MBR735-M3, VS-MBR745-M3

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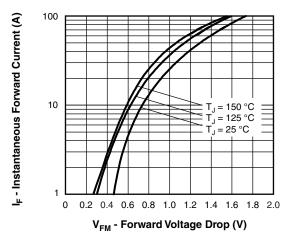
ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS				
		15 A	T _J = 25 °C	0.84				
Maximum forward voltage drop	V _{FM} ⁽¹⁾	7.5 A	T _J = 125 °C	0.57	V			
		15 A	1J = 125 C	0.72				
Maximum instantaneous reverse current	I _{RM} ⁽¹⁾	T _J = 25 °C	Rated DC voltage	0.1	mA			
Maximum instantaneous reverse current		T _J = 125 °C	Haled DC Vollage	15				
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal rang	400	pF				
Typical series inductance	L _S	Measured from top of termi	8.0	nΗ				
Maximum voltage rate of change	dV/dt	Rated V _R	1000	V/µs				

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	OL TEST CONDITIONS		UNITS				
Maximum junction temperature range	TJ		-65 to +150	°C				
Maximum storage temperature range	T _{Stg}		-65 to +175					
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	3.0	°C/W				
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.50					
Approximate weight			2	g				
Approximate weight			0.07	oz.				
Mounting torque minimum			6 (5)	kgf ⋅ cm				
Mounting torque maximum			12 (10)	(lbf · in)				
Marking device		Casa style TO 220AC 21	MBR735					
warking device		Case style TO-220AC 2L		R745				

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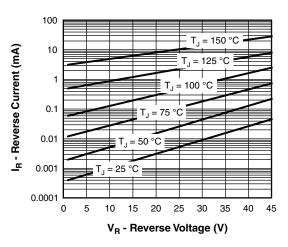


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

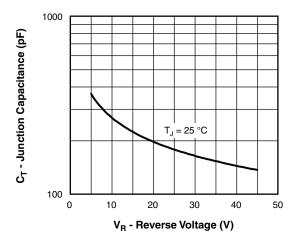


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

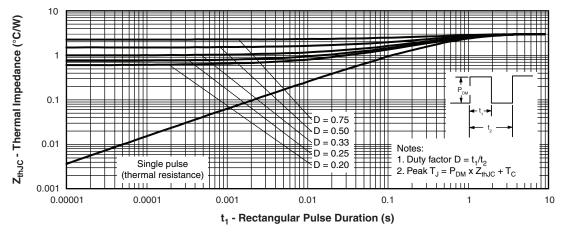


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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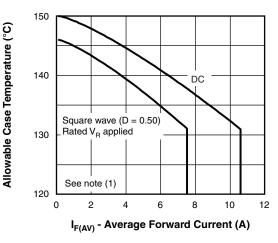


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

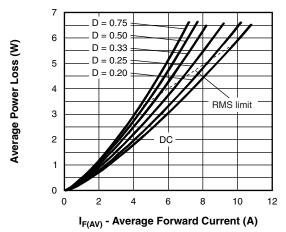


Fig. 6 - Forward Power Loss Characteristics

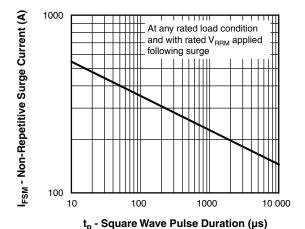


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

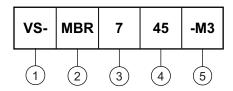
 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D)}; \ I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$

VS-MBR735-M3, VS-MBR745-M3

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ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Schottky MBR series

- Environmental digit

-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

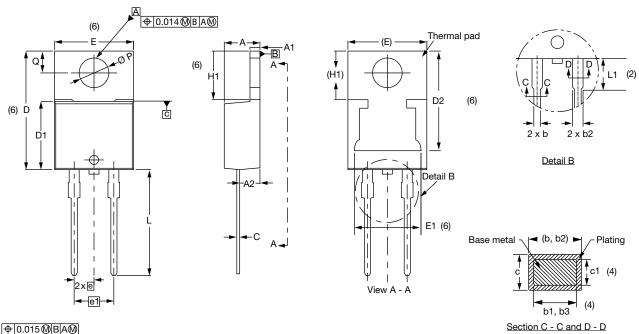
ORDERING INFORMATION (Example)								
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION						
VS-MBR735-M3	50	Antistatic plastic tubes						
VS-MBR745-M3	50	Antistatic plastic tubes						

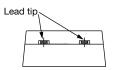
LINKS TO RELATED DOCUMENTS								
Dimensions	www.vishay.com/doc?96156							
Part marking information	www.vishay.com/doc?95391							
SPICE model	www.vishay.com/doc?95298							

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TO-220AC 2L

DIMENSIONS in millimeters and inches





Conforms to JEDEC® outline TO-220AC

SYMBOL	MILLIMETERS		INCHES		NOTES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183			D2	11.68	13.30	0.460	0.524	6, 7
A1	1.14	1.40	0.045	0.055			E	10.11	10.51	0.398	0.414	3, 6
A2	2.50	2.92	0.098	0.115			E1	6.86	8.89	0.270	0.350	6
b	0.69	1.01	0.027	0.040			е	2.41	2.67	0.095	0.105	
b1	0.38	0.97	0.015	0.038	4		e1	4.88	5.28	0.192	0.208	
b2	1.20	1.73	0.047	0.068			H1	6.09	6.48	0.240	0.255	6
b3	1.14	1.73	0.045	0.068	4		L	13.52	14.02	0.532	0.552	
С	0.36	0.61	0.014	0.024			L1	3.32	3.82	0.131	0.150	2
c1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.91	0.139	0.154	
D	14.85	15.35	0.585	0.604	3		Q	2.60	3.00	0.102	0.118	
D1	8.38	9.02	0.330	0.355				•	•			

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- (7) Outline conforms to JEDEC® TO-220, except D2



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