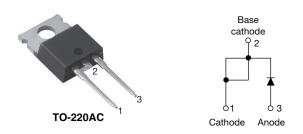


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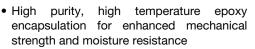
High Performance Schottky Rectifier, 10 A



PRIMARY CHARACTERISTICS					
I _{F(AV)}	10 A				
V _R	35 V, 40 V, 45 V				
V _F at I _F	0.49 V				
I _{RM}	15 mA at 125 °C				
T _J max.	175 °C				
E _{AS}	13 mJ				
Package	TO-220AC				
Circuit configuration	Single				

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Meet JESD 201 class 2 whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-10TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	10	Α			
V _{RRM}		35/45	V			
I _{FSM}	t _p = 5 μs sine	1050	Α			
V _F	10 A _{pk} , T _J = 125 °C	0.49	V			
T _{.1}	Range	-55 to +175	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-10TQ035HN3	VS-10TQ040HN3	VS-10TQ045HN3	UNITS
Maximum DC reverse voltage	V_R	35	40	45	V
Maximum working peak reverse voltage	V_{RWM}	33	40	45	V

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS	
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 151 °C, rectangular waveform		10		
Maximum peak one cycle non-repetitive	_	5 μs sine or 3 μs rect. pulse Following any rated load		1050	Α	
surge current See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	280	1	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25$ °C, $I_{AS} = 2$ A, L = 6.5 mH		13	mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 µs Frequency limited by T _J maximum V _A = 1.5 x V _B typical		2	Α	



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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
		10 A	T _{.1} = 25 °C	0.57	V	
Maximum forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	20 A	1j=25 C	0.67		
	V _{FM} (1)	10 A	T _{.1} = 125 °C	0.49		
		20 A	1j=125 C	0.61		
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C		2	mA	
See fig. 2	IRM (''	T _J = 125 °C	V _R = Rated V _R	15	IIIA	
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		900	pF	
Typical series inductance	LS	Measured lead to lead 5 mm from package body		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs	

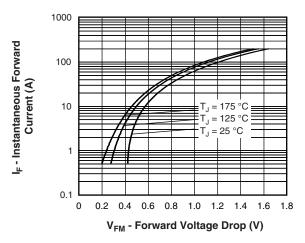
Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +175	°C	
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	2.0	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50		
Approximate weight				2	g	
Approximate weight				0.07	oz.	
Maunting taxaus	minimum			6 (5)	kgf. cm	
Mounting torque -	maximum			12 (10)	(lbf. in)	
Marking device				10TQ	035H	
			Case style TO-220AC		040H	
				10TQ	045H	

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1000 $T_J = 175 \, ^{\circ}C$ 100 I_R - Reverse Current (mA) 10 T_J = 150 °C $T_1 = 125 \, ^{\circ}C$ T_{.1} = 100 °C 0.1 T_{.1}= 75 °C 0.01 50 °C 0.001 25 °C 0.0001 15 20 25 30 35 40 0 10 V_R - Reverse Voltage (V)

Fig. 1 - Maximum Forward Voltage Drop Characteristics

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

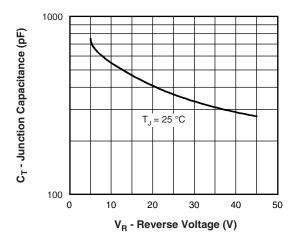


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

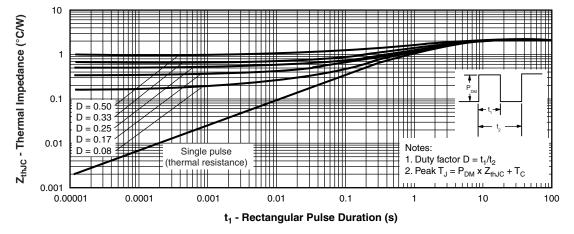


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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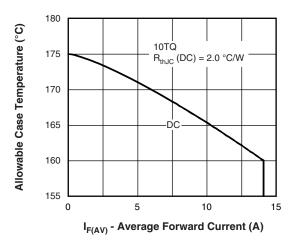


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

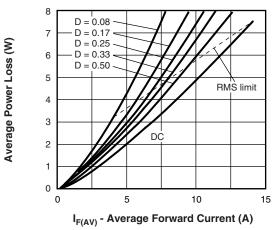


Fig. 6 - Forward Power Loss Characteristics

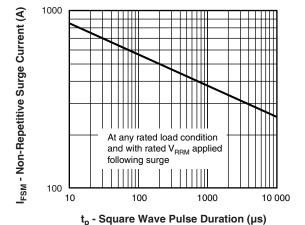


Fig. 7 - Maximum Non-Repetitive Surge Current

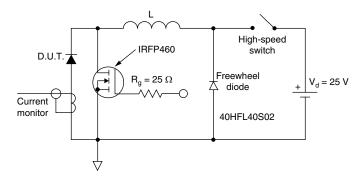
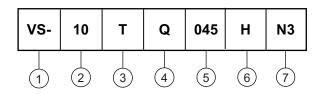


Fig. 8 - Unclamped Inductive Test Circuit

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (10 = 10 A)

3 - Package:

T = TO-220

4 - Schottky "Q" series

035 = 35 V

5 - Voltage ratings

040 = 40 V045 = 45 V

6 - H = AEC-Q101 qualified

7 - Environmental digit

• N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-10TQ035HN3	50	1000	Antistatic plastic tube			
VS-10TQ040HN3	50	1000	Antistatic plastic tube			
VS-10TQ045HN3	50	1000	Antistatic plastic tube			

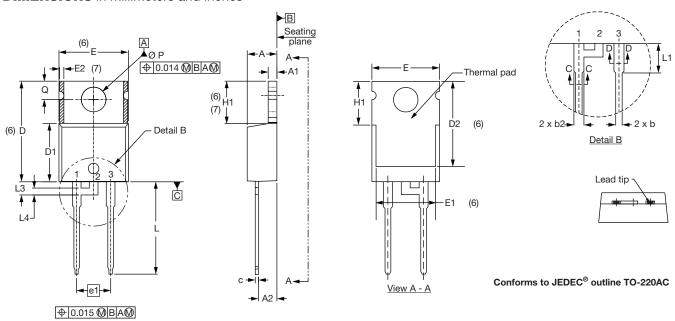
LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95221
Part marking information	www.vishay.com/doc?95068



Vishay Semiconductors

TO-220AC

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	INCHES		NOTES
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOIES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6
Е	10.11	10.51	0.398	0.414	3, 6

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
e1	4.88	5.28	0.192	0.208	
H1	5.84	6.86	0.230	0.270	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
L3	1.78	2.13	0.070	0.084	
L4	0.76	1.27	0.030	0.050	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- $^{(7)}$ Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline



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