

HALOGEN FREE

# **High Performance Schottky Rectifier, 100 A**



PowerTab<sup>®</sup>

### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	100 A			
V <sub>R</sub>	100 V			
V <sub>F</sub> at I <sub>F</sub>	0.82 V			
I <sub>RM</sub>	180 mA at 125 °C			
E <sub>AS</sub>	9 mJ			
T <sub>J</sub> max.	175 °C			
Package	PowerTab <sup>®</sup>			
Circuit configuration	Single			

#### **FEATURES**

- 175 °C max. operating junction temperature
- High frequency operation
- · Low forward voltage drop
- Continuous high current operation
- Guard ring for enhanced ruggedness and long term reliability
- Screw mounting only
- Designed and qualified according to JEDEC®-JESD 47
- PowerTab<sup>®</sup> package
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912"><u>www.vishav.com/doc?99912</u></a>

#### **DESCRIPTION**

The VS-100BGQ100 Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, reverse battery protection, and redundant power subsystems.

### **MECHANICAL DATA**

Case: PowerTab®

Molding compound meets UL 94 V-0 flammability rating

Terminal: nickel plated, screwable

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
1	Rectangular waveform	100	A		
I <sub>F(AV)</sub>	T <sub>C</sub>	124	°C		
$V_{RRM}$		100	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	6300	Α		
V	100 A <sub>pk</sub> (typical)	0.77	V		
$V_{F}$	TJ	125	°C		
TJ	Range	-55 to +175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	100BGQ100	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	100	V	
Maximum working peak reverse voltage	$V_{RWM}$	100	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 124 °C, rectangular waveform		100	Α
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load	6300	
non-repetitive surge current		10 ms sine or 6 ms rect. pulse Condition and with rated V <sub>RRM</sub> applied		800	A
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_J = 25  ^{\circ}\text{C},  I_{AS} = 2  \text{A},  L = 4.5  \text{mH}$		9	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		Α	

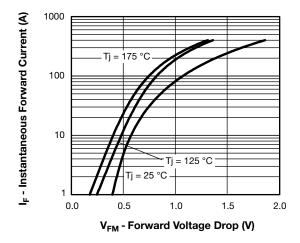


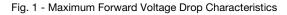
ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES		UNITS
PANAMETER				TYP.	MAX.	DIVITO
Forward voltage drop		50 A	T <sub>J</sub> = 25 °C	0.83	0.86	- V
	V <sub>FM</sub> <sup>(1)</sup>	100 A		1.01	1.08	
	V FM (*)	50 A	T <sub>J</sub> = 125 °C	0.66	0.7	
		100 A		0.77	0.82	
Developed Includes a surrout (1)	ı (1)	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	22	300	μA
Reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 125 °C		14	18	mA
Maximum junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ , (test signal range 100 kHz to 1 MHz) 25 °C		13	20	pF
Typical series inductance	L <sub>S</sub>	Measured from tab to mounting plane 3.5		.5	nΗ	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V/µ:		V/µs		

### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300 µs, duty cycle < 2 %

PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS	
Maximum junction an temperature range	d storage	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C	
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation	0.50	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.30	C/VV	
Approximate weight				5	g	
Mounting torque -	minimum			1.2 (10)	N·m	
	maximum			2.4 (20)	(lbf $\cdot$ in)	
Marking device			Case style PowerTab®	100BC	Q100	





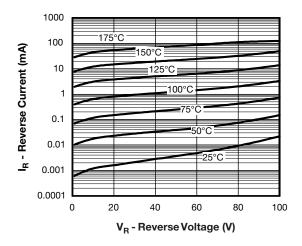


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



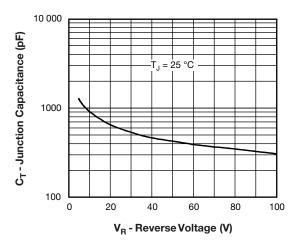


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

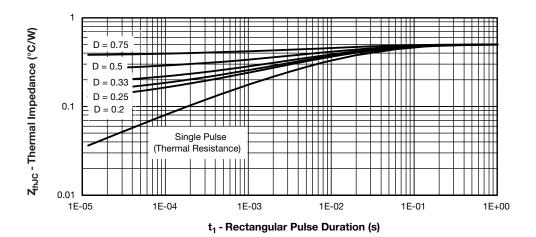


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

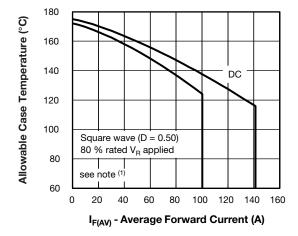


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

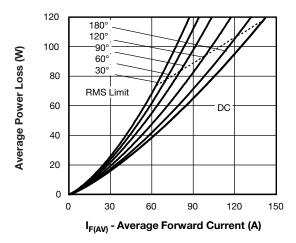
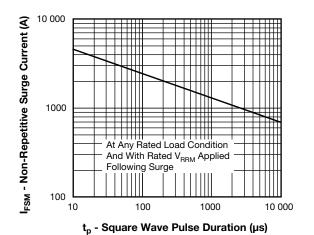


Fig. 6 - Forward Power Loss Characteristics





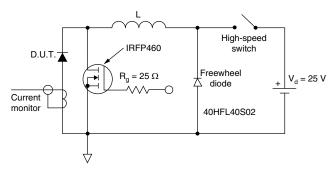


Fig. 8 - Unclamped Inductive Test Circuit

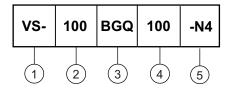
Fig. 7 - Maximum Non-Repetitive Surge Current

#### Note

(1) Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$ ;  $Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 80 \%$  rated  $V_R$ 

#### **ORDERING INFORMATION TABLE**

### Device code



- 1 Vishay Semiconductors product
- Current rating (100 = 100 A)
- 3 Essential part number
- 4 Voltage rating (100 = 100 V)
- 5 Environmental digit:
  - -N4 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

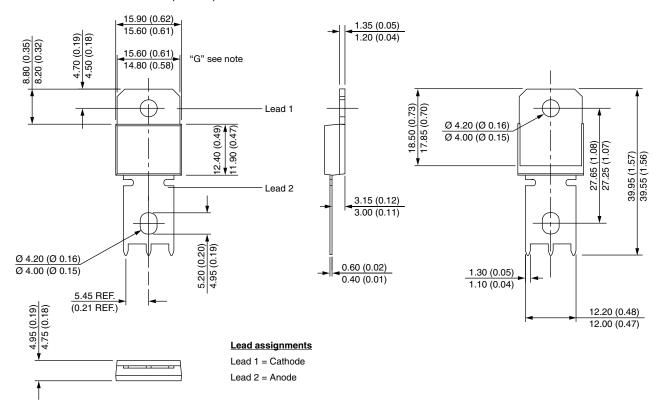
ORDERING INFORMATION (Example)		
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION
VS-100BGQ100-N4	25/tube	Antistatic plastic tube

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95240</u>				
Part marking information	www.vishay.com/doc?95467			
Application note	www.vishay.com/doc?95179			
SPICE model	www.vishay.com/doc?96588			



# PowerTab®

### **DIMENSIONS** in millimeters (inches)



#### Note:

Outline conform to JEDEC® TO-275, except for dimension "G" only



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Vishay

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