

Ceramic Singlelayer DC Disc Capacitors, 5 kV_{DC} General Purpose



FEATURES

- High capacitance in small sizes
- Low losses
- Wide range of different lead styles
- Material categorization:
for definitions of compliance please see
www.vishay.com/doc?99912


RoHS
COMPLIANT

APPLICATIONS

- Lighting ballasts
- Switching power supplies
- Bypassing, coupling and decoupling
- DC blocking

DESIGN

The capacitors consist of a ceramic disc which is silver plated on both sides. Connection leads are made of tinned copper having diameters of 0.6 mm or 0.8 mm.

The capacitors may be supplied with straight or kinked leads having a lead spacing of 12.5 mm.

Coating is made of blue colored flame retardant epoxy resin in accordance with UL 94 V-0.

CAPACITANCE RANGE

22 pF to 2.2 nF

RATED VOLTAGE

5 kV_{DC}

DIELECTRIC STRENGTH

7500 V_{DC}, 2 s Component test

INSULATION RESISTANCE AT 500 V_{DC}

≥ 10 000 MΩ (60 s)

TOLERANCE ON CAPACITANCE

± 10 %, ± 20 %

DISSIPATION FACTOR

Class 1:

 $C < 30 \text{ pF: } \left(\frac{100 \text{ pF}}{C} + 0.7 \right) \times 10^{-4} \text{ max. (1 MHz)}$
 $C \geq 30 \text{ pF: max. 0.1 \% (1 MHz)}$

Class 2: max. 2.5 % (1 kHz)

QUICK REFERENCE DATA

DESCRIPTION	VALUE	
Ceramic Class	1	2
Ceramic Dielectric	N750, Y5U	
Voltage (V _{DC})	5000	
Min. Capacitance (pF)	22	220
Max. Capacitance (pF)	330	2200
Mounting	Radial	

OPERATING TEMPERATURE RANGE

-40 °C to +85 °C ⁽¹⁾

Note

- ⁽¹⁾ For explanation about the difference of operating temperature range and temperature characteristic of capacitance please see
www.vishay.com/doc?48299

TEMPERATURE CHARACTERISTICS

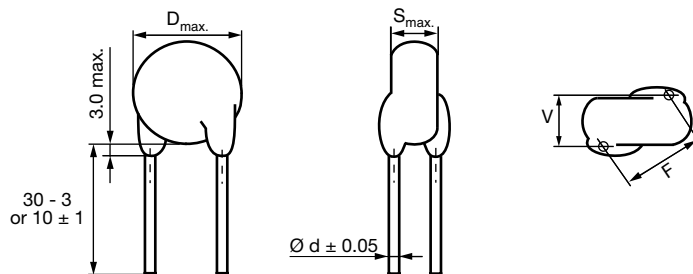
Class 1: N750

Class 2: Y5U

SECTIONAL SPECIFICATIONS

Climatic category (according to EN 60068-1):
40 / 085 / 21

DIMENSIONS in millimeters



ORDERING INFORMATION

ORDERING INFORMATION								ORDERING CODE
CAPACITANCE (pF)	TOLERANCE (%)	BODY DIAMETER D _{max.} (mm)	BODY THICKNESS S _{max.} (mm)	LEAD SPACING ⁽¹⁾ F (mm) ± 1 mm	LEAD DIAMETER ⁽¹⁾ d (mm) ± 0.05 mm	WIDTH ⁽¹⁾ V (mm) ± 0.5 mm	MISSING DIGITS SEE ORDERING CODE BELOW	
N750								
22	± 10	7.0	4.8	12.5	0.6	2.2	HEU220KBE###KR	
33		9.3					HEU330KBE###KR	
47							HEU470KBE###KR	
68		11.8	5.2		0.8	1.9	HEU680KBE###KR	
82							HEU820KBE###KR	
100							HEU101KBE###KR	
150							HEU151KBE###KR	
220							HEU221KBE###KR	
330		19.5	HEU331KBE###KR					
Y5U								
220	± 20 ⁽²⁾	7.0	5.0	12.5	0.6	3.8	HEE221#BE###KR	
330		9.0			0.8		HEE331#BE###KR	
680							HEE681#BE###KR	
1000		11.0					HEE102#BE###KR	
1500		13.0					HEE152#BE###KR	
2200		15.0					HEE222#BE###KR	

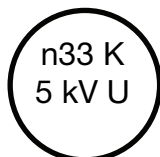
Notes

- (1) Standard lead configuration, other lead spacing and diameter available on request
(2) ± 10 % available on request

ORDERING CODE

#	7 th digit	Capacitance tolerance	± 10 % = K, ± 20 % = M
###	10 th to 12 th digit	Lead configuration	See "General Information" www.vishay.com/doc?22001
Example	HEE	222	M
	Series	Capacitance value	Tolerance code
			BE
			Voltage code
			EJ0
			Lead configuration
			K
			Internal code
			R
			RoHS compliant

MARKING



HEU 22 pF to 100 pF
HEE 220 pF to 1.0 nF



HEU 150 pF to 330 pF
HEE 1.5 nF to 2.2 nF



STORAGE

The capacitors must not be stored in a corrosive atmosphere, where sulphide or chloride gas, acid, alkali or salt are present. Exposure of the components to moisture, should be avoided. The solderability of the leads is not affected by storage of up to 24 months (temperature +10 °C to +35 °C, relative humidity up to 60 %). Class 2 ceramic dielectric capacitors are also subject to aging, see www.vishay.com/doc?22001.

SOLDERING

SOLDERING SPECIFICATIONS		
Soldering test for capacitors with wire leads: (according to IEC 60068-2-20, solder bath method)		
	SOLDERABILITY	RESISTANCE TO SOLDERING HEAT
Soldering temperature	235 °C ± 5 °C	260 °C ± 5 °C
Soldering duration	2 s ± 0.5 s	10 s ± 1 s
Distance from component body	≥ 2 mm	≥ 5 mm

SOLDERING RECOMMENDATIONS

Soldering of the component should be achieved using a Sn60/40 type or a silver-bearing Sn62/36/2Ag type solder. Ceramic capacitors are very sensitive to rapid changes in temperature (thermal shock) therefore the solder heat resistance specification (see Soldering Specifications table) should not be exceeded. Subjecting the capacitor to excessive heating may result in thermal shocks that can crack the ceramic body. Similarly, excessive heating can cause the internal solder junction to melt.

CLEANING

The components should be cleaned immediately following the soldering operation with vapor degreasers.

SOLVENT RESISTANCE

The coating and marking of the capacitors are resistant to the following test method: IEC 60068-2-45 (method XA).

MOUNTING

If a defined product stop is required for mounting on a PCB, a mechanically formed product stop (kinked or inline wire) or a mounting tool should be used.

We do not recommend modifying the lead terminals, e.g. bending or cropping. This action could break the coating or crack the ceramic insert. If however, the lead must be modified in any way, we recommend support of the lead with a clamping fixture next to the coating.

OPERATING VOLTAGE

In case the voltage is applied to the circuit, starting as well as stopping, may generate irregular voltage for a transit period because of resonance or switching. Be sure to use a capacitor with a rated voltage range that includes these irregular voltages.

OPERATING TEMPERATURE AND SELF-GENERATED HEAT

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high frequency, pulse, or similar application, it may have self-generated heat due to dielectric dissipation.

Temperature increase due to self-generated heating should not exceed 20 °C while operating at an atmosphere temperature of 25 °C.

When measuring, the surface temperature, make sure that the capacitor is not affected by radiant, conductive and convective heat by its surroundings. Excessive heat may lead to thermo-mechanical deterioration of the capacitor's characteristics and reliability.

RELATED DOCUMENTS	
General Information	www.vishay.com/doc?22001



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