www.vishay.com



- Duel element for redundant current sensing
- High power to resistor size ratio
- Proprietary processing technique produces extremely low resistance values
- All welded construction
- Solid metal nickel-chrome alloy resistive element with unique design for low TCR (down to ± 10 ppm/°C)
- Very low inductance (< 5 nH)
- Low thermal EMF (as low as < 1.25 μV/°C)</li>
- AEC-Q200 qualified
- PATENT(S): <u>www.vishay.com/patents</u>
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

STANDARD ELECTRICAL SPECIFICATIONS						
GLOBAL MODEL	SIZE	POWER RATING P <sub>70 °C</sub> W	TOLERANCE ± %	RESISTANCE VALUE RANGE Ω	RESISTANCE VALUES CURRENTLY AVAILABLE <sup>(1)</sup> PER ELEMENT Ω	WEIGHT (typical) g
WSBR8518	8518	36	5	30µ to 100µ	100µ	36

Note

<sup>(1)</sup> Other values may be available, contact factory

- -

TECHNICAL SPECIFICATIONS				
DADAMETED		RESISTOR CHARACTERISTICS		
PARAMETER	UNIT	WSBR8518		
Temperature coefficient	ppm/°C	± 10 for 100 μΩ		
Operating temperature range	°C	-65 to +170		
Thermal EMF	μV/°C	< 1.25		
Inductance	nH	< 5		
Maximum current rating	A	(P/R) <sup>1/2</sup>		

GLOBAL PART NUMBER INFORMATION						
GLOBAL PART NU	MBERING: WSBR8518L1	000JTA4 (WSBR8518	<b>A4, 0.0001</b> Ω)			
W S B R 8 5 1 8 L 1 0 0 0 J T A 4						
GLOBAL MODEL	RESISTANCE VALUE	TOLERANCE CODE	PACKAGING CODE	SPECIAL	PLATING OPTIONS	
WSBR8518	<b>L</b> = mΩ <b>L1000</b> = 0.0001 Ω	<b>J</b> = ± 5 %	K = bulk pack T = tray pack	Blank = no pins A4 = 4 pins B4 = 4 pins	Blank = unplated <b>P</b> = tin plated	
				<b>D4</b> = 4 pins		

PATENT(S): www.vishay.com/patents

This Vishay product is protected by one or more United States and international patents.



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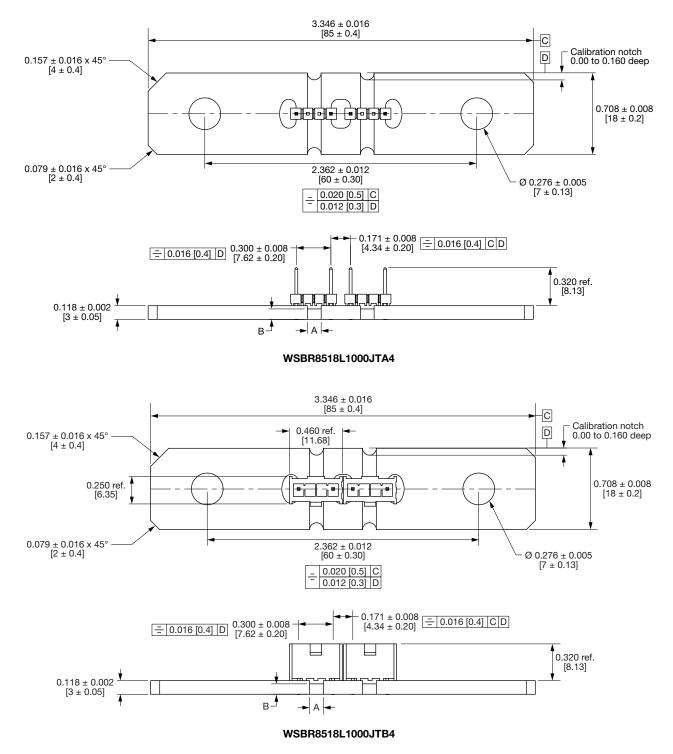
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WSBR Series

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### **DIMENSIONS** in inches (millimeters)

/ISHA\



2

For technical questions, contact: <u>ww2cresistors@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



# **WSBR Series**

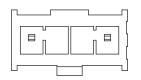
## Vishay Dale

## **CONNECTION OPTIONS**



Voltage sense pins in position 1 and 4, position 2 and 3 are blank.

#### A Series



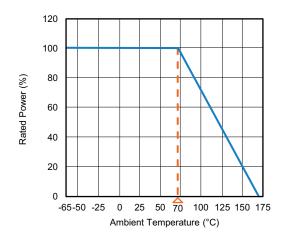
Voltage sense pins in position 1 and 4, position 2 and 3 are blank.

B Series

#### Note

- Connection options are examples. Other configurations available upon request (links to external website)
  - A series connector modified with the middle two pins removed
  - $\underline{\mathsf{B}\ \text{series\ connector}}$  modified with the middle two pins removed
  - B series female connector
  - Connector specifications datasheet

### DERATING



SIZE	RESISTANCE VALUE PER ELEMENT (μΩ)	ELEMENT MATERIAL	A REF.	B REF.
8518	100	NiCr	0.120 (3.05)	0.090 (2.29)

TOLERANCES ON DECIMALS .xxx ± 0.005 [.x ± 0.1]

UNLESS OTHERWISE LISTED

PERFORMANCE				
TEST	CONDITIONS OF TEST	TEST LIMITS		
Thermal shock	-55 °C to +150 °C, 1000 cycles, 15 min at each extreme	± 0.5 % ΔR		
Short time overload	5 x rated power for 5 s	± 0.5 % ΔR		
Low temperature storage	-65 °C for 24 h	± 0.2 % ΔR		
High temperature exposure	1000 h at +170 °C	± 1.0 % ΔR		
Bias humidity	+85 °C, 85 % RH, 10 % bias, 1000 h	± 0.5 % ΔR		
Mechanical shock	100 g's for 6 ms, 5 pulses	± 0.2 % ΔR		
Vibration	Frequency varied 10 Hz to 2000 Hz in 1 min, 3 directions, 12 h	± 0.2 % ΔR		
Load life	1000 h at +70 °C, 1.5 h "ON", 0.5 h "OFF"	± 1.0 % Δ <i>R</i>		
Moisture resistance	MIL-STD-202, method 106, 0 % power, 7b not required	± 0.2 % ΔR		



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