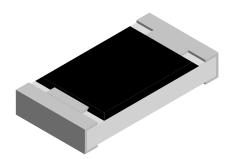


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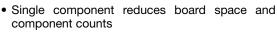
CRCC

Vishay Dale

Thick Film Resistor/Capacitor Chip, Surface Mount



FEATURES





- X7R dielectric characteristic
- · Wrap around termination
- · Thick film resistor/capacitor element
- Inner electrode protection
- Flow and reflow solderable
- · Automatic placement capability, standard size
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

STANDARD ELECTRICAL SPECIFICATIONS											
	SIZE	SIZE METRIC	RESISTOR CHARACTERISTICS			CAPACITOR CHARACTERISTICS				6	
GLOBAL MODEL			POWER RATING P ₇₀ °C W	TEMP. COEFF. ± ppm/°C	RESISTANCE TOLERANCE ± %		DIELECTRIC	TEMPERATURE COEFFICIENT %	CAP. TOL. ± %	CAP. VOLTAGE V _{DC}	CAP. RANGE
CRCC1206	1206	3216	0.125	200	5	10 to 1M	X7R	± 15	20	50	10 pF to 270 pF

Notes

RESISTOR

- Operating temperature range: -55 °C to +125 °C
- Technology: Thick film
- Packaging: See appropriate catalog or web page.

CAPACITOR

- Operating temperature range: X7R -55 °C to +125 °C
- Maximum dissipation factor: 2.5 %

TECHNICAL SPECIFICATIONS						
PARAMETER	UNIT	RESISTOR	X7R CAPACITOR			
Rated dissipation at 70 °C	W	0.125	-			
Capacitor voltage rating	V	-	50			
Dielectric withstanding voltage (5 s, 50 mA charge)	V _{DC}	-	125			
Category temperature range	°C	-55 / +125	-55 / +125			
Insulation resistance	Ω	> 10 ¹⁰	> 10 ¹⁰			
Weight/1000 pieces	g	0.65	2			

Power rating depends on the maximum temperature at the solder point, the component placement density and the substrate material.

GLOBAL PART NUMBER INFORMATION New Global Part Numbering: CRCC1206472J220MTF (preferred part numbering format) С С С 2 0 6 2 0 M F **GLOBAL MODEL** RESISTANCE VALUE RES. TOLERANCE CAPACITANCE VALUE (pF) CAP. TOLERANCE **PACKAGING CRCC1206** 2 digit significant figure, $F = \pm 1 \%$ 2 digit significant figure, $K = \pm 10 \%$ **EA** = Lead (Pb)-free, T/R (4000 pieces) followed by a multiplier followed by a multiplier $G = \pm 2 \%$ $M = \pm 20 \%$ TF = Tin/Lead, **100** = 10 Ω $J = \pm 5 \%$ 100 = 10 pF560 = 56 pFT/R (4000 pieces) 683 = 68 kO $\textbf{105} = 1.0 \ \text{M}\Omega$ 271 = 270 pF Historical Part Number Example: CRCC1206472J220MR02 (will continue to be accepted) **CRCC1206** 472 **R02** 220 M MODEL RESISTANCE VALUE RES. TOLERANCE CAPACITANCE VALUE CAP. TOLERANCE **PACKAGING**

Revision: 04-Nov-16

For additional information on packaging, refer to the Surface Mount Network Packaging document (www.vishay.com/doc?31540).

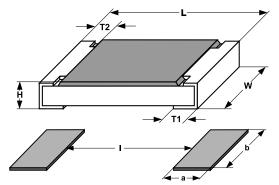


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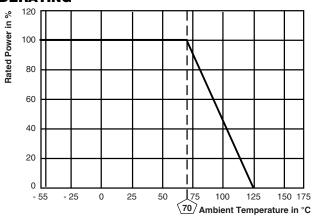
DIMENSIONS



S	IZE	DIMENSIONS in millimeters						
INCH	METRIC	L	W	Н	T1	T2		
1206	3216	3.2 ± 0.15	1.6 ± 0.15	0.55 ± 0.15	0.5 ± 0.25	0.5 ± 0.25		

Ī	c	IZE	SOLDER PAD DIMENSIONS in millimeters						
	3	Z E	REFLOW SOLDERING			WAVE SOLDERING			
Ī	INCH	METRIC	а	b	ı	а	b	I	
	1206	3216	0.9	1.7	2.0	1.1	1.7	2.2	

DERATING



SCHEMATIC



PERFORMANCE						
TEST	CONDITIONS OF TEST	TEST RESULTS (T	TEST RESULTS (TYPICAL TEST LOTS)			
1231	CONDITIONS OF TEST	R	С			
Endurance test at 70 °C MIL-Std-202 method 108	1000 h at 70 °C, 1.5 h "ON", 0.5 h "OFF"	± (5 % + 2 Ω)	± 20 %			
Dielectric withstanding voltage MIL-Std-202 method 301	125 V _{DC} , 5 s, 50 mA charge	No physic	al damage			
Thermal shock MIL-Std-202 method 107	100 cycles, -55 °C to +125 °C	± (5 % + 2 Ω)	± 20 %			
Moisture MIL-Std-202 method 106	Omit steps 7A and B	± (5 % + 2 Ω)	± 20 %			
Resistance to soldering heat EIA 575	10 s at 260 °C solder bath temperature	± (5 % + 2 Ω)	± 20 %			
High temperature exposure EIA 575	125 °C for 100 h	± (5 % + 2 Ω)	± 20 %			
Low temperature operation EIA 575	1 h at -55 °C then 45 min at 50 V	± (5 % + 2 Ω)	± 20 %			
Solderability and leaching EIA 575 3.12	Condition C	95 % c	overage			

APPLICABLE SPECIFICATIONS

- IPC standards
- EIA 575



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