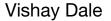
## LPE-6562





Surface-Mount Transformers / Inductors, Gapped and Ungapped, Custom Configurations Available



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### **FEATURES**

• Material categorization: for definitions of compliance please <u>www.vishay.com/doc?99912</u> see

**ELECTRICAL SPECIFICATIONS** 



#### RoHS COMPLIANT

(multiple winds are connected in parallel) **Inductance Range:** 10  $\mu$ H to 330 000  $\mu$ H, measured at 0.10 V<sub>RMS</sub> at 10 kHz without DC current, using an HP 4263A or HP 4284A impedance analyzer

**DC Resistance Range:** 0.03  $\Omega$  to 53.7  $\Omega$ , measured at +25 °C ± 5 °C

Rated Current Range: 3.00 A to 0.06 A Dielectric Withstanding Voltage: 500 V<sub>RMS</sub>, 60 Hz, 5 s

STANDARD ELECTRICAL SPECIFICATIONS								
MODEL	IND. (µH)	IND. TOL. (%)	SCHEMATIC LETTER	DCR MAX. (Ω)	MAX. RATED DC CURRENT (A) <sup>(1)</sup>	SATURATING CURRENT (A) <sup>(2)</sup>		
LPE6562ER221NU	220	± 30	A	0.28	0.90	N/A		
LPE6562ER331NU	330	± 30	Α	0.34	0.81	N/A		
LPE6562ER471NU	470	± 30	Α	0.40	0.74	N/A		
LPE6562ER681NU	680	± 30	A	0.48	0.67	N/A		
LPE6562ER102NU	1000	± 30	A	0.59	0.61	N/A		
LPE6562ER152NU	1500	± 30	А	0.72	0.55	N/A	₹	
LPE6562ER222NU	2200	± 30	А	0.87	0.50	N/A	Ś	
LPE6562ER332NU	3300	± 30	А	1.07	0.45	N/A	ᆸ	
LPE6562ER472NU	4700	± 30	А	1.27	0.41	N/A	2	
LPE6562ER682NU	6800	± 30	Α	1.53	0.38	N/A	<b>UNGAPPED MODELS</b>	
LPE6562ER103NU	10 000	± 30	А	1.86	0.34	N/A	Ω	
LPE6562ER153NU	15 000	± 30	Α	2.27	0.31	N/A	Ш	
LPE6562ER223NU	22 000	± 30	А	8.67	0.16	N/A	ΔP	
LPE6562ER333NU	33 000	± 30	А	10.6	0.14	N/A	g	
LPE6562ER473NU	47 000	± 30	А	12.7	0.13	N/A	5	
LPE6562ER683NU	68 000	± 30	А	15.2	0.12	N/A		
LPE6562ER104NU	100 000	± 30	А	18.5	0.11	N/A		
LPE6562ER154NU	150 000	± 30	А	37.7	0.08	N/A		
LPE6562ER224NU	220 000	± 30	Α	45.6	0.07	N/A		
LPE6562ER334NU	330 000	± 30	Α	53.7	0.06	N/A		
LPE6562ER100MG	10	± 20	В	0.03	3.09	5.055		
LPE6562ER150MG	15	± 20	В	0.04	2.79	4.160		
LPE6562ER220MG	22	± 20	В	0.05	2.26	3.460		
LPE6562ER330MG	33	± 20	В	0.08	1.81	2.840		
LPE6562ER470MG	47	± 20	D	0.12	1.48	2.390	B	
LPE6562ER680MG	68	± 20	С	0.19	1.20			
LPE6562ER101MG	100	± 20	D	0.29	0.98	1.650	MODELS	
LPE6562ER151MG	150	± 20	E	0.45	0.78	1.350	ō	
LPE6562ER221MG	220	± 20	E	0.54	0.71	1.115	ş	
LPE6562ER331MG	330	± 20	E	0.84	0.57	0.912	2	
LPE6562ER471MG	470	± 20	E	1.24	0.47	0.765	Ē	
LPE6562ER681MG	680	± 20	E	1.89	0.38	0.637	GAPPED	
LPE6562ER102MG	1000	± 20	E	2.91	0.31	0.526	3	
LPE6562ER152MG	1500	± 20	E	4.50	0.25	0.430		
LPE6562ER222MG	2200	± 20	E	6.90	0.20	0.355		
LPE6562ER332MG	3300	± 20	E	10.4	0.16	0.290		
LPE6562ER472MG	4700	± 20	E	15.7	0.13	0.243		

#### Notes

DC current that will create a maximum temperature rise of 30 °C when applied at +25 °C ambient.

(2)

DC current that will typically reduce the initial inductance by 20 %. UNGAPPED MODELS: Highest possible inductance with the lowest DCR and highest Q capability. Beneficial in filter, impedance matching and line coupling devices.

GAPPED MODELS: Capable of handling large amounts of DC current, tighter inductance tolerance with better temperature stability than ungapped models. Beneficial in DC/DC converters or other circuits carrying DC currents or requiring inductance stability over a temperature range.

Revision: 15-Apr-2025

1 For technical questions, contact: magnetics@vishay.com Document Number: 34066

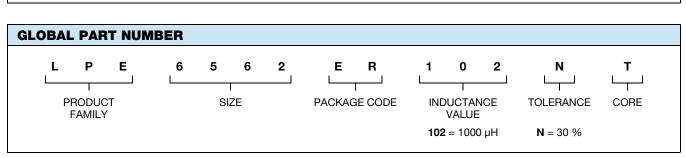
THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT

 LPE-6562

 www.vishay.com
 Vishay Dale

 DESCRIPTION

 LPE
 6562
 1000 µH
 ± 30 %
 A
 ER
 e2



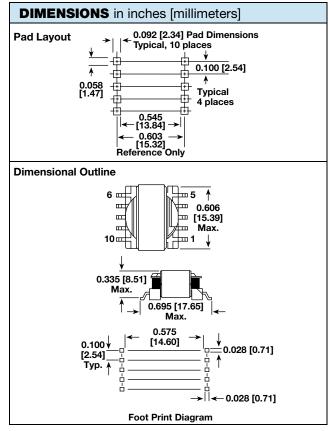
INDUCTANCE VALUE INDUCTANCE TOLERANCE CORE PACKAGE CODE JEDEC® LEAD (Pb)-FREE STANDARD

#### Note

MODEL

SIZE

· Series is also available with SnPb terminations by using package code RY for tape and reel (in place of ER) or SM for bulk (in place of EB).



#### Notes

- Pad layout guidelines per MIL-STD-275E (printed wiring for electronic equipment).
- Tolerances: xx ± 0.01" [± 0.25 mm]; xxx ± 0.005" [± 0.12 mm].
- The underside of these components contains metal and thus should not come in contact with active circuit traces.

SCHEMATIC (top view)							
Schematic A	Schen	natic B	Schematic C				
6 <b>0</b>	6 <b>0</b>	<b>0</b> 5	6 <b>O O</b> 5				
70	7 <b>0</b>	<u>مر مومع</u>	7 O O 4				
80	8 <b>0</b>	~~~ <u>°</u> @3	8 <b>00</b> 3				
90	9 <b>0</b>	~~~ <u>°</u> ₀₂	9 <b>00</b> 2				
100	10 <b>@</b>	~~~_°°1	10 <b>0</b> 1				
Schematic	D	Schematic E					
6 <b>O</b>	<b>9</b> 5	6 <b>O</b>	<b>0</b> 5				
7 🔨	<b>③</b> 4	7 <b>O</b>	<b>O</b> 4				
8 😳	<b>©</b> 3	8 <b>O</b>	<b>©</b> 3				
9 <b>0</b>	<b>∩</b> © 2	9 <b>0</b> —	<b>•••• ••</b> 2				
10 <b>@</b>	<b>○</b> <sup>©</sup> <sub>1</sub>	10 @	••••••••••••••••••••••••••••••••••••••				
Schematic           6 0           7 0           8 0           9 0	© 5 © 4 © 3	60 70 80 90	Chematic E © 5 © 4 © 3				

#### Note

• Schematic A is for ungapped LPE series.

ENVIRONMENTAL PERFORMANCE				
TEST	CONDITIONS			
Thermal cycling	Withstands -55 °C to +125 °C			
Operating temperature	-55 °C to +125 °C <sup>(1)</sup>			
High humidity	85 %			
Soldering heat	Tested to +230 °C			
Mechanical shock	Per MIL-STD-202, method 213 (100G)			
Vibration	Per MIL-STD-202, method 204 (20G)			
Solderability	Per industry standards			

#### Note

<sup>(1)</sup> Must be checked in end use application.

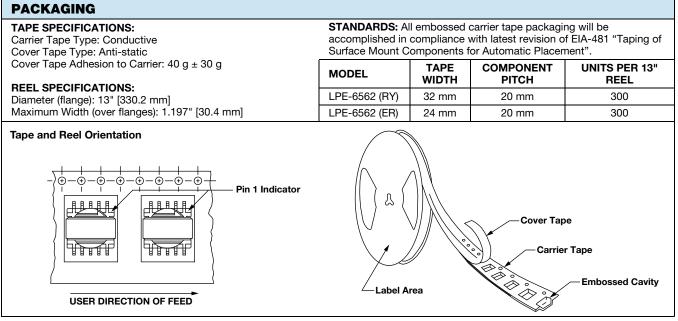
#### PART MARKING

- Vishay Dale
- Date code
- Marking code (suffix of model #)
- Pin 1 indicator

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SHAY

## Vishay Dale



Note

• Top view shown with cover tape removed.



Vishay

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