

Vishay Dale

# IHLP<sup>®</sup> Commercial Inductors, High Temperature (155 °C) Series



## LINKS TO ADDITIONAL RESOURCES



STANDARD ELECTRICAL SPECIFICATIONS						
L <sub>0</sub> INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A (μH)	DCR TYP. 25 °C (mΩ)	DCR MAX. 25 °C (mΩ)	HEAT RATING CURRENT DC TYP. (A) <sup>(1)</sup>	SATURATION CURRENT DC TYP. (A) <sup>(2)</sup>	SRF TYP. (MHz)	
0.22	3.95	4.23	18	11	190	
0.33	4.9	5.34	16	9.8	117	
0.47	6.58	7.04	14.2	7.8	92	
0.68	9.1	9.74	11.3	8.2	77.7	
1	11.5	12.1	10.8	8	60.7	
1.5	18	19.8	7.9	7.1	49.2	
2.2	24.7	26	6.6	6.3	39.8	
3.3	44.0	47.0	5.3	5.5	33.4	
4.7	72.8	78.3	4.1	3.7	23.8	
6.8	104	111	3.2	2.2	18.8	
10	132	138	2.8	1.6	15.9	
15	195	208	2.4	1.6	14.1	

#### Notes

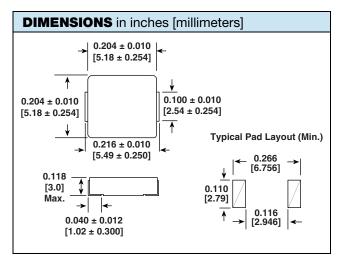
- All test data is referenced to 25 °C ambient
- Operating temperature range -55 °C to +155 °C
- The part temperature (ambient + temp. rise) should not exceed 155 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application
- Rated operating voltage (across inductor) = 50 V (1)
- DC current (A) that will cause an approximate  $\Delta T$  of 40 °C
- (2) DC current (A) that will cause L<sub>0</sub> to drop approximately 20 %

## **FEATURES**

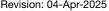
- High temperature, up to 155 °C
- Shielded construction
- Excellent DC/DC energy storage up to 1 MHz to 2 MHz. Filter inductor applications up the SRF (see Standard Electrical Specifications table).
- Lowest DCR/µH, in this package size
- Handles high transient current spikes without (5-2008) saturation
- Ultra low buzz noise, due to composite construction
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### APPLICATIONS

- PDA / notebook / desktop / server applications
- High current POL converters
- · Low profile, high current power supplies
- Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for field programmable gate array (FPGA)



IHLP-2020CZ-51	1.5 µH	± 20 %	EK	e3
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC <sup>®</sup> LEAD (Pb)-FREE STAND
GLOBAL PAR	TNUMBER			
PRODUCT FAI	P        2        0          MILY	2 0 C Z SIZE	E K 1 PACKAGE CODE	R 5   INDUCTANCE TOL.   VALUE
PACKAGE CO	DDE OPTIONS			
EK = tape and reel	packaging (3500 pcs or	13-inch reel)		
ER = tape and reel	packaging (2000 pcs or	13-inch reel)		
<b>-</b>	kaging details see Packa	ning Methods		



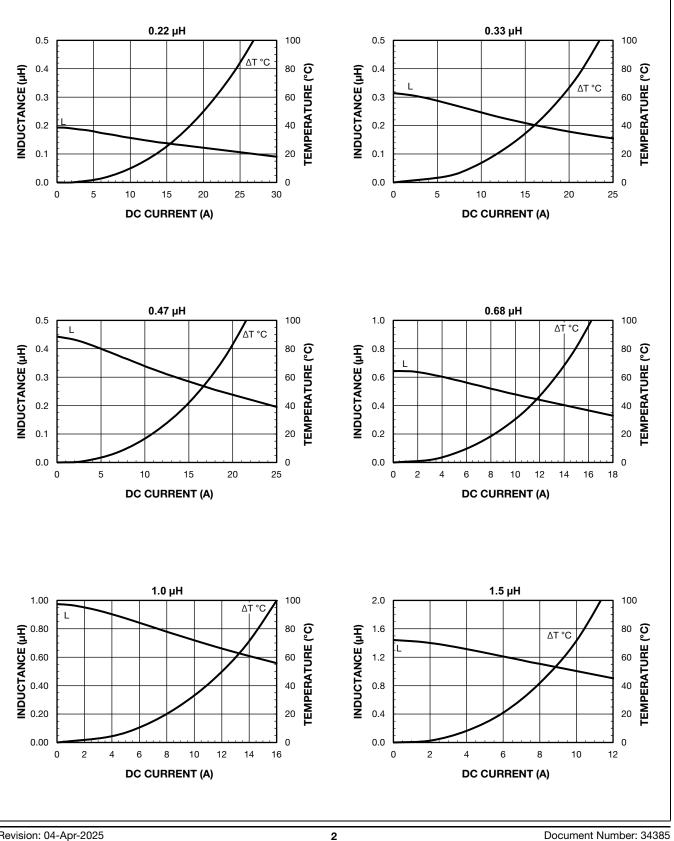
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RoHS COMPLIANT HALOGEN FREE GREEN



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### **PERFORMANCE GRAPHS**



Revision: 04-Apr-2025

For technical questions, contact: magnetics@vishay.com

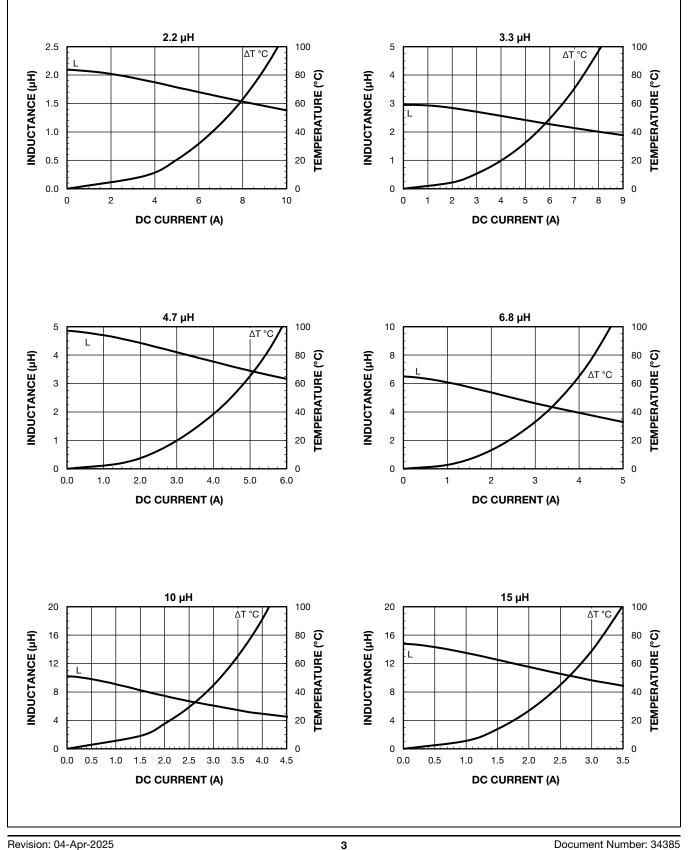
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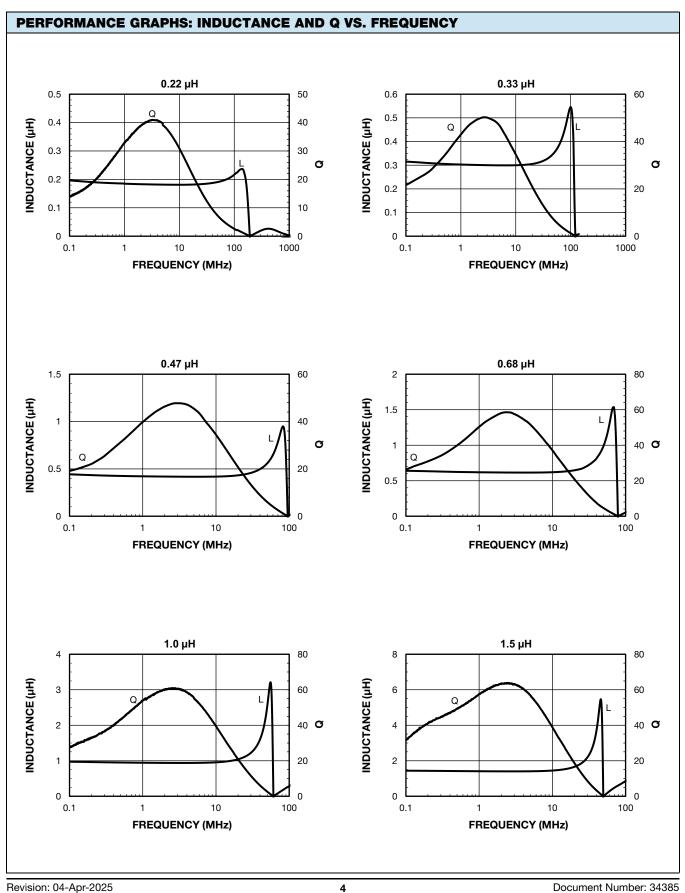
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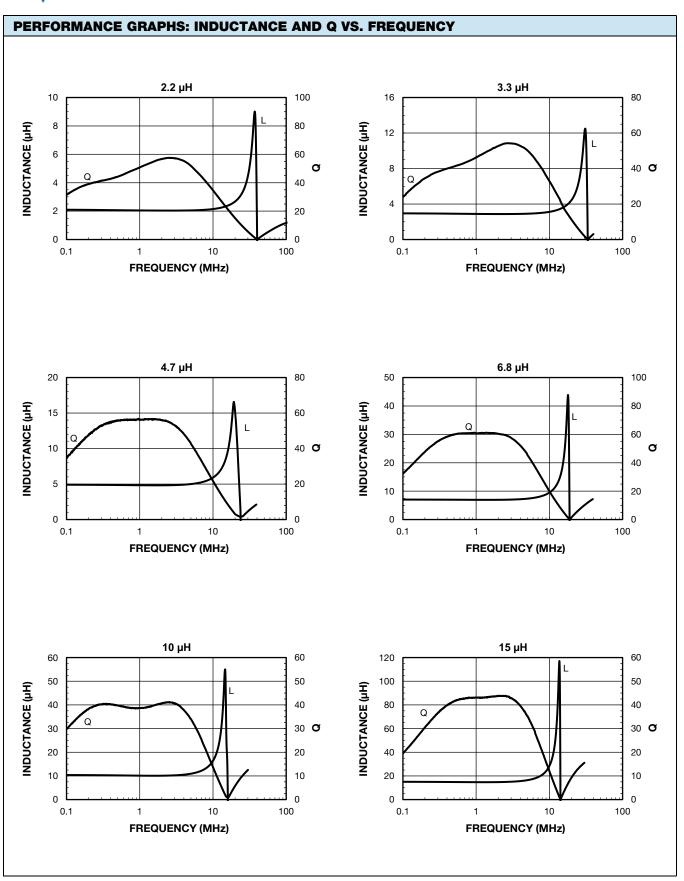
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