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Vishay Dale

COMPLIANT

HALOGEN

FREE GREEN

(5-2008)

Commercial Inductors, Ultra Low DCR, High Temperature (155 °C) Series





LINKS TO ADDITIONAL RESOURCES



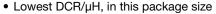
STANDARD ELECTRICAL SPECIFICATIONS									
L ₀ INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A	DCR ± 5 % AT 25 °C (mΩ)		HEAT RATING CURRENT DC (A) ⁽¹⁾	SATURATION CURRENT DC (A)		SRF TYP.			
(μH)	TYP.	MAX.	TYP.	TYP. (2)	TYP. (3)	(MHz)			
0.056	0.38	0.40	58	45	64	535			
0.100	0.97	1.02	46	31	46	186			

Notes

- All test data is referenced to 25 °C ambient
- Operating temperature range -55 °C to +155 °C
- The part temperature (ambient + temp. rise) should not exceed 155 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application
- ⁽¹⁾ DC current (A) that will cause an approximate ΔT of 40 °C
- (2) DC current (A) that will cause L₀ to drop approximately 20 %
- $^{(3)}$ DC current (A) that will cause L_0 to drop approximately 30 %

FEATURES

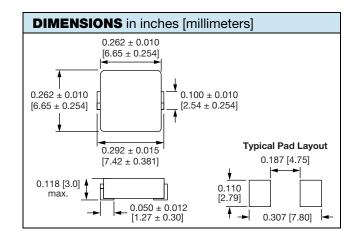
- High temperature rating, up to 155 °C
- Shielded construction
- Excellent DC/DC energy storage up to 10 MHz.
 Filter inductor applications up the SRF (see Standard Electrical Specifications table)



- Handles high transient current spikes without saturation
- Ultra low buzz noise, due to composite construction
- IHSR design; PATENT(S): www.vishay.com/patents
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Notebook / desktop / server applications
- · High current POL converters
- · Low profile, high current power supplies
- Multiphase DC/DC converter for server microprocessor power
- Low voltage input / output filters
- DC/DC converters in distributed power systems
- DC/DC converter for field programmable gate array (FPGA)

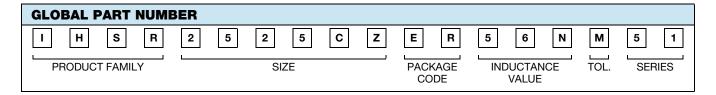


PATENT(S): www.vishay.com/patents

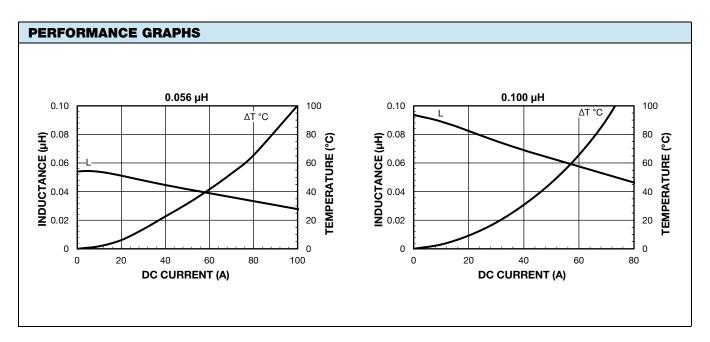
Revision: 16-May-2024

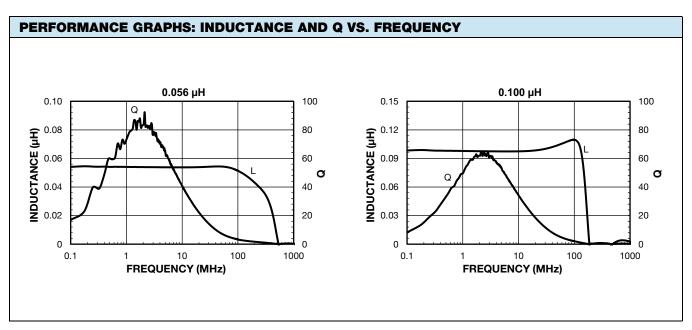
This Vishay product is protected by one or more United States and international patents.

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DESCRIPTION							
IHSR-2525CZ-51	0.056 μΗ	± 20 %	ER	e3			
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD			







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