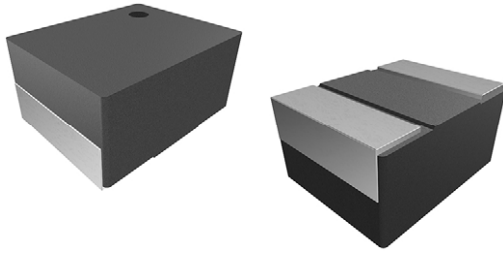


Low Profile, High Current Inductors



FEATURES

- Composite powdered iron construction
- Miniature size (2.0 x 1.6) and low profile
- Magnetic shielded
- Low DCR and core loss for improved efficiency
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


**RoHS
COMPLIANT**

APPLICATIONS

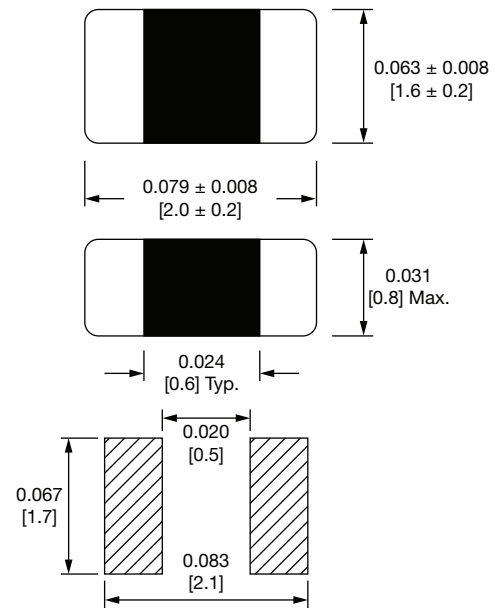
- Portable electronics
- Tablets and notebook computers
- POL DC/DC converters
- Battery powered devices
- Internet of things (IoT) devices

STANDARD ELECTRICAL SPECIFICATIONS						
L ₀ INDUCTANCE ± 20 % AT 1 MHz, 1.0 V, 0 A (μH)	DCR 25 °C (mΩ)		HEAT RATING CURRENT DC (A) ⁽³⁾		SATURATION CURRENT DC (A) ⁽⁴⁾	
	TYP.	MAX.	TYP.	MAX.	TYP.	MAX.
0.47	20	24	4.9	4.5	5.2	4.7
1.0	29	35	4.4	4.0	4.0	3.6
2.2	75	90	2.6	2.3	2.9	2.7

Notes

- (1) All test data is referenced to 25 °C ambient
- (2) Operating temperature range -55 °C to +125 °C
- (3) DC current (A) that will cause an approximate ΔT of 40 °C
- (4) DC current (A) that will cause L₀ to drop approximately 30 %
- (5) The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application

DIMENSIONS in inches [millimeters]



DESCRIPTION

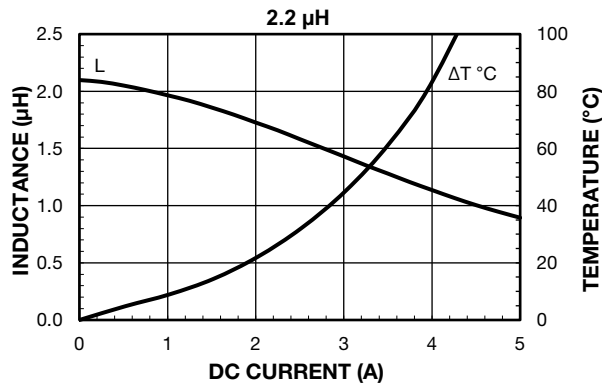
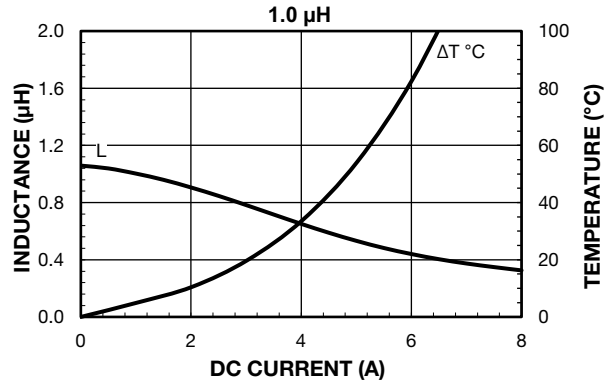
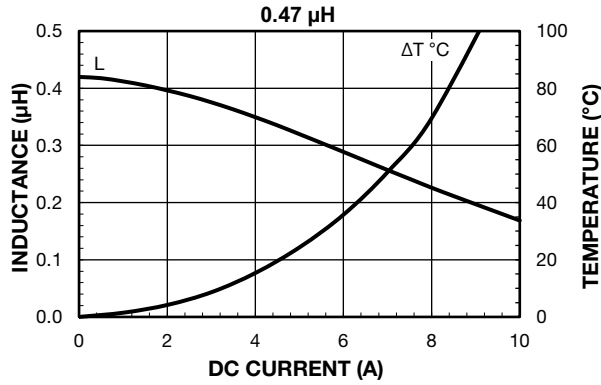
IHHP-0806ZH-01	1.0 μH	± 20 %	ER	e3
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD

GLOBAL PART NUMBER

I	H	H	P	0	8	0	6	Z	H	E	R	1	R	0	M	0	1
PRODUCT FAMILY				SIZE						PACKAGE CODE		INDUCTANCE VALUE		TOL.	SERIES		

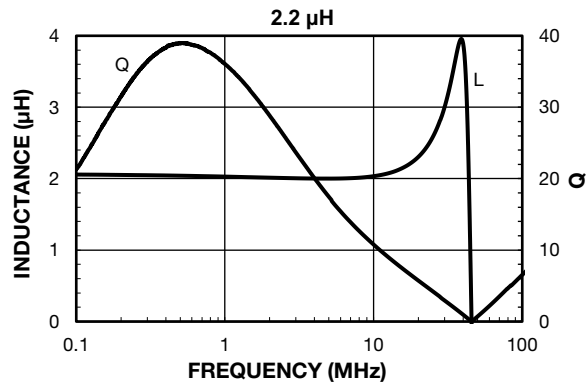
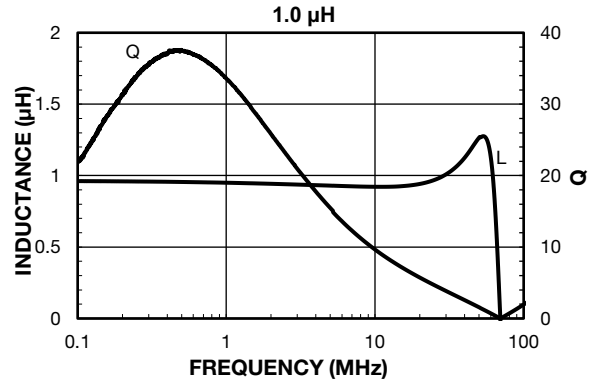
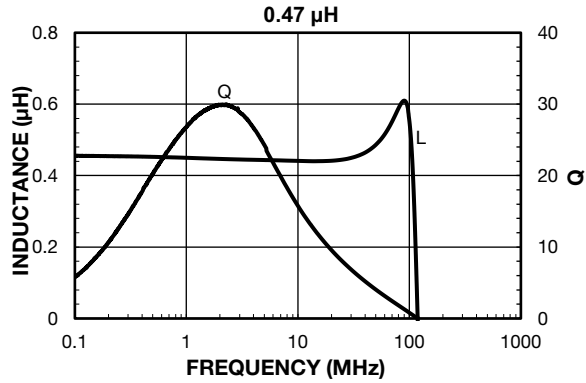


PERFORMANCE GRAPHS





PERFORMANCE GRAPHS: INDUCTANCE AND Q VS. FREQUENCY





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